TSF70R750S1

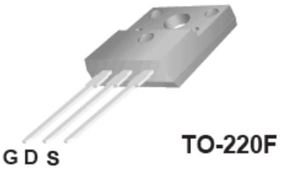
700V 7A N-Channel SJ-MOSFET

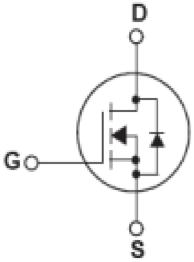
General Description

Truesemi SJ-FET is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance.
This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy.
SJ-FET is suitable for various AC/DC power conversion in switching mode operation for higher efficiency.

Features

- 750V @TJ = 150 ℃
- Typ. RDS(on) = 0.68Ω
- Ultra Low gate charge (typ. Qg = 25nC)
- 100% avalanche tested





Absolute Maximum Ratings

Symbol	Parameter	Value	Unit
V_{DSS}	Drain-Source Voltage	700	V
I_D	Drain Current -Continuous (TC = 25℃) -Continuous (TC = 100℃)	7* 4.4*	A
I_{DM}	Drain Current – Pulsed (Note 1)	21	A
V_{GSS}	Gate-Source voltage	±30	V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	86	mJ
I_{AR}	Avalanche Current (Note 1)	1.7	A
E_{AR}	Repetitive Avalanche Energy (Note 1)	0.2	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	15	V/ns
P_D	Power Dissipation (TC = 25℃)	28	W
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	℃
T_L	Maximum Lead Temperature for Soldering Purpose,1/8” from Case for 5 Seconds	300	℃

* Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	**Parameter**	Value	**Unit**
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	4.5	℃/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ.	--	℃/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	80	℃/W

Electrical Characteristics TC = 25℃ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS} = 0V, I_D = 250μA, T_J = 25℃	700	--	--	V
		V_{GS} = 0V, I_D = 250μA, T_J = 150℃	--	750	--	V
ΔBV_{DSS} / Δ_{TJ}	Breakdown Voltage Temperature Coefficient	I_D = 250μA, Referenced to 25℃	--	0.6	--	V/℃
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 700V, V_{GS} = 0V -T_J = 150℃	--	-- 10	1 --	μA μA
I_{GSSF}	Gate-Body Leakage Current, Forward	V_{GS} = 30V, V_{DS} = 0V	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	V_{GS} = -30V, V_{DS} = 0V	--	--	-100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	2.5	3.5	4.5	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	V_{GS} = 10V, I_D = 3.5A	--	0.68	0.75	Ω
g_{FS}	Forward Trans conductance	V_{DS} = 40V, I_D = 7A (Note 4)	--	6	--	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz	--	380	--	pF
C_{oss}	Output Capacitance		--	110	--	pF
C_{rss}	Reverse Transfer Capacitance		--	7	--	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{DD} = 400V, I_D = 3.5A R_G = 20Ω(Note 4, 5)	--	13	--	ns
t_r	Turn-On Rise Time		--	10	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	85	--	ns
t_f	Turn-Off Fall Time		--	14	--	ns
Q_g	Total Gate Charge	V_{DS} = 480V, I_D = 3.5A V_{GS} = 10V (Note 4, 5)	--	25	--	nC
Q_{gs}	Gate-Source Charge		--	2.0	--	nC
Q_{gd}	Gate-Drain Charge		--	2.7	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain-Source Diode Forward Current		--	--	7	A
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	21	A
V_{SD}	Drain-Source Diode Forward Voltage	V_{GS} = 0V, I_F = 3.5A	--	0.9	1.5	V
t_{rr}	Reverse Recovery Time	V_{GS} = 0V, I_F = 3.5A di_F/dt =100A/μs (Note 4)	--	190	--	ns
Q_{rr}	Reverse Recovery Charge		--	2.3	--	μC

NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. I_{AS}=1.7A, V_{DD}=50V, Starting TJ=25 ℃
3. I_{SD}≤7A, di/dt ≤ 200A/us, V_{DD} ≤ BV_{DSS}, Starting TJ = 25 ℃
4. Pulse Test: Pulse width ≤ 300us, Duty Cycle ≤ 2%
5. Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

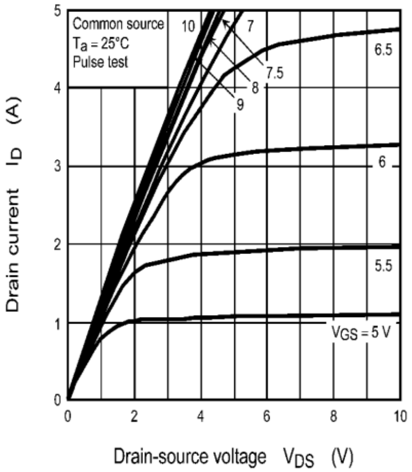


Figure 1: On-Region Characteristics@25°C

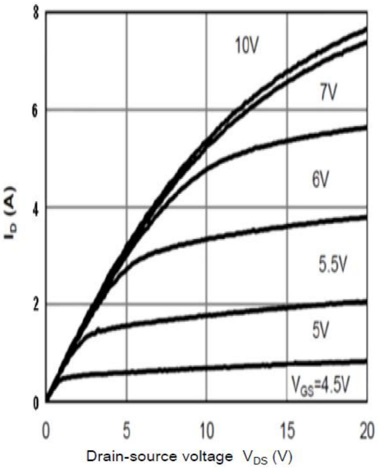


Figure 2: On-Region Characteristics@125°C

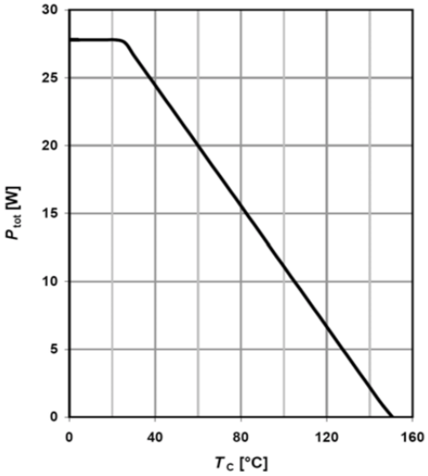


Figure 3:Power Dissipation

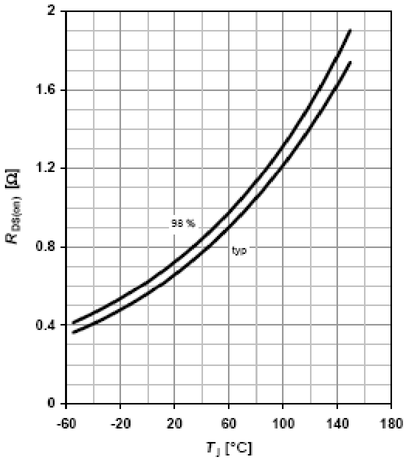


Figure 4: On-Resistance vs. Junction Temperature

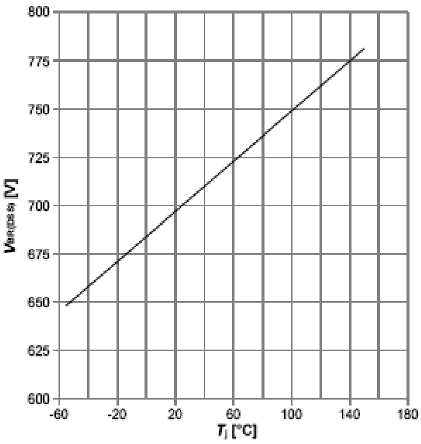


Figure 5: Break Down vs. Junction Temperature

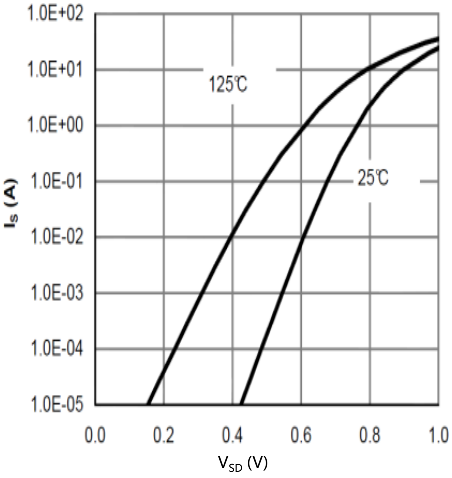


Figure 6: Body-Diode Characteristics

Typical Performance Characteristics

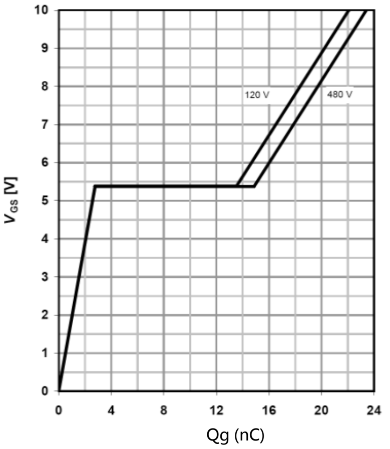


Figure 7: Gate-Charge Characteristics

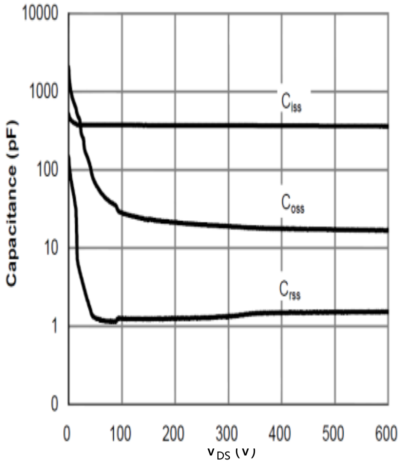


Figure 8: Capacitance Characteristics

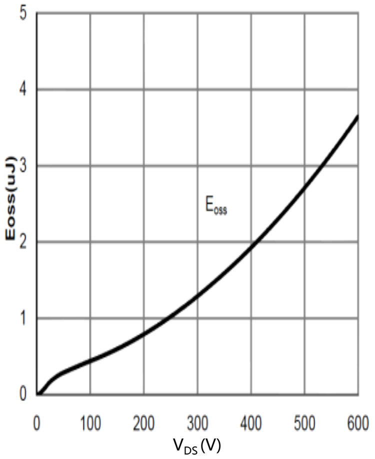


Figure 9: C_{oss} stored Energy

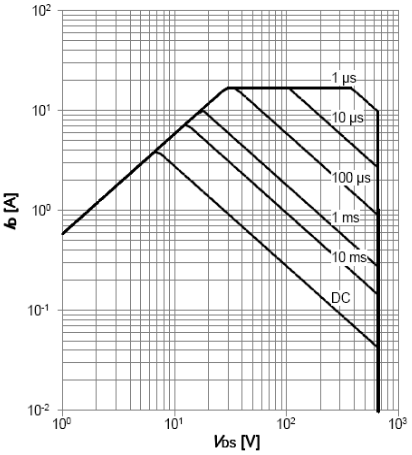


Figure 10: Maximum Forward Biased Safe Operating Area Tc=25 (°C),

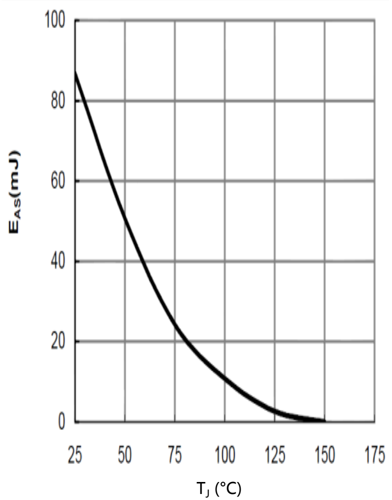


Figure 11: Avalanche energy

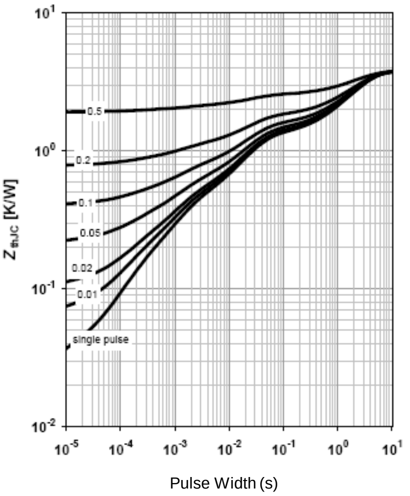


Figure 12: Maximum Transient Thermal Impedance

Typical Performance Characteristics

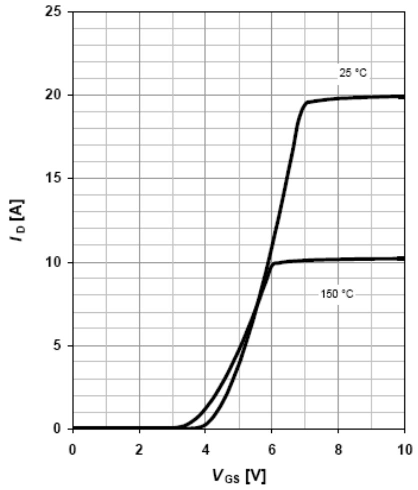


Figure 13: Typ. transfer characteristics

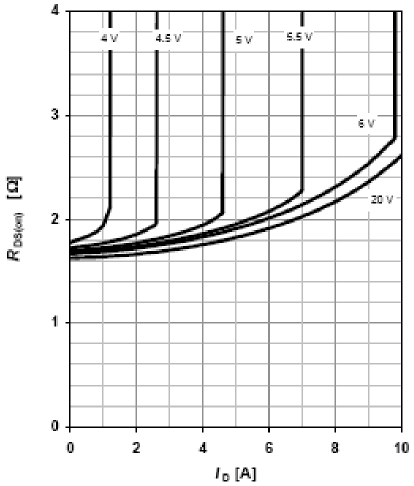
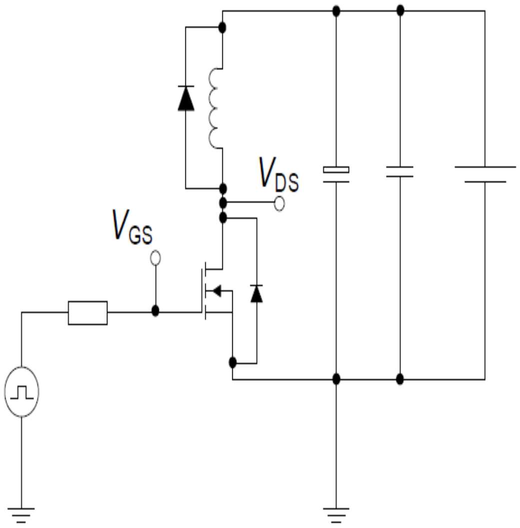


Figure 14 On-Resistance vs. Drain Current

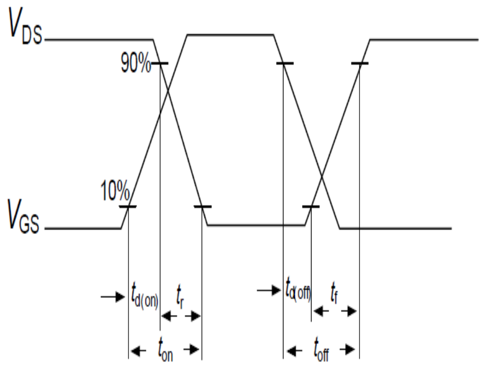
Test circuits

Switching times test circuit and waveform for inductive load

Switching times test circuit for inductive load

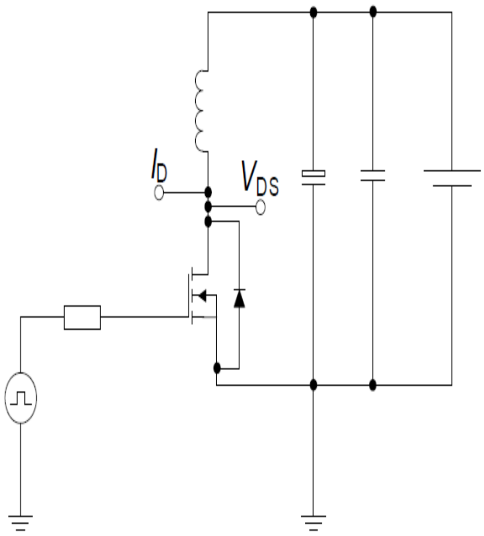


Switching time waveform

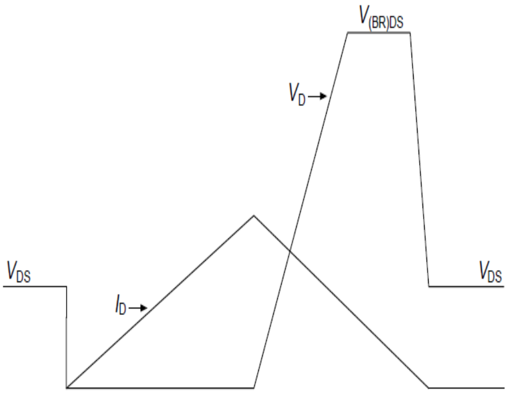


Unclamped inductive load test circuit and waveform

Unclamped inductive load test circuit



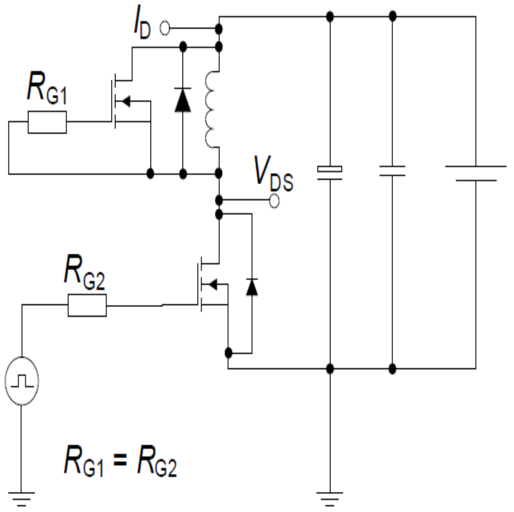
Unclamped inductive waveform



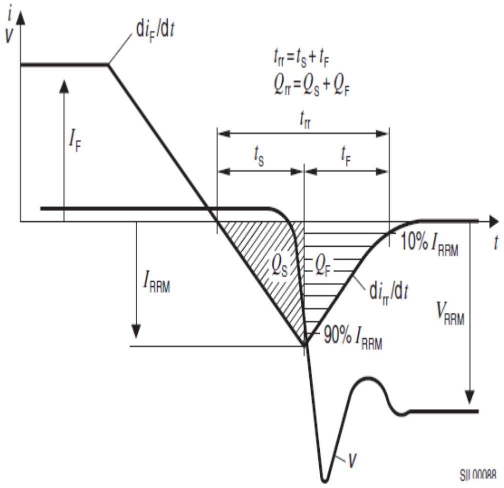
Test circuits

Test circuit and waveform for diode characteristics

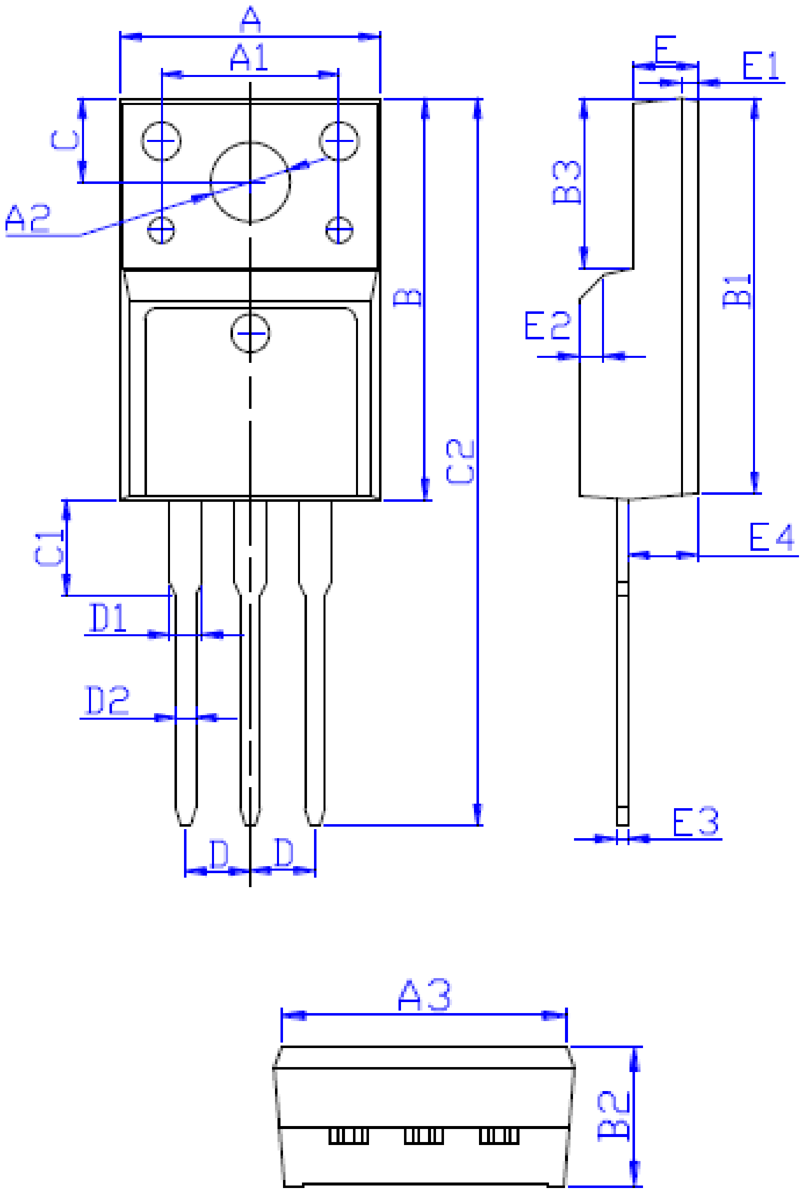
Test circuit for diode characteristics



Diode recovery waveform



Package Outline TO-220F



DIM	MILLIMETERS
A	10.16±0.30
A1	7.00±0.20
A2	3.12±0.20
A3	9.70±0.30
B	15.90±0.50
B1	15.60±0.50
B2	4.70±0.30
B3	6.70±0.30
C	3.30±0.25
C1	3.25±0.30
C2	28.70±0.50
D	Typical 2.54
D1	1.47 (MAX)
D2	0.80±0.20
E	2.55±0.25
E1	0.70±0.25
E2	1.0×45°
E3	0.50±0.20
E4	2.75±0.30