

## N-Channel Power MOSFET

500V, 16A, 0.35Ω

### FEATURES

- 100% UIS and R<sub>g</sub> tested
- Advanced planar process
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

KEY PERFORMANCE PARAMETERS		
PARAMETER	VALUE	UNIT
V <sub>DS</sub>	500	V
R <sub>DS(on)</sub> (max)	0.35	Ω
Q <sub>g</sub>	53	nC

### APPLICATIONS

- Power Supply
- AC/DC LED Lighting



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V <sub>DS</sub>	500	V
Gate-Source Voltage	V <sub>GS</sub>	±30	V
Continuous Drain Current (Note 1)	I <sub>D</sub>	T <sub>C</sub> = 25°C	16
		T <sub>C</sub> = 100°C	10
Pulsed Drain Current (Note 2)	I <sub>DM</sub>	64	A
Total Power Dissipation @ T <sub>C</sub> = 25°C	P <sub>DTOT</sub>	59.5	W
Single Pulse Avalanche Energy (Note 3)	E <sub>AS</sub>	608	mJ
Single Pulse Avalanche Current (Note 3)	I <sub>AS</sub>	7.8	A
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	- 55 to +150	°C

THERMAL PERFORMANCE			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction to Case Thermal Resistance	R <sub>θJC</sub>	2.1	°C/W
Junction to Ambient Thermal Resistance	R <sub>θJA</sub>	62	°C/W

**Thermal Performance Notes:** R<sub>θJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistances. The case-thermal reference is defined at the solder mounting surface of the drain pins. R<sub>θJC</sub> is guaranteed by design while R<sub>θCA</sub> is determined by the user's board design.

<b>ELECTRICAL SPECIFICATIONS</b> ( $T_A = 25^\circ\text{C}$ unless otherwise noted)						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	$BV_{DSS}$	500	--	--	V
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	$V_{GS(TH)}$	2.5	4	4.5	V
Gate Body Leakage	$V_{GS} = \pm 30\text{V}, V_{DS} = 0\text{V}$	$I_{GSS}$	--	--	$\pm 100$	nA
Zero Gate Voltage Drain Current	$V_{DS} = 500\text{V}, V_{GS} = 0\text{V}$	$I_{DSS}$	--	--	1	$\mu\text{A}$
Drain-Source On-State Resistance (Note 4)	$V_{GS} = 10\text{V}, I_D = 4\text{A}$	$R_{DS(on)}$	--	0.3	0.35	$\Omega$
<b>Dynamic</b> (Note 5)						
Total Gate Charge	$V_{DS} = 400\text{V}, I_D = 8\text{A},$ $V_{GS} = 10\text{V}$	$Q_g$	--	53	--	nC
Gate-Source Charge		$Q_{gs}$	--	15.4	--	
Gate-Drain Charge		$Q_{gd}$	--	18.5	--	
Input Capacitance	$V_{DS} = 50\text{V}, V_{GS} = 0\text{V},$ $f = 1.0\text{MHz}$	$C_{iss}$	--	2551	--	pF
Output Capacitance		$C_{oss}$	--	153	--	
Reverse Transfer Capacitance		$C_{rss}$	--	15	--	
Gate Resistance		$R_g$	--	1.5	3	$\Omega$
<b>Switching</b> (Note 6)						
Turn-On Delay Time	$V_{DD} = 250\text{V}, R_G = 5\Omega,$ $I_D = 8\text{A}, V_{GS} = 10\text{V}$	$t_{d(on)}$	--	17	--	ns
Turn-On Rise Time		$t_r$	--	27	--	
Turn-Off Delay Time		$t_{d(off)}$	--	40	--	
Turn-Off Fall Time		$t_f$	--	24	--	
<b>Source-Drain Diode</b>						
Body-Diode Continuous Forward Current		$I_S$	--	--	16	A
Body-Diode Pulsed Current		$I_{SM}$	--	--	64	A
Forward On Voltage (Note 4)	$I_S = 8\text{A}, V_{GS} = 0\text{V}$	$V_{SD}$	--	--	1.2	V
Reverse Recovery Time	$I_S = 8\text{A}$	$t_{rr}$	--	280	--	ns
Reverse Recovery Charge		$di_f/dt = 100\text{A}/\mu\text{s}$	$Q_{rr}$	--	3.4	--

**Notes:**

- Current limited by package.
- Pulse width limited by the maximum junction temperature.
- $L = 20\text{mH}, I_{AS} = 7.8\text{A}, V_{DD} = 50\text{V}, R_G = 25\Omega$ , Starting  $T_J = 25^\circ\text{C}$
- Pulse test:  $PW \leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- For DESIGN AID ONLY, not subject to production testing.
- Switching time is essentially independent of operating temperature.

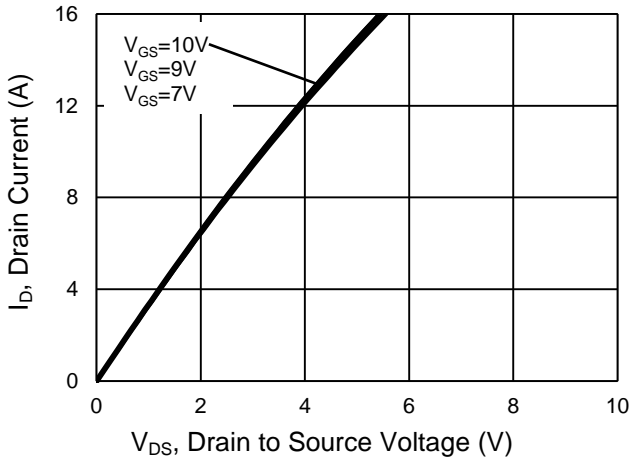
**ORDERING INFORMATION**

ORDERING CODE	PACKAGE	PACKING
TSM16ND50CI C0G	ITO-220	50pcs / Tube

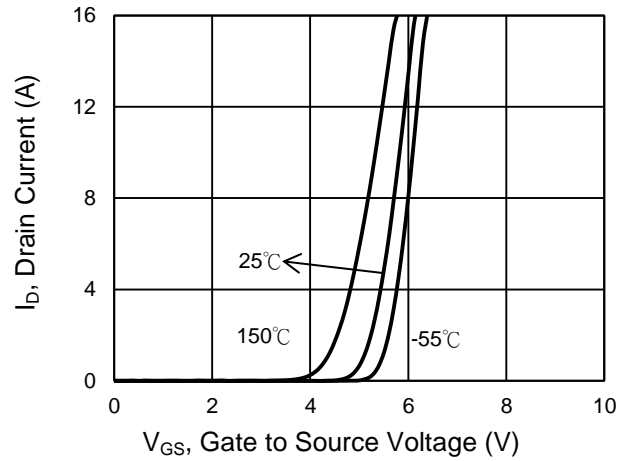
**CHARACTERISTICS CURVES**

( $T_C = 25^\circ\text{C}$  unless otherwise noted)

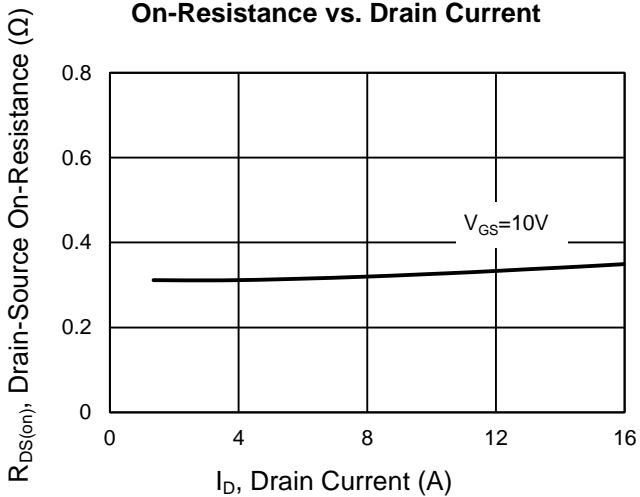
**Output Characteristics**



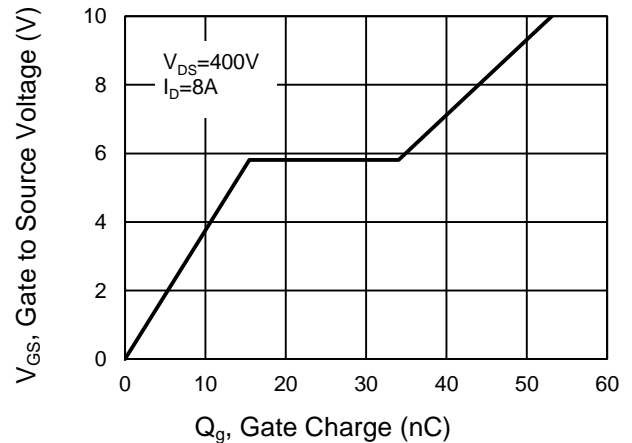
**Transfer Characteristics**



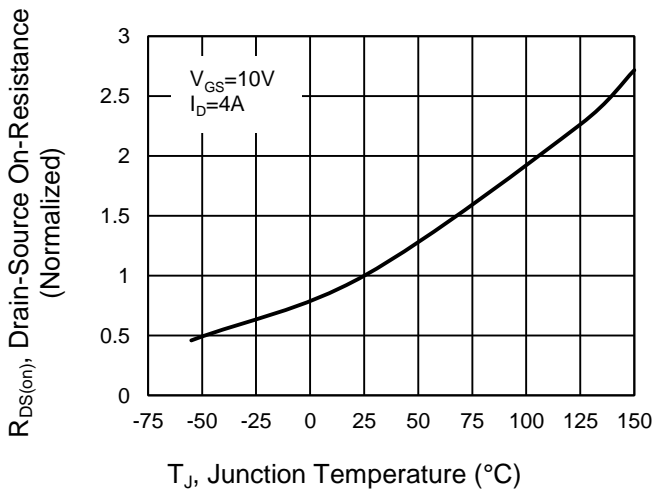
**On-Resistance vs. Drain Current**



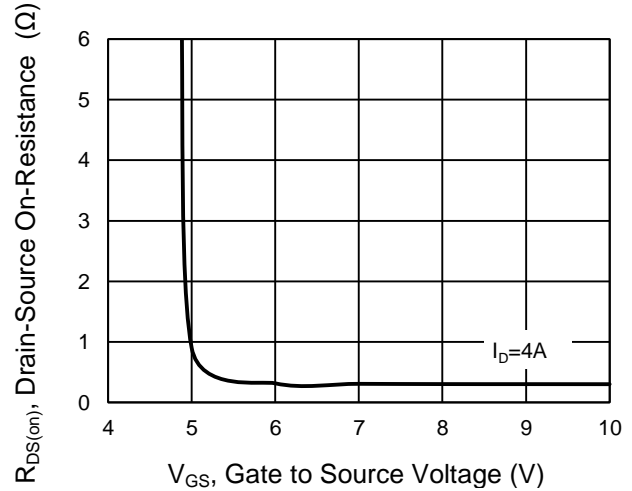
**Gate-Source Voltage vs. Gate Charge**



**On-Resistance vs. Junction Temperature**



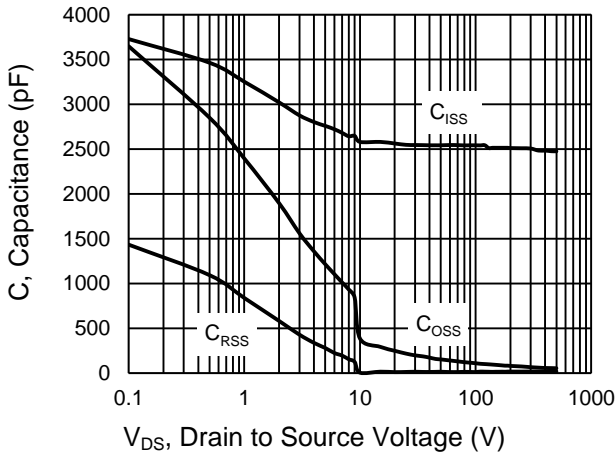
**On-Resistance vs. Gate-Source Voltage**



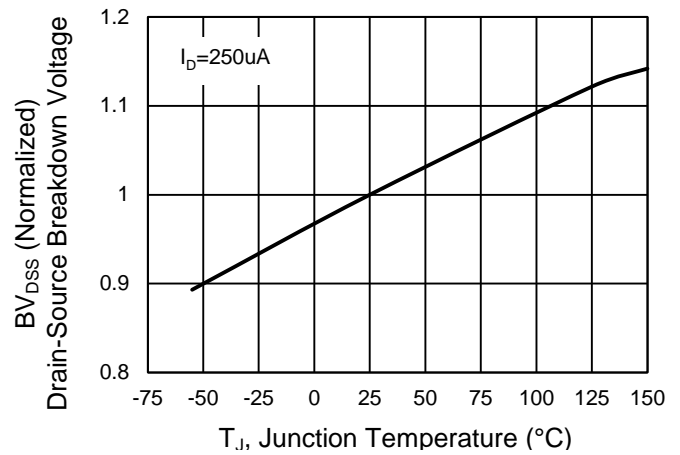
**CHARACTERISTICS CURVES**

( $T_C = 25^\circ\text{C}$  unless otherwise noted)

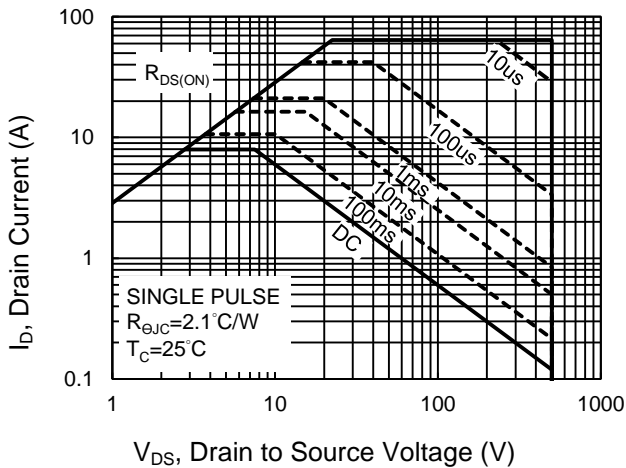
**Capacitance vs. Drain-Source Voltage**



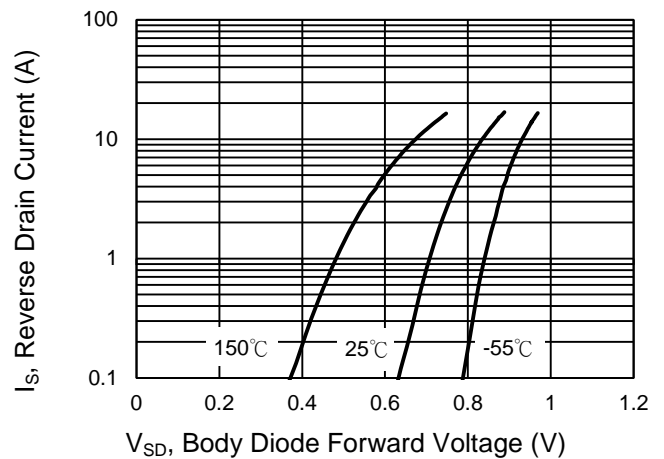
**$BV_{DSS}$  vs. Junction Temperature**



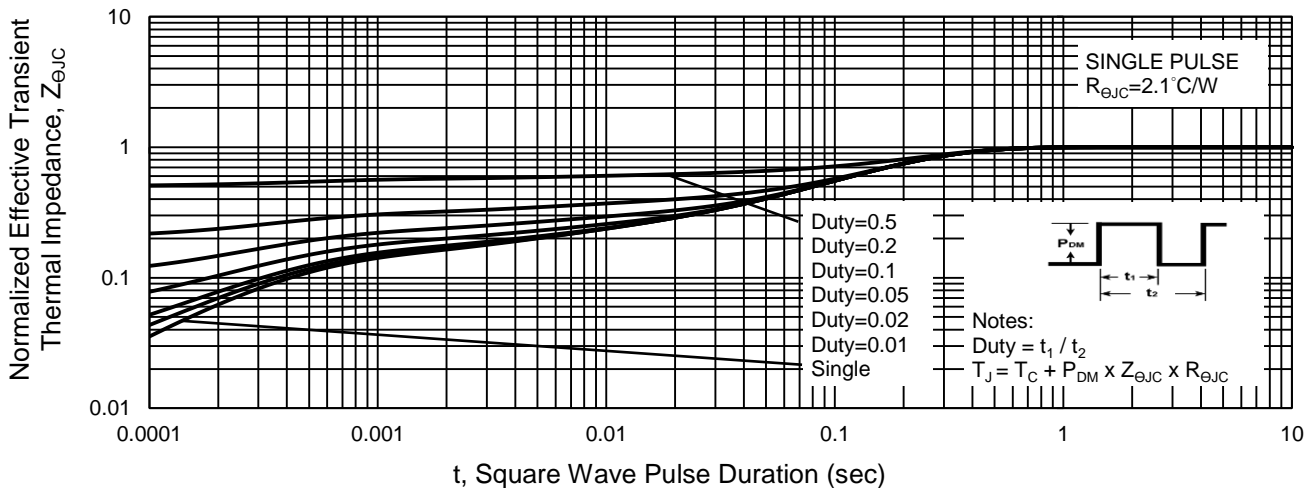
**Maximum Safe Operating Area, Junction-to-Case**



**Source-Drain Diode Forward Current vs. Voltage**

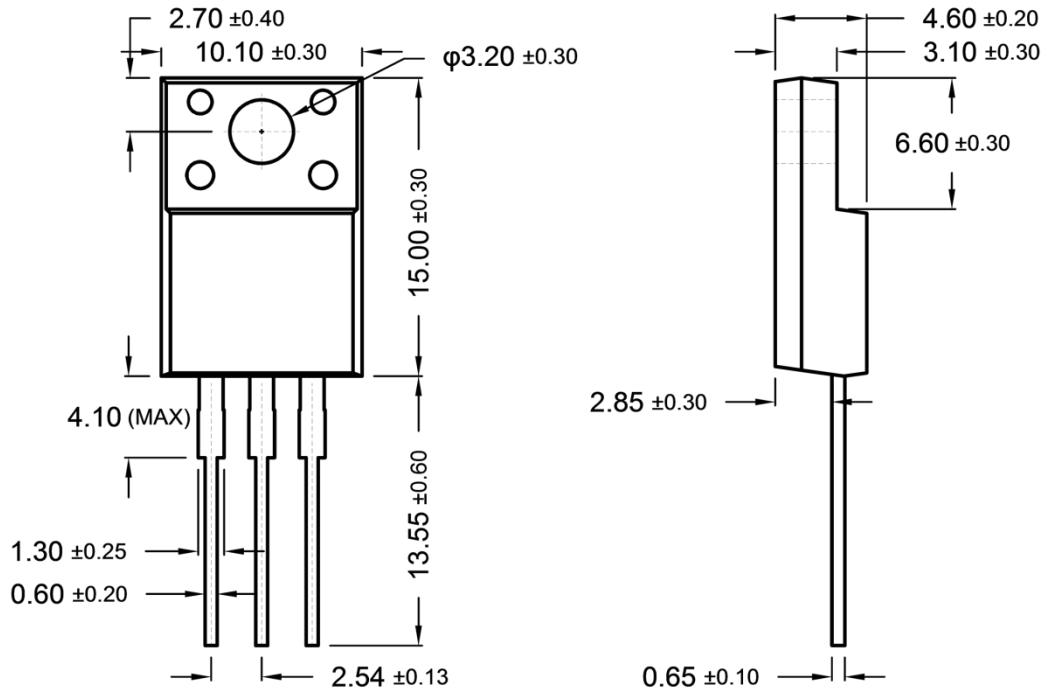


**Normalized Thermal Transient Impedance, Junction-to-Case**

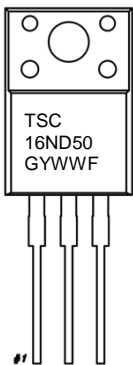


**PACKAGE OUTLINE DIMENSIONS** (Unit: Millimeters)

**ITO-220**



**MARKING DIAGRAM**



- G** = Halogen Free
- Y** = Year Code
- WW** = Week Code (01~52)
- F** = Factory Code

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