HITACHI

KAOHSIUNG HITACHI ELECTRONICS CO., LTD.

FOR MESSRS:_____ DATE: Oct. 02th 2008

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

TX20D19VM2BPA

Contents

ACCEPTED BY:_____ PROPOSED BY: Dan Cheng

2. RECORD OF REVISION

DATE	SHEET No.	SUMMARY

3. GENERAL DATA

3.1 DISPLAY FEATURES

This module is a 8" WVGA of 16:9 format amorphous silicon TFT.The pixel format is vertical stripe and sub pixels are arranged as R(red), G(green), B(blue) sequentially. This display is RoHS compliant, and COG (chip on glass) technology and LED backlight are applied on this display.

Part Name	TX20D19VM2BPA
Module Dimensions	189.0(W) mm x 120.0(H) mm x 9.0(D) mm typ.
LCD Active Area	174.0(W) mm x 104.4(H) mm
Dot Pitch	0.0725(W) mm x 3(R, G, B)(W) x 0.2175(H) mm
Resolution	800 x 3(RGB)(W) x 480(H) dots
Color Pixel Arrangement	R, G, B Vertical stripe
LCD Type	Transmissive Color TFT; Normally White
Display Type	Active Matrix
Number of Colors	262k Colors
Backlight	15 LEDs parallel x 3 serial (45 LEDs in total)
Weight	(240) g (typ.)
Interface	C-MOS; 18-bit RGB; 40 pins
Power Supply Voltage	3.3V for LCD; 12V for Backlight
Power Consumption	990 mW for LCD; 2.16W for backlight
Viewing Direction	12 O'clock (The direction without image inversion and least brightness change)
Touch Panel	Resistive type; Film on Glass; 4-wire type; Antiglare surface

4. ABSOLUTE MAXIMUM RATINGS

Item		Symbol	Min.	Max.	Unit	Remarks
Supply Voltage		VDD	0	4.0	V	-
Input Voltage of Logic		VI	-0.3	VDD+0.3	V	Note 1
Operating Temperature		Top	-20	70	°C	Note 2
Storage Temperature		Tst	-30	80	°C	Note 2
LED Unit	Forward Current	IF	-	35	mA	Note 3
	Reverse Voltage	VR	-	5	V	

Note 1: The rating is defined for the signal voltages of the interface such as DTMG, DCLK and RGB data bus.

Note 2: The maximum rating is defined as above based on the temperature on the panel surface, which might be different from ambient temperature after assembling the panel into the application. Moreover, some temperature-related phenomenon as below needed to be noticed:

- Background color, contrast and response time would be different in temperatures other than 25°C.
- Operating under high temperature will shorten LED lifetime.

Note 3: Fig. 4.1 shows the maximum rating of LED forward current against temperature. The backlight unit in this display has been set to 12 mA per LED. This is within the range when operating the display between -20~70°C.

For the dimming function, reducing the LED backlight voltage 12V to the expected brightness is recommeded. Aleernatively, using PWM signal with 10KHz and 12V amplitude to dim by adjusting the duty ratio.

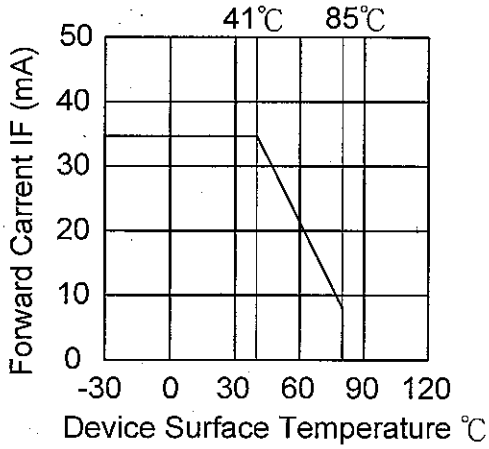


Fig. 4.1

5. ELECTRICAL CHARACTERISTICS

5.1 LCD CHARACTERISTICS

$T_a = 25\ ^\circ C,\ \mathrm{VSS} = 0\mathrm{V}$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
Power Supply Voltage	VDD	-	3.0	3.3	3.6	V	-
Input Voltage of Logic	VI	"H" level	0.8VDD	-	VDD	V	Note 1
		"L" level	VSS	-	0.2VDD		
Power Supply Current	IDD	-	-	300	400	mA	Note 2
Vsync Frequency	f_v	-	-	60	75	Hz	-
Hsync Frequency	f_H	-	-	31.6	39.2	KHz	-
DCLK Frequency	f_{CLK}	-	-	33.3	40	MHz	-

Note 1: The rating is defined for the signal voltages of the interface such as DTMG, DCLK and RGB data bus.

Note 2: An all black check pattern is used when measuring IDD, f_v is set to 60 Hz.

5.2 BACKLIGHT CHARACTERISTICS

$T_a = 25\ ^\circ C$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
LED Input Voltage	VLED	Backlight Unit	(11.5)	(12.0)	(12.5)	V	Note1
LED Forward Current	ILED	Backlight Unit	-	180	185	mA	-
LED Lifetime	-	180 mA	-	40K	-	hrs	Note 2

Note 1: Fig. 5.1 shows the LED backlight circuit. The circuit has 45 LEDs in total and R is 249 Ω.

Note 2: The estimated lifetime is specified as the time to reduce 50% brightness by applying 180 mA at 25°C.

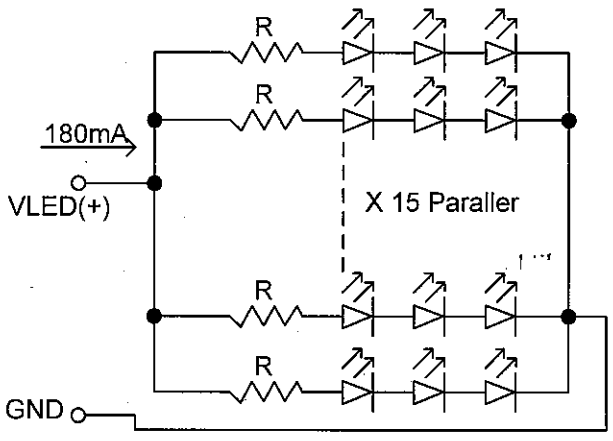


Fig. 5.1

6. OPTICAL CHARACTERISTICS

The optical characteristics are measured based on the conditions as below:

- Supplying the signals and voltages defined in the section of electrical characteristics.
- The backlight unit needs to be turned on for 30 minutes.
- The ambient temperature is 25°C.
- In the dark room around 500~1000 lx, the equipment has been set for the measurements as shown in Fig 6.1.

$T_a = 25\ ^{\circ}C$, $f_v = 60$ Hz, VDD = 3.3V

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
Brightness of White		-	$\phi=0^{\circ},\theta=0^{\circ}$, ILED= 180 mA	240	320	-	cd/m^2	Note 1
Brightness Uniformity		-		70	-	-	%	Note 2
Contrast Ratio		CR		100	200	-	-	Note 3
Response Time		Rise(Ton)	$\phi=0^{\circ},\theta=0^{\circ}$	-	20	40	ms	Note 4
		Fall(Toff)		-	10	20		
NTSC Ratio		-	$\phi=0^{\circ},\theta=0^{\circ}$	-	(60)	-	%	-
Viewing Angle		θ x	$\phi=0^{\circ}$, CR ≥ 10	50	60	-	Degree	Note 5
		θ x′	$\phi=180^{\circ}$, CR ≥ 10	50	60	-		
		θ y	$\phi=90^{\circ}$, CR ≥ 10	50	55	-		
		θ y′	$\phi=270^{\circ}$, CR ≥ 10	50	70	-		
Color Chromaticity	Red	X	$\phi=0^{\circ},\theta=0^{\circ}$	0.57	0.62	0.67	-	Note 6
		Y		0.31	0.36	0.41		
	Green	X		0.30	0.35	0.40		
		Y		0.56	0.61	0.66		
	Blue	X		0.10	0.15	0.20		
		Y		0.03	0.08	0.13		
	White	X		0.29	0.34	0.39		
		Y		0.31	0.36	0.41		

Note 1: The brightness is measured from the center point of the panel, P5 in Fig. 6.2, for the typical value.

Note 2: The brightness uniformity is calculated by the equation as below:

$$\text{Brightness uniformity} = \frac{\text{Min. Brightness}}{\text{Max. Brightness}} \text{ X100\%}$$

, which is based on the brightness values of the 9 points measured by BM-5 as shown in Fig. 6.2.

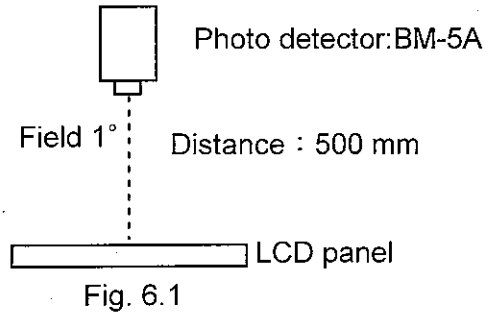


Fig. 6.1

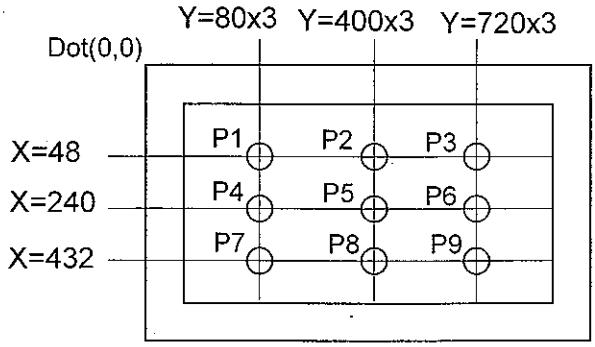


Fig. 6.2

Note 3: The Contrast ratio is measured from the center point of the panel, P5, and defined as the following equation:

$$CR = \frac{\text{Brightness of White}}{\text{Brightness of Black}} X100\%$$

Note 4: The definition of response time is shown in Fig. 6.3. The rising time is the period from 90% brightness to 10% brightness when the data is from white to black. Oppositely, Falling time is the period from 10% brightness rising to 90% brightness.

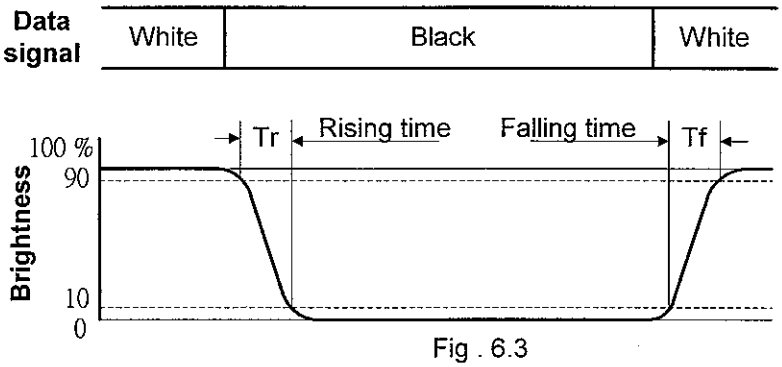


Fig . 6.3

Note 5: The definition of viewing angle is shown in Fig. 6.4. Angle ϕ is used to represent viewing directions, for instance, $\phi = 270°$ means 6 o'clock, and $\phi = 0°$ means 3 o'clock. Moreover, angle θ is used to represent viewing angles from axis Z toward plane XY.

The viewing direction of this display is 12 o'clock, which means that a photograph with gray scale would not be reversed in color and the brightness change would be less from this direction. However, the best contrast peak would be located at 6 o'clock.

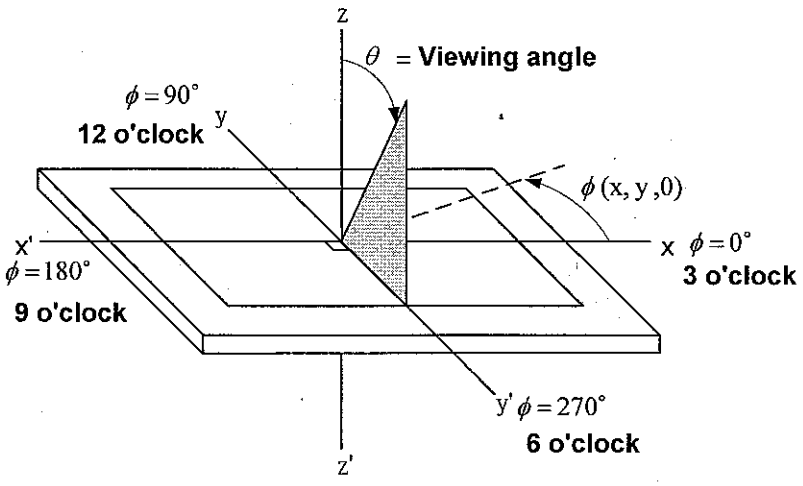
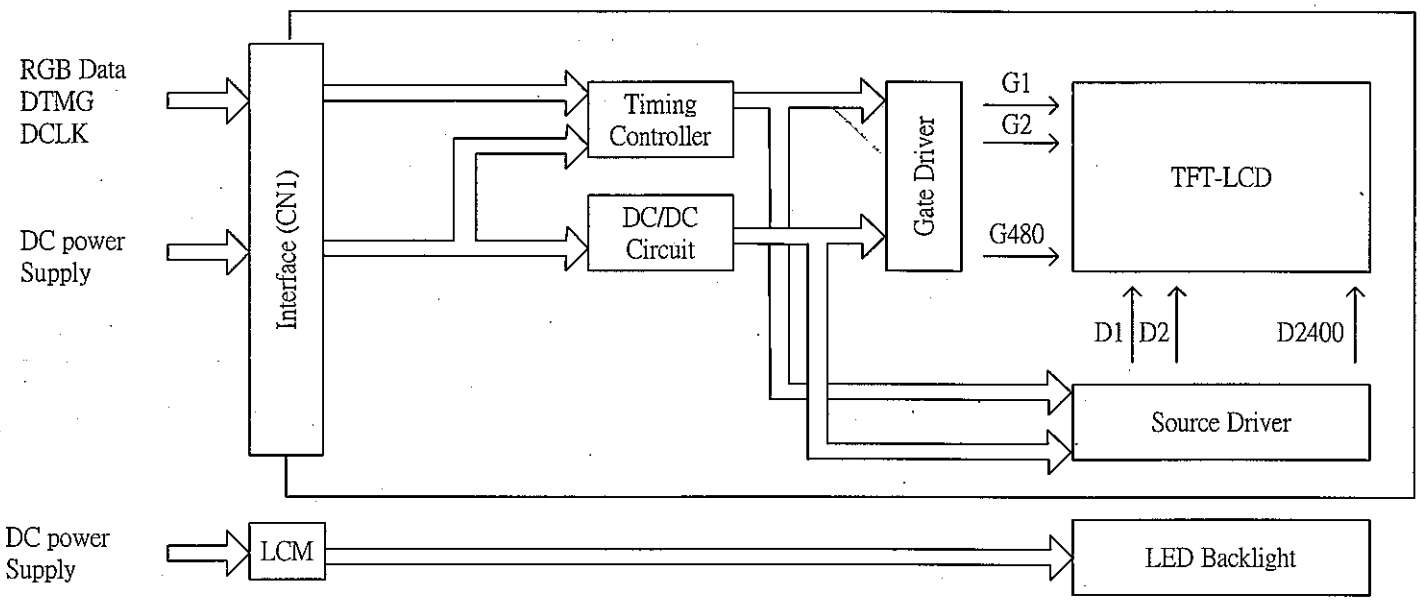


Fig. 6.4

Note 6: The color chromaticity is measured from the center point of the panel, P5, as shown in Fig. 6.2.

7 BLOCK DIAGRAM



8. RELIABILITY TESTS

Test Item	Condition	
High Temperature	1) Operating 2) 70°C	240 hrs
Low Temperature	1) Operating 2) -20°C	240 hrs
High Temperature	1) Storage 2) 80°C	240 hrs
Low Temperature	1) Storage 2) -30°C	240 hrs
Heat Cycle	1) Operating 2) –20°C ~70°C 3) 3hrs~1hr~3hrs	240 hrs
Thermal Shock	1) Non-Operating 2) -35°C ↔ 85°C 3) 0.5 hr ↔ 0.5 hr	240 hrs
High Temperature & Humidity	1) Operating 2) 40°C & 85%RH 3) Without condensation 4) Note 3	240 hrs
Vibration	1) Non-Operating 2) 20~200 Hz 3) 2G 4) X, Y, and Z directions	1 hr for each direction
Mechanical Shock	1) Non-Operating 2) 10 ms 3) 50G 4) ±X, ± Y and ±Z directions	Once for each direction
ESD	1) Operating 2) Tip: 200 pF, 250 Ω 3) Air discharge for glass: ± 8KV 4) Contact discharge for metal frame: ± 8KV 5) Contact discharge for LCD interface: ± 100V	1) Glass: 9 points 2) Metal frame: 8 points 3) Connector: all pins

Note 1: Display functionalities are inspected under the conditions defined in the specification after the reliability tests.

Note 2: The display is not guaranteed for use in corrosive gas environments.

Note 3: Under the condition of high temperature & humidity, if the temperature is higher than 40℃, the humidity needs to be reduced as Fig. 7.1 shown.

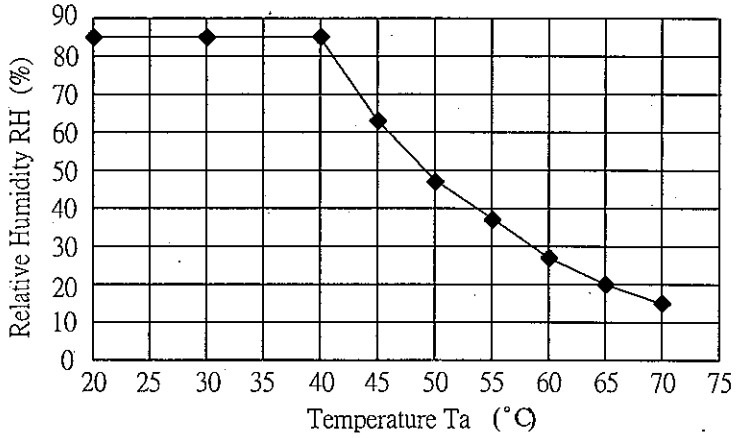


Fig. 7.1

9. LCD INTERFACE

9.1 INTERFACE PIN CONNECTIONS

The display interface connector is FA5B040HP1R3000 made by JAE (Thickness: 0.3±0.05mm; Pitch: 0.5±0.05mm) and more details of the connector are shown in the section of outline dimension.

Pin assignment of LCD interface is as below:

Pin No.	Signal	Function	Pin No.	Signal	Function
1	VDD	Power Supply for Logic	21	G4	Green Data
2	VDD		22	G3	
3	VDD		23	VSS	GND
4	VDD		24	G2	Green Data
5	NC	No Connection	25	G1	
6	DTMG	Timing Signal for Data	26	G0	
7	VSS	GND	27	VSS	GND
8	NC	No Connection	28	R5	Red Data
9	VSS	GND	29	R4	
10	(IC)	Note 1	30	R3	
11	VSS	GND	31	VSS	GND
12	B5	Blue Data	32	R2	Red Data
13	B4		33	R1	
14	B3		34	R0	
15	VSS	GND	35	(IC)	Note 1
16	B2	Blue Data	36	VSS	GND
17	B1		37		
18	B0		38	DCLK	Dot Clock
19	VSS	GND	39	VSS	GND
20	G5	Green Data	40		

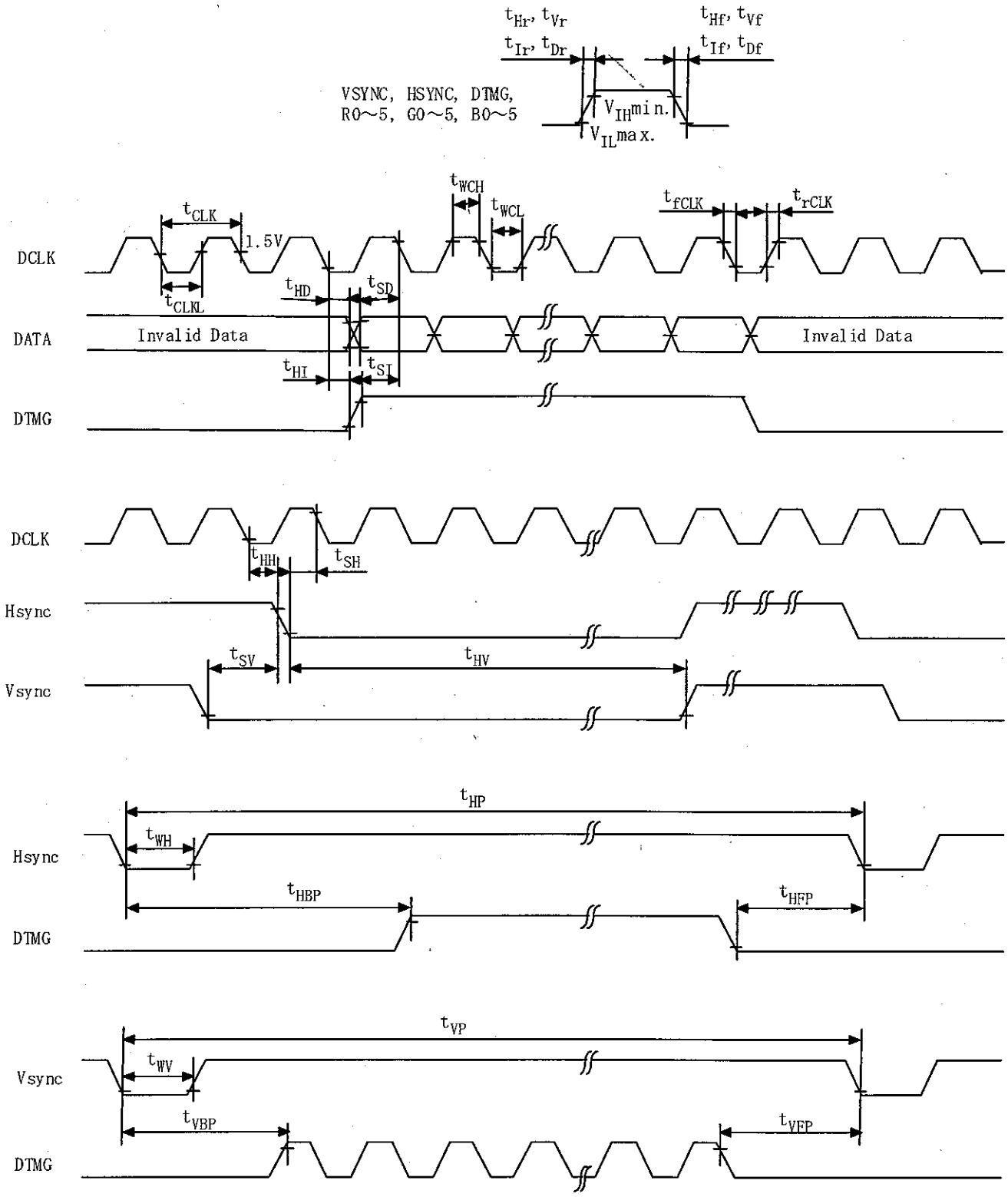
Note 1: Keep open electrically , Hitachi test use only.

The backlight interface connector is BHR-03VS-1 made by JST, and pin assignment of backlight is as below:

Pin No.	Signal	Level	Function
1	V_{LED}+	-	Power Supply for LED
2	NC	-	No connection
3	V_{LED}-	-	GND

9.2 TIMING CHART

DTMG (Data Enable) is the signal to determine valid data, and the timing of DTMG can be determined from Hsync and Vsync as below. For this display, only DTMG and DCLK are the essential signals. Hsync and Vsync are not necessary to connect to display interface after DTMG has been generated and input.



Data is latched by falling edge triggered DCLK

9.3 INTERFACE TIMING SPECIFICATIONS

Item		Symbol	Min.	Typ.	Max.	Unit
DCLK	Cycle time	t_{CLK}	25	30	33	ns
	Low level Width	t_{WCL}	12	-	-	
	High level Width	t_{WCH}	12	-	-	
	Rise time	t_{rCLK}	-	-	25	
	Fall time	t_{fCLK}	-	-	25	
	Duty	D	0.45	0.5	0.55	-
Hsync	Set up time	t_{SH}	5	-	-	ns
	Hold time	t_{HH}	10	-	-	
	Cycle	t_{HP}	944	1056	1088	tCLK
	Valid width	t_{WH}	4	128	-	
	Rise/Fall time	t_{Hr},t_{Hf}	-	-	30	ns
Vsync	Set up	t_{SV}	0	-	-	tCLK
	Hold	t_{HV}	2	-	-	
	Cycle	t_{VP}	515	525	610	tHP
	Valid width	t_{WV}	1	2	-	
	Rise/Fall time	t_{Vr},t_{Vf}	-	-	50	ns
DTMG	Set up time	t_{SI}	5	-	-	ns
	Hold time	t_{HI}	10	-	-	
	Rise/Fall time	t_{Ir},t_{If}	-	-	30	ns
	Horizontal back porch	t_{HBP}	7	216	-	tCLK
	Horizontal front porch	t_{HFP}	-	40	-	
	Vertical back porch	t_{VBP}	4	34	-	tHP
	Vertical front porch	t_{VFP}	-	11	-	
Data	Set up time	t_{SD}	5	-	-	ns
	Hold time	t_{HD}	10	-	-	
	Rise/Fall time	t_{Dr},t_{Df}	-	-	25	ns

Note 1: Vsync needs to be set as odd numbers.

9.4 POWER SEQUENCE

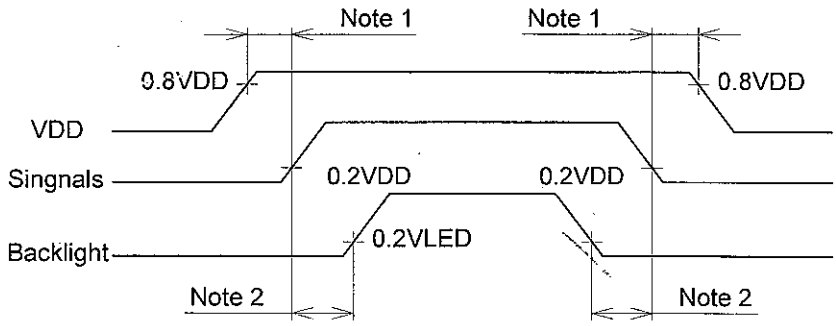


Fig. 9.1

Note 1: In order to avoid any damages, the correct power On sequence must be followed and VDD have to be applied before all other signals (DTMG, DCLK, RGB data). The opposite is true for power Off where VDD have to be remained on until all other signals have been switch off. The recommended time period is 1 second.

Note 2: In order to avoid showing uncompleted patterns in transient state. It is recommended that switching the backlight on is delayed for 1 second after the signals have been applied. The opposite is true for power Off where the backlight have to be switched off 1 second before the signals are removed.

9.5 DATA INPUT for DISPLAY COLOR

	COLOR & Gray Scale	Data Signal																	
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green (0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue (0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (62)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red (61)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
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	Red (1)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (62)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green (61)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
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	Green (1)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green (0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue (62)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue (61)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
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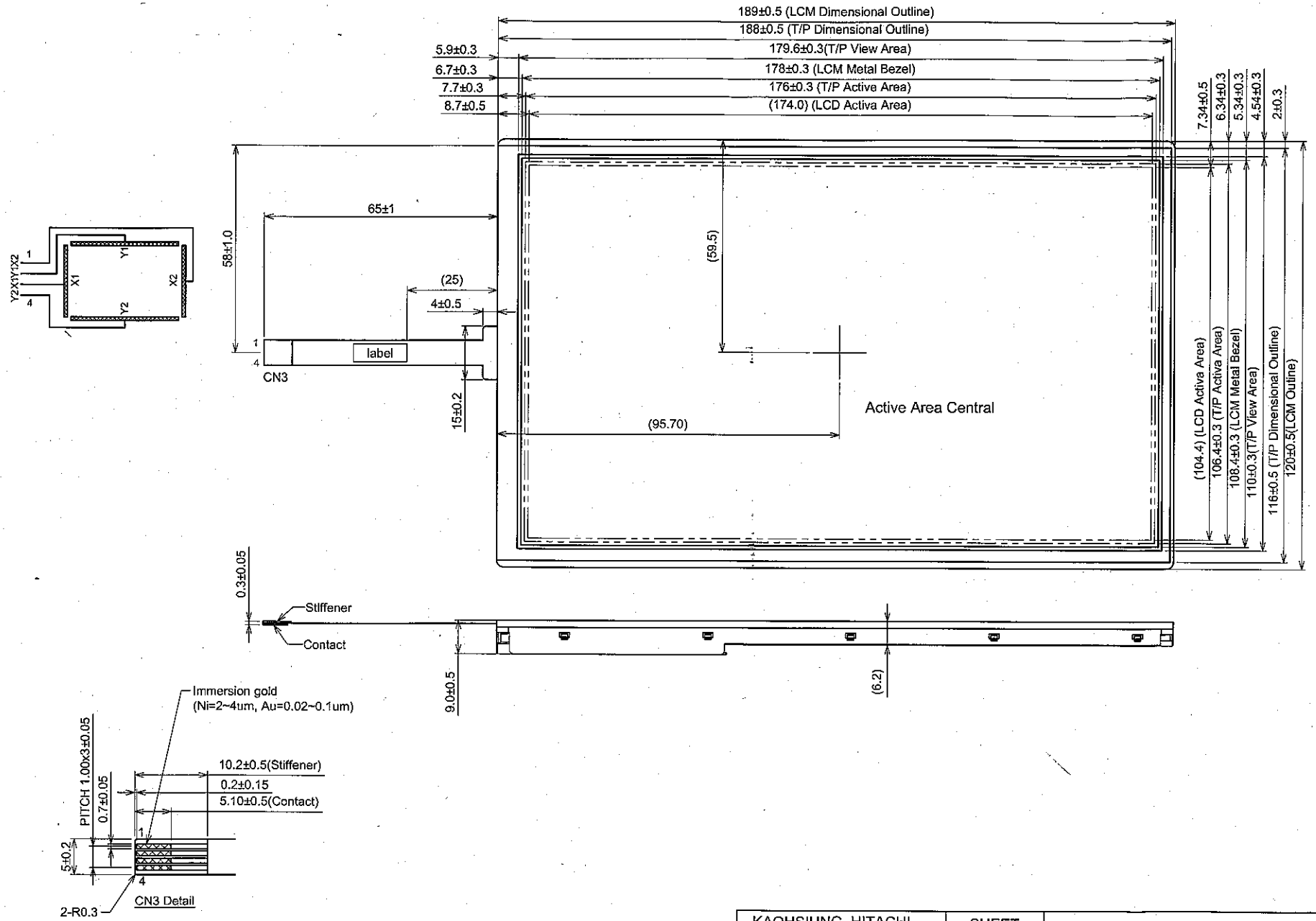
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue (1)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue (0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

10. OUTLINE DIMENSIONS

10.1 SURFACE SIDE



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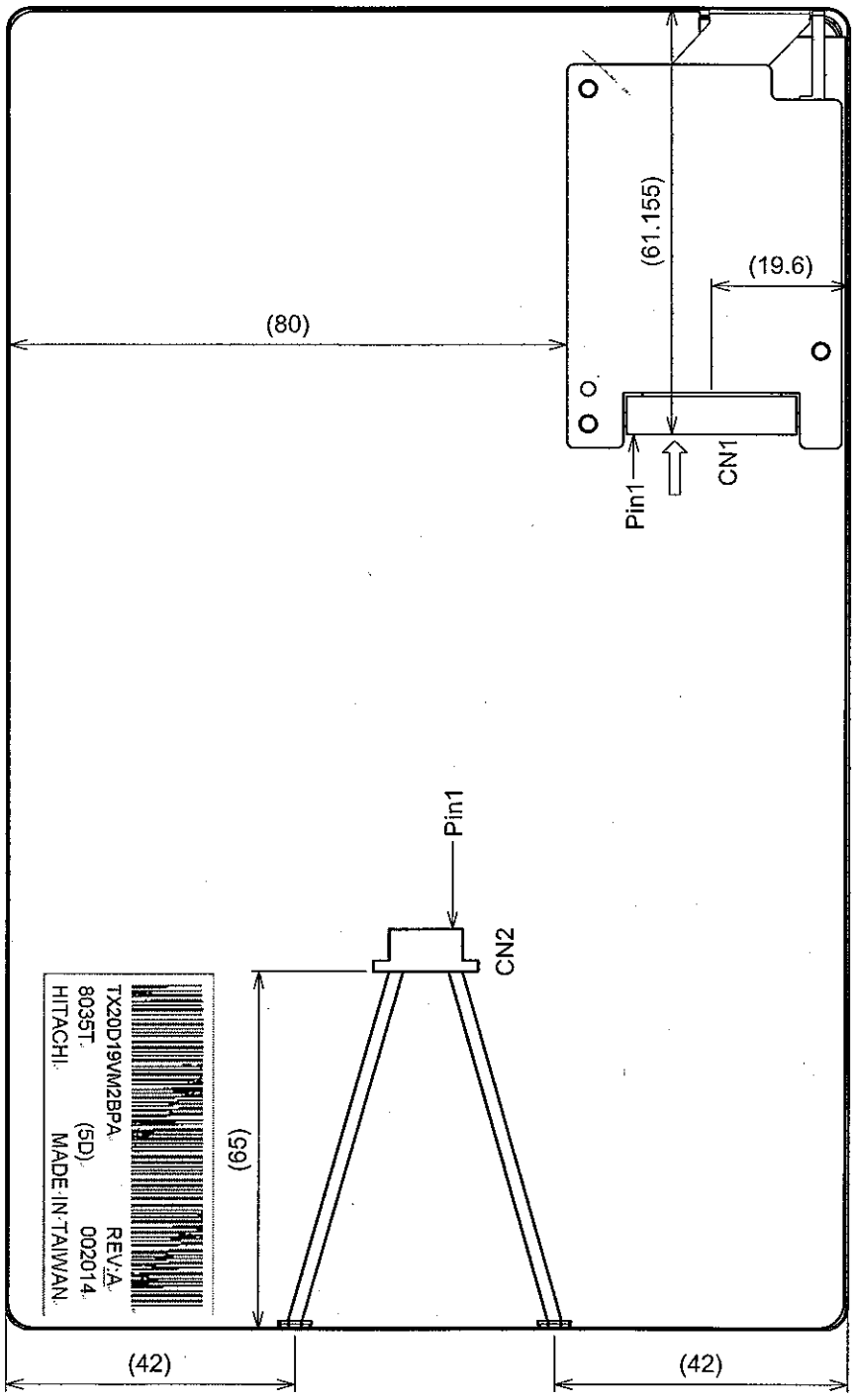
10.2 BACK SIDE



Note 1) CN1 : FA5B040HP1R3000
CN2 : BHR-03VS-1(JST)

Unit : mm

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11. TOUCH PANEL

The type of touch panel used on this display is resistive, analog, 4-wire and film on glass, and more characteristics are shown as below:

11.1 OPERATING CONDITIONS

Item	Specification	Remarks
Operating Voltage	5VDC	-

11.2 ELECTRICAL CHARACTERISTICS

Item		Specification	Remarks
Resistance Between Terminal	X1-X2	380~1010Ω	-
	Y1-Y2	180~520Ω	
Insulation Resistance	X-Y	20MΩmin.	At 25V DC
Linearity	X	±1.5% max.	Note 1
	Y	±1.5% max.	
Chattering		10ms max.	-

Note 1: The test conditions and equipments of linearity are as below:

- Material of pen: poly-acetal resin
- End shape: R 0.8 mm
- Test force: 150 gf
- Pitch: 10 mm
- Test area is shown in Fig. 11.1

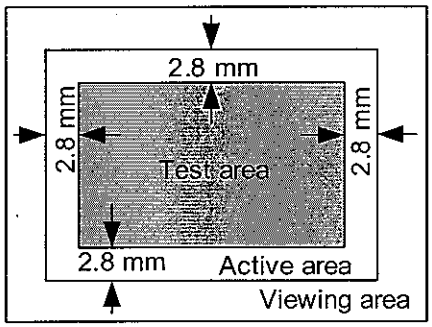


Fig. 11.1

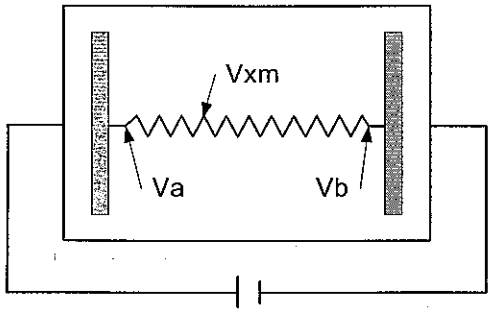


Fig. 11.2

As shown in Fig. 11.2, applying voltage meter to measure Va, Vb and Vxm, where Va is the maximum voltage in the active area; Vb is the minimum voltage in the active area; Vxm is the measured voltage of point x selected by random. Afterwards, the linearity can be calculated by following equation:

$$Linearity = \frac{|Vxi - Vxm|}{Va - Vb} \times 100\%,$$

where Vxi is the idea voltage of point x.

The method to measure the linearity of Y-axis is the same as above.

11.3 MECHANICAL CHARACTERISTICS

Item	Specification	Remarks
Pen Input Pressure	5 ~ 80gf	R0.8, Polyacetal Pen
Finger	5 ~ 80gf	R8.0, Silicon Rubber
Surface Hardness	3H min.	JIS K 5400

11.4 OPTICAL CHARACTERISTICS

Item	Specification	Remarks
Transmittance	80% min.	-

11.5 SAFETY AND ATTENTIONS

1) Do not put heavy shock or stress on the touch panel.
2) Please use soft cloth or absorbent cotton with ethanol to clean the touch panel by gently wiping. Moreover, please wipe it by horizontal or vertical direction instead of circling to prevent leaving scars on the touch panel's surface.
3) Do not use any harmful chemicals such as acetone, toluene, and isopropyl alcohol to clean the display's surface.

12. APPEARANCE STANDARD

The appearance inspection is performed in a dark room around 2000 lx based on the conditions as below:

- The distance between inspector's eyes and display is 30 cm.
- The viewing zone is defined with angle θ shown in Fig. 11. The inspection should be performed within 45° when display is shut down. The inspection should be performed within 5° when display is power on.

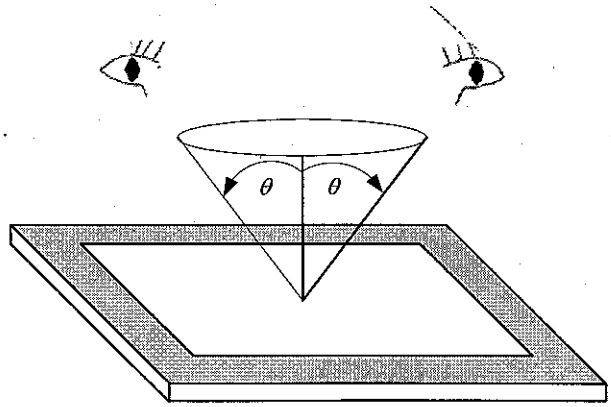


Fig. 12.1

12.1 THE DEFINITION OF LCD ZONE

LCD panel is divided into 3 areas as shown in Fig.11.2 for appearance specification in next section. A zone is the LCD active area (dot area); B zone is the area, which extended 1 mm out from LCD active area; C zone is the area between B zone and metal frame.

In terms of housing design, B zone is the recommended window area customers' housing should be located in.

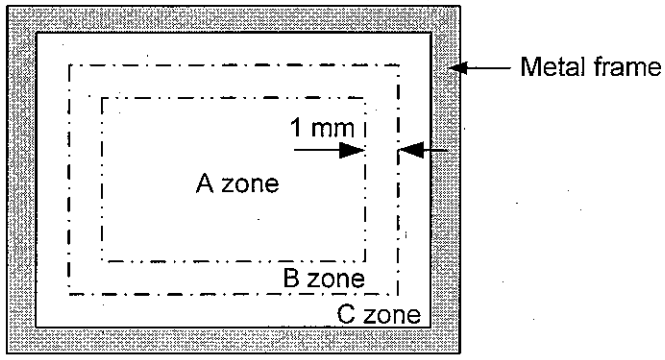


Fig. 12.2

12.2 LCD APPEARANCE SPECIFICATION

The specification as below is defined as the amount of unexpected phenomenon or material in different zones of LCD panel. The definitions of length, width and average diameter using in the table are shown in Fig. 12.3 and Fig. 12.4.

Item	Criteria				Applied zone
Scratches	Length (mm)	Width (mm)	Maximum number	Minimum space	A
	Ignored	W≦0.01	Ignored	-	
	L≦40	W≦0.02	10	-	
	L≦20	W≦0.04	10	-	
	Round (Dot Shape)				
	Average diameter (mm)		Maximum number	Minimum space	
	D≦0.2		Ignore	-	
	D≦0.4		10	-	
Dent	Serious one is not allowed				A
Wrinkles in polarizer	Serious one is not allowed				A
Bubbles on polarizer	Average diameter (mm)		Maximum number		A
	D≦0.3		Ignored		
	D≦0.5		10		
	D≦1.0		5		
1) Stains 2) Foreign Materials 3) Dark Spot	Filamentous (Line shape)				A
	Length (mm)	Width (mm)	Maximum number		
	Ignored	W≦0.02	Ignored		
	L≦2.0	W≦0.03	10		
	L≦1.0	W≦0.06	10		
	Round (Dot shape)				A
	Average diameter (mm)	Maximum number	Minimum Space		
	D≦0.22	Ignored	-		
	D≦0.33	5	-		
	D>0.33	0	-		
	In total	Filamentous + Round=10			
	Those wiped out easily are acceptable				
Dot-Defect (Note 1)		Type	Maximum number		A
	Bright dot-defect	1 dot	4		
		2 adjacent dot	1		
		3 adjacent dot or above	Not allowed		
		Density	2(ϕ 20mm)		
		In total	5		
	Dark dot-defect	1 dot	5		
		2 adjacent dot	2		
		3 adjacent dot or above	Not allowed		
		Density	3(ϕ 20mm)		
		In total	5		
	In total		10		

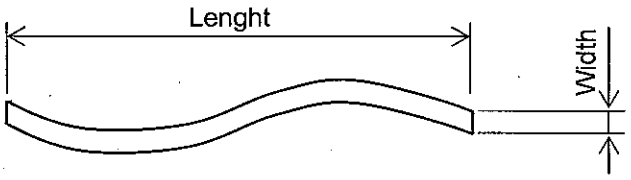
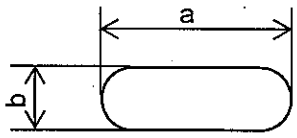


Fig 12.3



Average diameter = $\frac{a+b}{2}$

Fig 12.4

Note 1: The defi nitions of dot defect are as below:

- The defect area of the dot must be bigger than half of a dot.
- For bright dot-defect, showing black pattern, the dot's brightness must be over 30% brighter than others.
- For dark dot-defect, showing white pattern, the dot's brightness must be under 70% darker than others.
- The definition of 1-dot-defect is the defect-dot, which is isolated and no adjacent defect-dot.
- The definition of adjacent dot is shown as Fig. 12.5.
- The Density of dot defect is defined in the area within diameter ϕ =20mm.

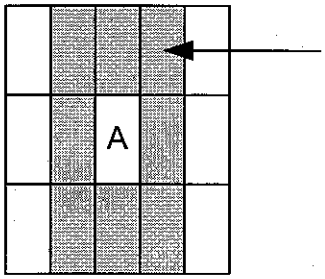


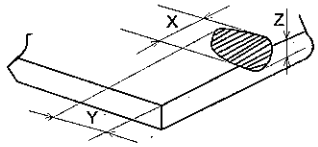
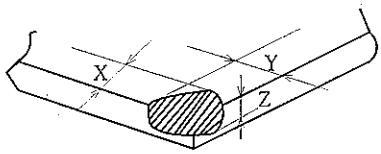
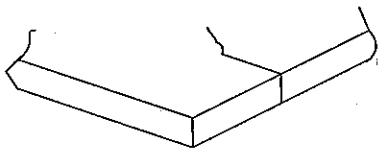
Fig. 12.5

12.3 TOUCH PANEL APPEARANCE SPECIFICATION

The specification as below is defined by the amount of unexpected material in different zones of touch panel.

Item	Criteria			Applied zone
Scratches	Width (mm)	Length (mm)	Maximum number	A
	W>0.1	L≧10	Not allowed	
	0.10≧W>0.05	L<10	4 pcs max.	
	0.05≧W	L<10	Ignored	
Foreign Materials	Filamentous (Line shape)			A
	Width (mm)	Length (mm)	Maximum number	
	0.10≧W>0.05	3<L	Not allowed	
	0.05≧W	L≦3	Ignored	
	Round (Dot shape)			A
	Average diameter (mm)		Maximum number	
	D>0.3		Not allowed	
	0.3≧D>0.2		3 pcs max.	
	0.2≧D>0.1		5 pcs max.	
	0.1≧D		Ignored	
	D≦0.5mm		Ignored	B

The limitation of glass flaw occurred on touch panel is defined in the table as below.

Item	Specifications	
Edge flaw		X ≤ 3.0 mm Y ≤ 3.0 mm Z ≤ Thickness
Corner flaw		X ≤ 3.0 mm Y ≤ 3.0 mm Z ≤ Thickness
Progressive flaw		Not allowed

13. PRECAUTIONS

13.1 PRECAUTIONS of MOUNTING

1) Please refer to Fig. 13.1 for housing the display with touch panel into applications. The Fig. 13.1 shows some points as below:

- The cushion needs to be designed between housing and touch panel in order to avoid unexpected pressure to cause any wrong reactions, and the cushion should be located in the insulated area.
- The housing should not cover the active area of touch panel as the figure shown.

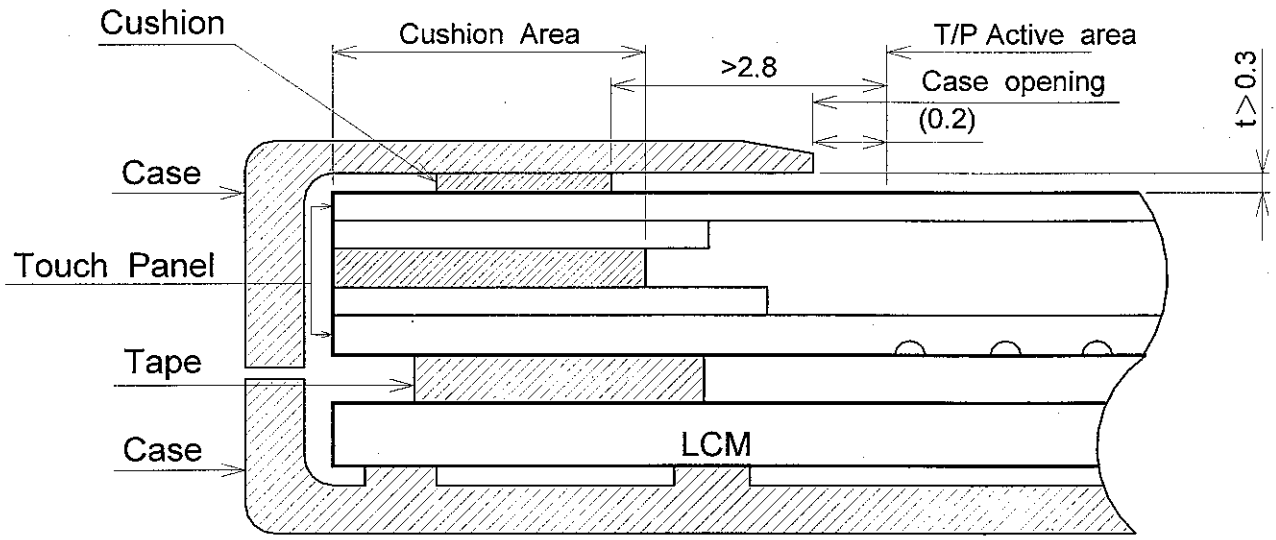


Fig 13.1

13.2 PRECAUTIONS of ESD

1) Before handling the display, please ensure your body has been connected to ground to avoid any damages by ESD. Also, do not touch display's interface directly when assembling.

2) Please remove the protection film very slowly before turning on the display to avoid generating ESD.

13.3 PRECAUTIONS of HANDLING

1) In order to keep the appearance of display in good condition, please do not rub any surfaces of the displays by sharp tools harder than 3H, especially touch panel, metal frame and polarizer.

2) Please do not pile the displays in order to avoid any scars leaving on the display. In order to avoid any injuries, please pay more attention for the edges of glasses and metal frame, and wear finger cots to protect yourself and the display before working on it.

3) Touching the display area or the terminal pins with bare hand is prohibited. This is because it will stain the display area and cause poor insulation between terminal pins, and might affect display's electrical characteristics furthermore.

4) Do not use any harmful chemicals such as acetone, toluene, and isopropyl alcohol to clean display's surfaces.

5) Please use soft cloth or absorbent cotton with ethanol to clean the display by gently wiping. Moreover, when wiping the display, please wipe it by horizontal or vertical direction instead of circling to prevent leaving scars on the display's surface, especially polarizer.

6) Please wipe any unknown liquids immediately such as saliva, water or dew on the display to avoid color fading or any permanently damages.

7) Maximum pressure to the surface of the display must be less than $1{,}96 \times 10^4$ Pa. If the area of adding pressure is less than $1\,cm^2$, the maximum pressure must be less than 1.96N.

13.4 PRECAUTIONS OF OPERATING

1) Please input signals and voltages to the displays according to the values defined in the section of electrical characteristics to obtain the best performance. Any voltages over than absolute maximum rating will cause permanent damages to this display. Also, any timing of the signals out of this specification would cause unexpected performance.

2) When the display is operating at significant low temperature, the response time will be slower than it at 25C°. In high temperature, the color will be slightly dark and blue compared to original pattern. However, these are temperature-related phenomenon of LCD and it will not cause permanent damages to the display when used within the operating temperature.

3) The use of screen saver or sleep mode is recommended when static images are likely for long periods of time. This is to avoid the possibility of image sticking.

4) Spike noise can cause malfunction of the circuit. The recommended limitation of spike noise is no bigger than ± 100 mV.

13.5 PRECAUTIONS of STORAGE

If the displays are going to be stored for years, please be aware the following notices.

1) Please store the displays in a dark room to avoid any damages from sunlight and other sources of UV light.

2) The recommended long term storage temperature is between 10C°~35C° and 55%~75% humidity to avoid causing bubbles between polarizer and LCD glasses, and polarizer peeling from LCD glasses.

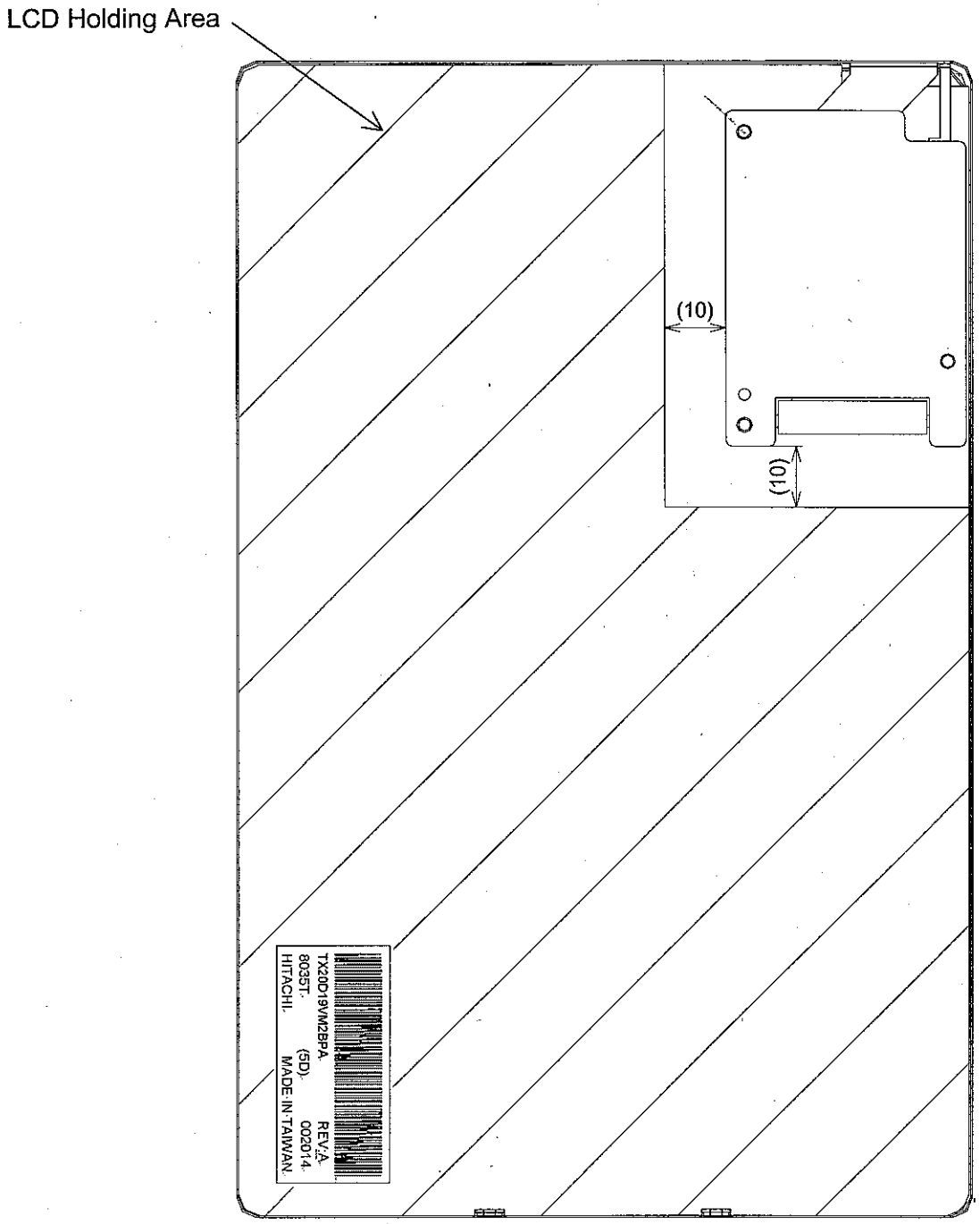
3) It would be better to keep the displays in the container, which is shipped from Hitachi, and do not unpack it.

4) Please do not stick any labels on the display surface for a long time, especially on the polarizer.

13.6 MOUNTING PRECAUTION

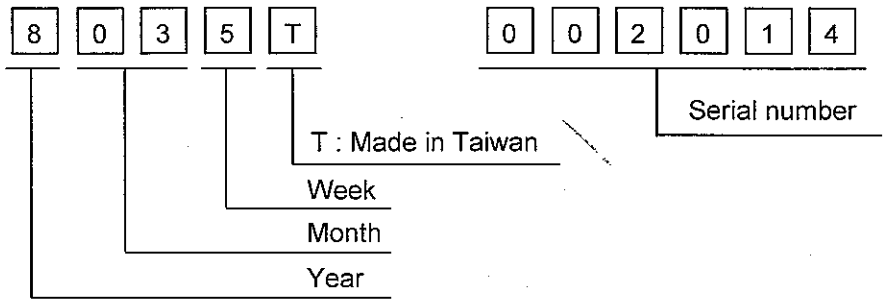
1) When assembling the LCM Module, please refer to the below.

2) The use of cushion is recommended in order to protect the module from shock.



14. DESIGNATION of LOT MARK

1) The lot mark is showing in Fig.14.3. First 4 digits are used to represent production lot, T represented made in Taiwan, and the last 5 digits are the serial number.



2) The tables as below are showing what the first 4 digits of lot mark are shorted for.

Year	Mark
2008	8
2009	9
2010	0
2011	1
2012	2

Month	Mark	Month	Mark
1	01	7	07
2	02	8	08
3	03	9	09
4	04	10	10
5	05	11	11
6	06	12	12

Week (Days)	Mark
1~7	1
8~14	2
15~21	3
22~28	4
29~31	5

3) Except letters I and O, revision number will be showen on lot mark and following letters A to Z.

4) The location of the lot mark is on the back of the display shown in Fig. 14.3.

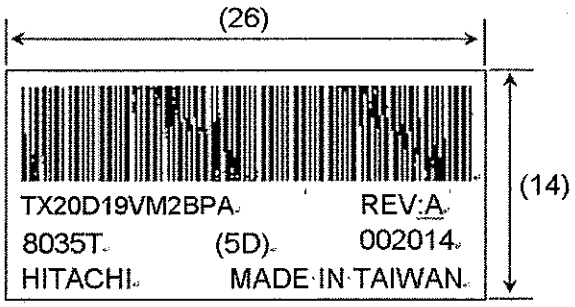


Fig 14.3