Japan Display Inc.

FOR MESSRS : _____ DATE : Jun. 18th ,2024

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

TX40D201VM0BAB

Contents

ACCEPTED BY: _____ PROPOSED BY: Alex Lee

2. RECORD OF REVISION

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Jun.18,'24	7B64PS 2703-TX40D201VM0BAB-2 Page 3-1/1	3.1 DISPLAY FEATURES Revised：This module is a 15.8" FHD of 16:9 format amorphous silicon TFT. →This module is a 15.8" format amorphous silicon TFT.
	7B64PS 2708-TX40D201VM0BAB-2 Page 8-1/1	8. RELIABILITY TESTS of Revised：Heat Cycle (1) Non-Operating → (1) Operating
	7B64PS 2709-TX40D201VM0BAB-2 Page 9-2/10	9.1 INTERFACE PIN CONNECTIONS Delete : The touch panel interface FPC (CN3)

3. GENERAL DATA

3.1 DISPLAY FEATURES

This module is a 15.8” format amorphous silicon TFT. The pixel format is vertical stripe and sub pixels are arranged as R (red), G (green), B (blue) sequentially. This display is RoHS compliant, COG (chip on glass) technology and LED backlight are applied on this display.

Part Name	TX40D201VM0BAB
Module Dimensions	409.8(W) mm x 109.5(H) mm x 14.5(D) mm
LCD Active Area	389.76(W) mm x 91.35(H) mm
Pixel Pitch	0.15225(W) mm x 0.15225(H) mm
Resolution	2560 x 3(RGB)(W) x 600(H) dots
Color Pixel Arrangement	R, G, B Vertical stripe
LCD Type	IPS, Normally Black, Transmissive Type
Display Type	Active Matrix
Number of Colors	16.7M Colors (8-bit RGB)
Backlight	Light Emitting Diode (LED)
Weight	658g
Interface	LVDS; 20 pins
Power Supply Voltage	3.3V for LCD ; 42.4V for Backlight
Power Consumption	1.6W for LCD; 11.872W for Backlight
Viewing Direction	Super Wide Version (In Plane Switching)

4. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Remarks
Supply Voltage	V_{DD}	-0.3	4.0	V	-
Input Voltage of Logic	V_I	-0.3	V_{DD} +0.3	V	Note 1
Operating Temperature	T_{op}	-30	80	˚C	Note 2
Storage Temperature	T_{st}	-30	80	˚C	Note 2
Backlight Input Current	I_{LED}	0	280	mA	-

Note 1: The rating is defined for the signal voltages of the interface such as CLK and data pairs.

Note 2: The maximum rating is defined as above based on the chamber temperature, which might be different from ambient temperature after assembling the panel into the application. Moreover, some temperature-related phenomenon as below needed to be noticed:

- Background color, contrast and response time would be different in temperatures other than 25 ˚C.
- Operating under high temperature will shorten LED lifetime.

5. ELECTRICAL CHARACTERISTICS

5.1 OPERATING CONDITIONS

$T_a = 25\ ^{\circ}C,\ V_{SS} = 0V$

Item	Symbol	Condition	Standard Value			Unit	Remarks
			Min.	Typ.	Max.		
Power Supply Voltage	V_{DD}	-	3.0	3.3	3.6	V	-
Power Supply Current	I_{DD}	Note 1	400	480	560	mA	Note 1,5
Input Signal Voltage	V_{IH1}	-	$0.8V_{DD}$	-	V_{DD}	V	Note 2
	V_{IL1}	-	V_{SS}	-	$0.2V_{DD}$	V	
Allowable Ripple Voltage	VRP	-	-	-	100	mV (p-p)	Note 3
Differential Input High Threshold	VTH	VICM=1.2V	-	-	100	mV	Note 4
Differential Input Low Threshold	VTL	VICM=1.2V	-100	-	-	mV	
Input Differential Voltage	|VID|	-	100	-	600	mV	
Differential Input Common Mode Voltage	VICM	-	1	1.2	1.4	V	
Frame Frequency	f_{Frame}	-	40	60	65	Hz	-
CLK Frequency	f_{CLK}	-	69.8	101	109.4	MHz	-

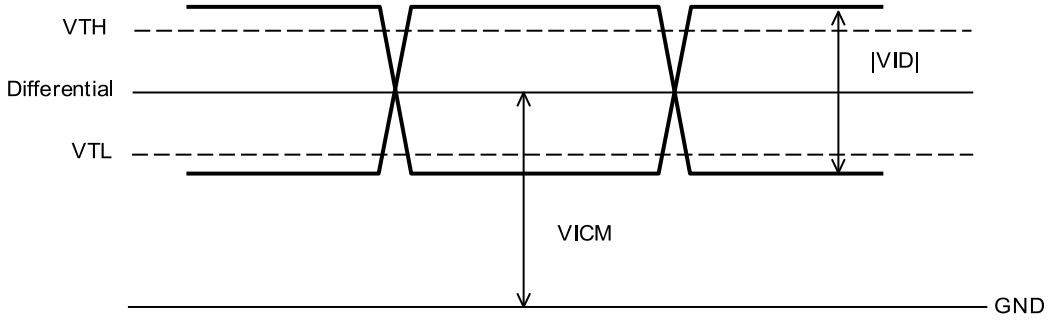
Note 1: Measurement pattern: All white.

Power supply voltage: Typ. voltage.

Note 2: Signals of interest is SD and MODE.

Note 3: Applied pin is {V_{DD}}

Note 4: Signal of interest is LVDS.



Note 5: 1.5A fuse is applied in the module for I_{DD}. For display activation and protection purpose, power supply is recommended larger than 3.75 to start the display and break fuse once any short circuit occurred.

5.2 BACKLIGHT CHARACTERISTICS

$T_a = 25\ ^\circ C$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
LED Input Voltage	V_{LED}	I_{LED}=280mA	-	42.4	46.5	V	Note 1
LED Forward Current	I_{LA}	V_{LED} = (42.4V)	-	70	-	mA	Note 2
LED Lifetime	-	I_{LED}=280mA	-	50K	-	hrs	Note 2

Note 1: Fig. 5.1 shows the LED backlight circuit.

Note 2: The estimated lifetime is specified as the time to reduce 50% brightness by applying I_{LA}=70mA at 25°C.

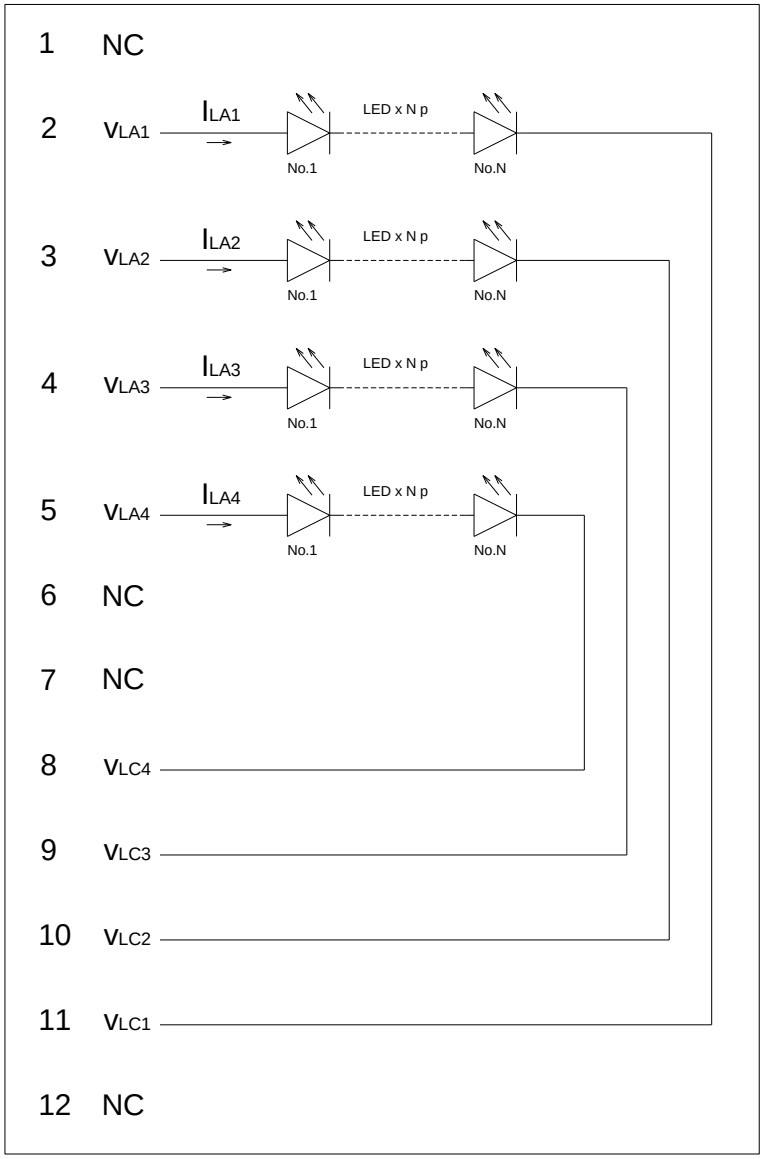


Fig 5.1

6. OPTICAL CHARACTERISTICS

The optical characteristics are measured based on the conditions as below:

- Supplying the signals and voltages defined in the section of electrical characteristics.
- The backlight unit needs to be turned on for 30 minutes.
- The ambient temperature is 25°C.
- In the dark room around 200 lx, the equipment has been set for the measurements as shown in Fig 6.1.

$T_a = 25\ ^\circ C$, $f_{Frame} = 60$ Hz, $V_{DD} = 3.3V$

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
Brightness of White		-	$\phi = 0^\circ, \theta = 0^\circ$, I_{LED}= 280 mA	1120	1300	-	cd/m^2	Note 1
Brightness Uniformity		-		70	-	-	%	Note 2
Contrast Ratio		CR		800	1100	-	-	Note 3
Response Time		$T_r + T_f$	$\phi = 0^\circ, \theta = 0^\circ$	-	20	-	ms	Note 4
NTSC Ratio		-	$\phi = 0^\circ, \theta = 0^\circ$	-	70	-	%	-
Viewing Angle		θx	$\phi = 0^\circ$, CR ≥ 10	-	85	-	Degree	Note 5
		$\theta x'$	$\phi = 180^\circ$, CR ≥ 10	-	85	-		
		θy	$\phi = 90^\circ$, CR ≥ 10	-	85	-		
		$\theta y'$	$\phi = 270^\circ$, CR ≥ 10	-	85	-		
Color Chromaticity	Red	X	$\phi = 0^\circ, \theta = 0^\circ$	0.60	0.65	0.70	-	Note 6
		Y		0.29	0.34	0.39		
	Green	X		0.26	0.31	0.36		
		Y		0.59	0.64	0.69		
	Blue	X		0.10	0.15	0.20		
		Y		0.06	0.11	0.16		
	White	X		0.26	0.31	0.36		
		Y		0.25	0.35	0.04		

Note 1: The brightness is measured from the panel center point, P5 in Fig. 6.2, for the typical value.

Note 2: The brightness uniformity is calculated by the equation as below:

$$\text{Brightness uniformity} = \frac{\text{Min. Brightness}}{\text{Max. Brightness}} \times 100\%$$

which is based on the brightness values of the 9 points measured by BM-5 as shown in Fig. 6.2.

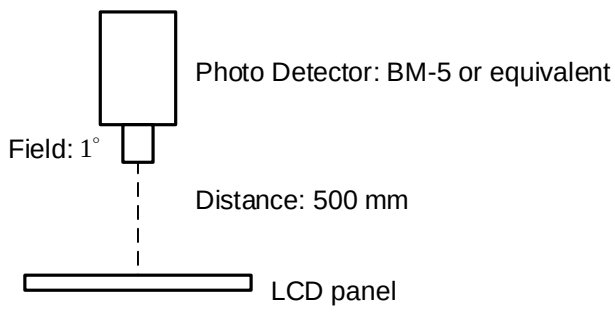


Fig. 6.1

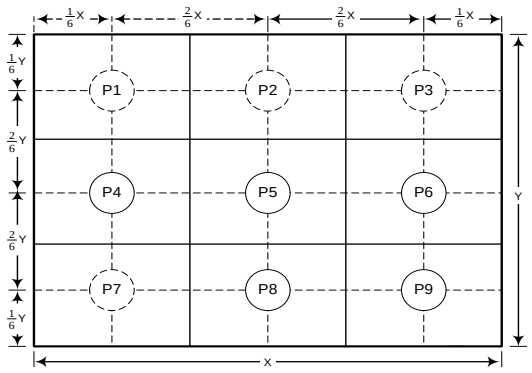


Fig. 6.2

Note 3: The Contrast Ratio is measured from the center point of the panel, P5, and defined as the following equation:

$$CR = \frac{\text{Brightness of White}}{\text{Brightness of Black}}$$

Note 4: The definition of response time is shown in Fig. 6.3. The rising time is the period from 10% brightness to 90% brightness when the data is from black to white. Oppositely, Falling time is the period from 90% brightness rising to 10% brightness.

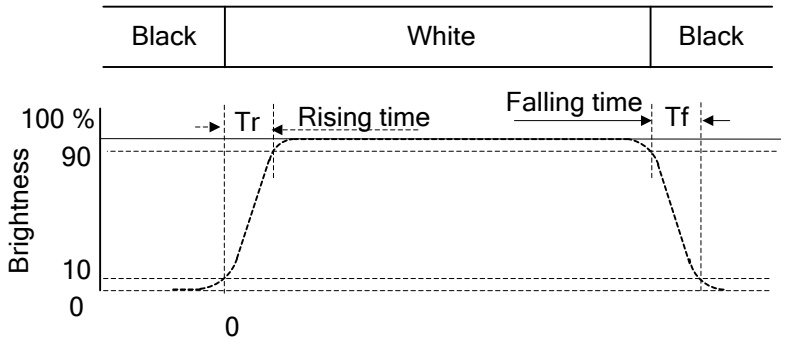


Fig 6.3

Note 5: The definition of viewing angle is shown in Fig. 6.4. Angle ϕ is used to represent viewing directions, for instance, $\phi = 270^\circ$ means 6 o'clock, and $\phi = 0^\circ$ means 3 o'clock. Moreover, angle θ is used to represent viewing angles from axis Z toward plane XY.

The display is super wide viewing angle version; 85° viewing angle can be obtained from each viewing direction.

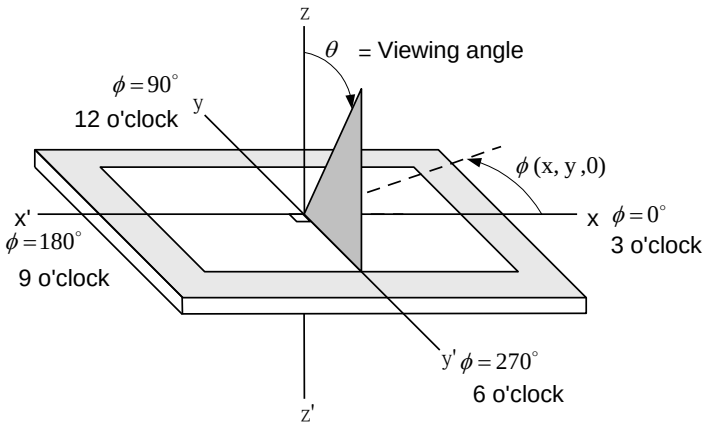
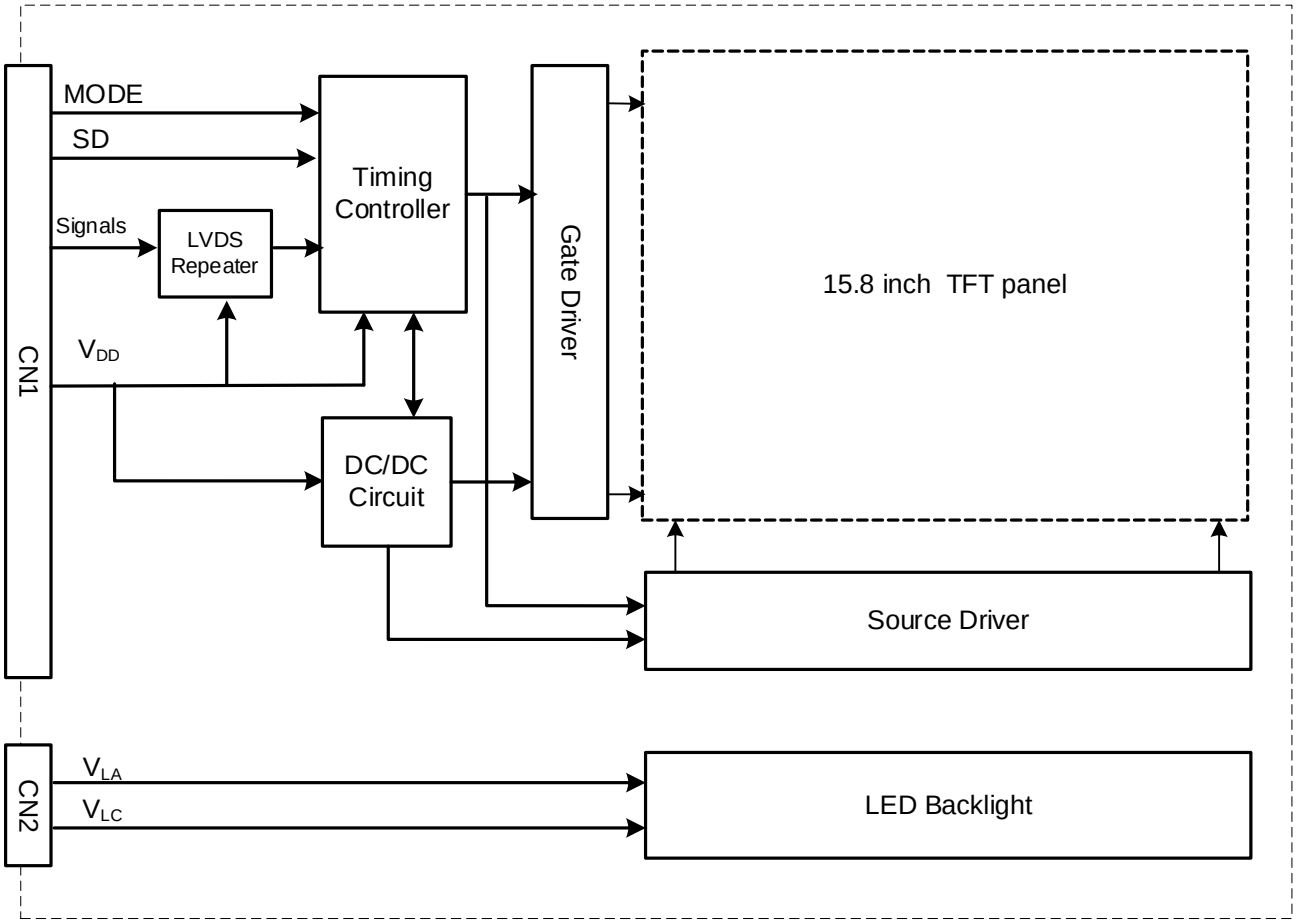


Fig 6.4

Note 6: The color chromaticity is measured from the center point of the panel, P5, as shown in Fig. 6.2.

7. BLOCK DIAGRAM



Note1：Signals are CLK and pixel data pairs.

8. RELIABILITY TESTS

Test Item	Condition	
High Temperature	1) Operating 2) 80°C	240 hrs
Low Temperature	1) Operating 2) -30°C	240 hrs
High Temperature	1) Storage 2) 80°C	240 hrs
Low Temperature	1) Storage 2) -30°C	240 hrs
Heat Cycle	1) Operating 2) -30°C↔80°C 3) 3hrs~1hr~3hrs	240 hrs
Thermal Shock	4) Non-Operating 5) -30°C↔80°C 6) 0.5 hr↔0.5 hr	240 hrs
High Temperature & Humidity	1) Operating 2) 65°C& 85%RH 3) Without condensation	240 hrs (Note 3)
Vibration	1) Non-Operating 2) 10～200 Hz 3) 5G 4) X, Y, and Z directions	1 hrs for each direction
Mechanical Shock	1) Non-Operating 2) 10 ms 3) 80G 4) ±X, ±Y and ±Z directions	Once for each direction
ESD	1) Operating 2) Tip: 150 pF, 330Ω 3) Air discharge for glass: ±8KV 4) Contact discharge for metal frame: ±8KV	1) Glass: 9 points 2) Metal frame: 8 points (Note4)

Note 1: Display functionalities are inspected under the conditions defined in the specification after the reliability tests.

Note 2: The display is not guaranteed for use in corrosive gas environments.

Note 3: Under the condition of high temperature & humidity, if the temperature is higher than 65℃, the humidity needs to be reduced as Fig. 8.1 shown.

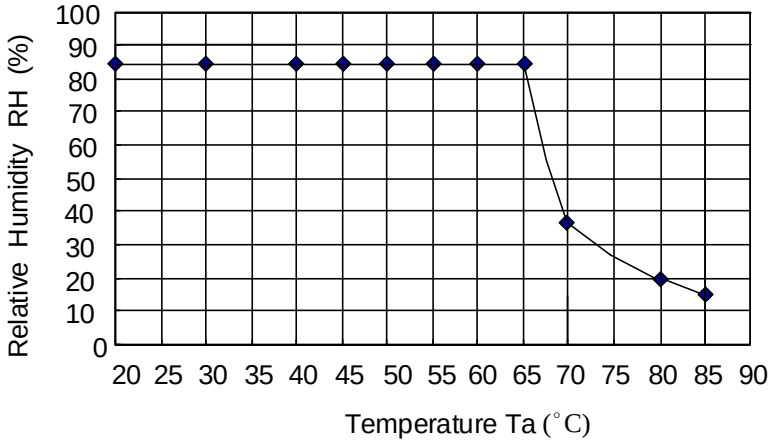


Fig. 8.1

Note 4: All pins of LCD interface (CN1) have been tested by ±100V contact discharge of ESD under non-operating condition.

9. LCD INTERFACE

9.1 INTERFACE PIN CONNECTIONS

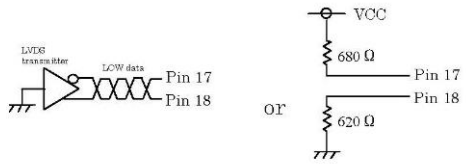
The display interface (CN1) is FI-SEB20P-HF13E made by JAE.

Pin assignment is as below:

No	Symbol	Function (MODE = Low)		Function (MODE = High)
		6 bit input	8 bit input	8 bit input
1	V_{DD}	+3.3V Power Supply for Logic		
2	V_{DD}	+3.3V Power Supply for Logic		
3	V_{SS}	GND (0V)		
4	V_{SS}	GND (0V)		
5	IN0-	R0~R5, G0	R2~R7, G2	R0~R5, G0
6	IN0+			
7	V_{SS}	GND (0V)		
8	IN1-	G1~G5, B0~B1	G3~G7, B2~B3	G1~G5, B0~B1
9	IN1+			
10	V_{SS}	GND (0V)		
11	IN2-	B3~B5, DE	B4~B7, DE	B3~B5, DE
12	IN2+			
13	V_{SS}	GND (0V)		
14	CLK IN-	Pixel Clock		
15	CLK IN+			
16	V_{SS}	GND (0V)		
17	IN3-	See:*2)	R0~R1, G0~G1, B0~B1	R6~R7, G6~G7, B6~B7
18	IN3+			
19	MODE	Low= JEIDA	Low= JEIDA	High= VESA
20	SD	Scan Direction Control (Note 3)		

Note 1: INn- and INn+ (n=0,1,2,3), CLK IN- and CLK IN+ should be wired by twist-pairs or side-by-side FPC patterns, respectively.

Note 2: Recommended wiring of Pin 17,18 (6 bit input)



Note 3: Scan direction is available to be switched as below.

SD：Low or Open (Default)

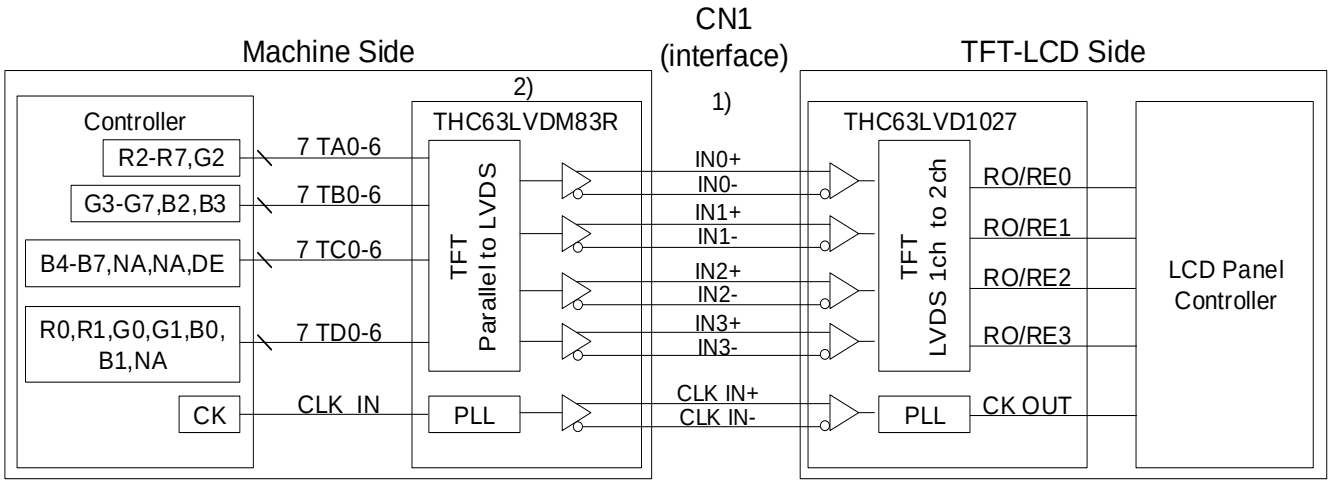
SD：High

The backlight interface (CN2) is SM10B-SRSS-TB(LF)(SN) made by JST.

Pin assignment is as below:

Pin No.	Symbol	Signal	Pin No.	Symbol	Signal
1	VLA1	LED Anode Terminal1	6	VLC1	LED Cathode Terminal1
2	VLA2	LED Anode Terminal2	7	VLC2	LED Cathode Terminal2
3	VLA3	LED Anode Terminal3	8	VLC3	LED Cathode Terminal3
4	VLA4	LED Anode Terminal4	9	VLC4	LED Cathode Terminal4
5	NC	Non-Connection	10	NC	Non-Connection

9.2 LVDS INTERFACE

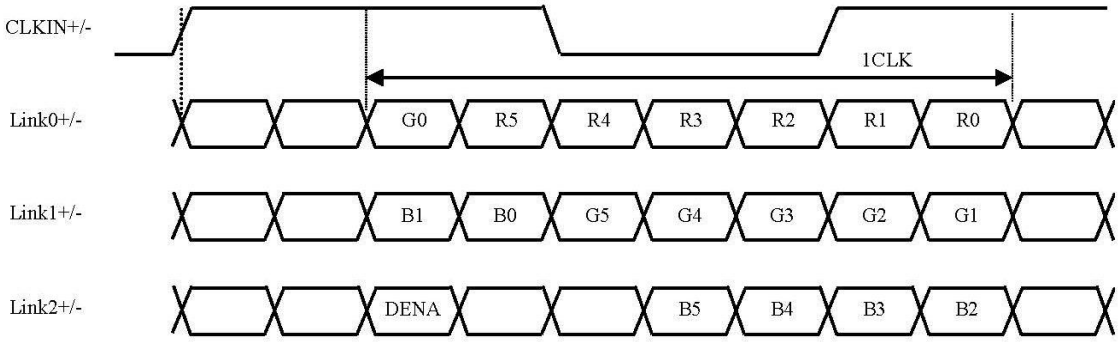


Note 1: LVDS cable impedance should be 100 ohms per signal line when each 2-lines (+, -) is used in differential mode.

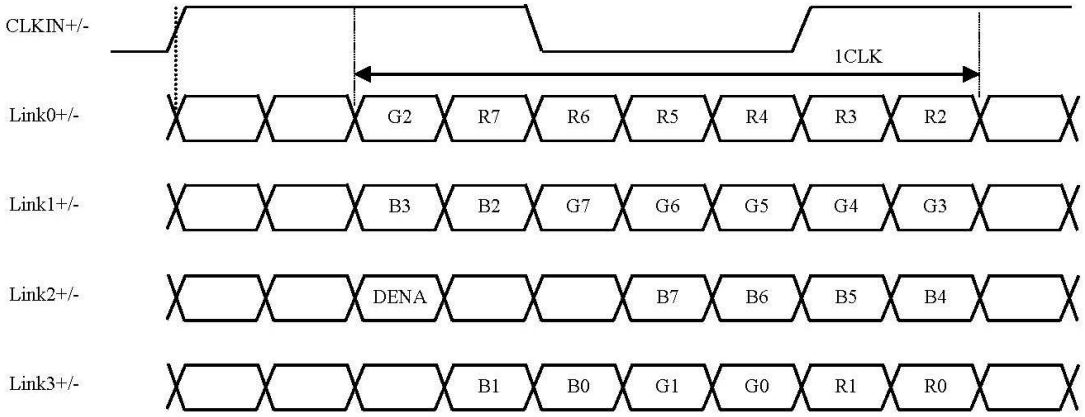
Note 2: The recommended transmitter, THC63LVDM83R, is made by Thine or equivalent, which is not contained in the module.

9.3 LVDS DATA FORMAT

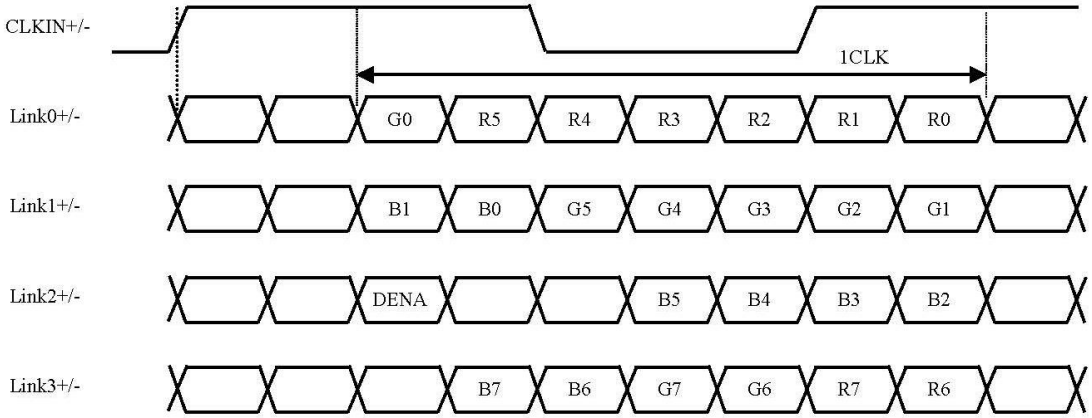
9.3.1 MODE = Low (6 bit input)



9.3.2 MODE = Low (8 bit input)



9.3.3 MODE = High (8 bit input)



DENA: Display Enable

NA: Not Available

9.4 TIMING CHART

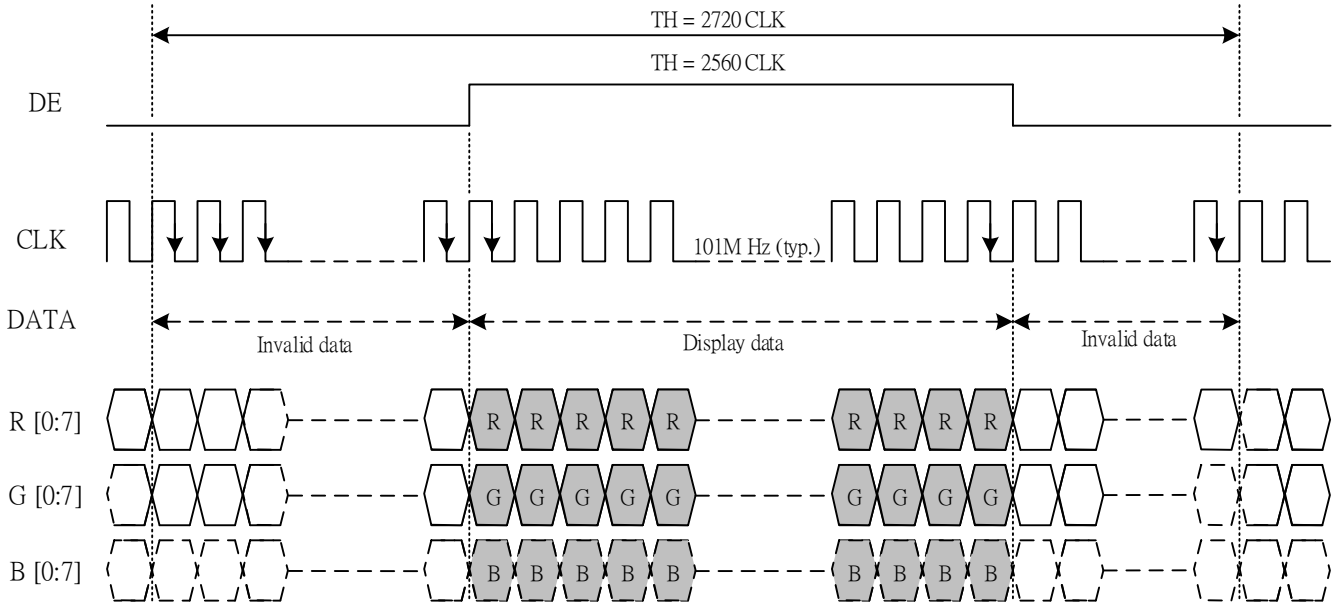


Fig. 9.1 Horizontal Timing

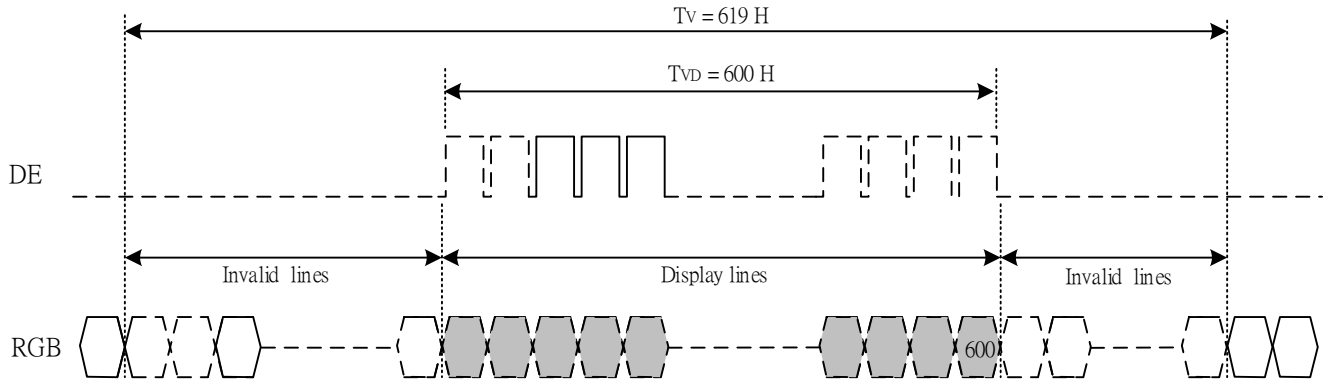


Fig. 9.2 Vertical Timing

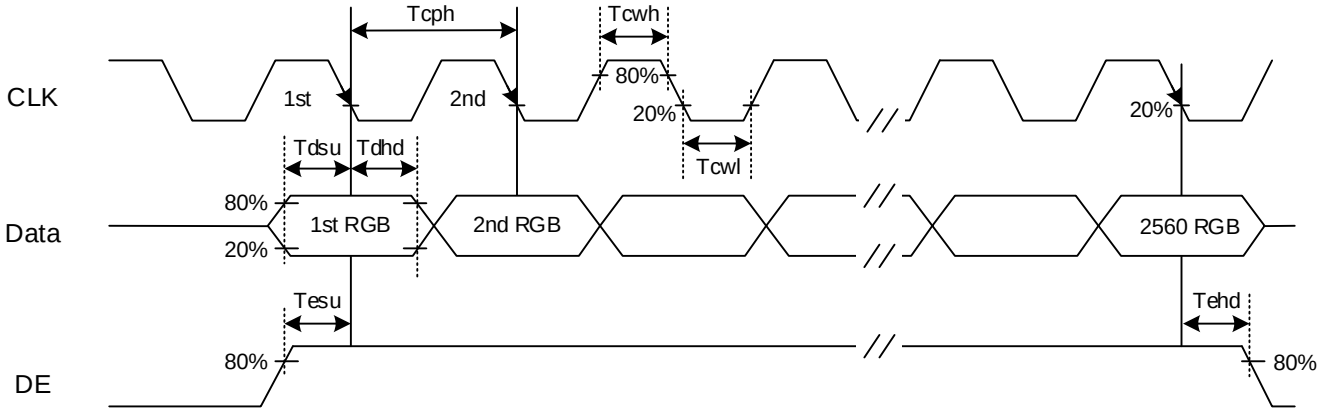


Fig. 9.3 Setup & Hold Time

9.5 TIME TABLE

The column of timing sets including minimum, typical, and maximum as below are based on the best optical performance, frame frequency (f_{Frame}) = 60 Hz to define. If 60 Hz is not the aim to set, less than 65 Hz for f_{Frame} is recommended to apply for better performance by other parameter combination as the definitions in section 5.1.

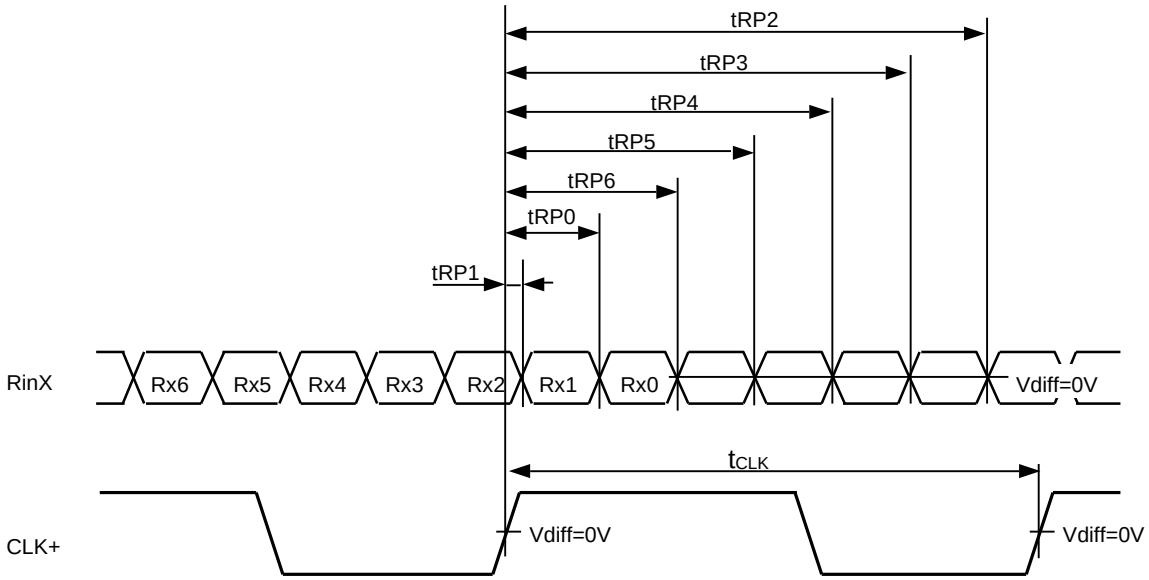
A. Horizontal and Vertical Timing

Item		Symbol	Min.	Typ.	Max.	Unit
Horizontal	CLK Frequency	fclk	-	101	-	M Hz
	Display Data	thd		2560		CLK
	Cycle Time	th	2652	2720	3082	
Vertical	Display Data	tvd		600		H
	Cycle Time	tv	604	619	680	

B. Setup and Hold Time

Item		Symbol	Min.	Typ.	Max.	Unit
CLK	Duty	Tcwh	40	50	60	%
	Cycle Time	Tcph	-	13.89	-	ns
Data	Setup Time	Tdsu	6	-	-	
	Hold Time	Tdhd	6	-	-	
DE	Setup Time	Tesu	6	-	-	
	Hold Time	Tehd	6	-	-	

9.6 LVDS RECEIVER TIMING



RinX= (RinX+)-(RinX-) (X=0, 1, 2, 3)

Item		Symbol	Min.	Typ.	Max.	Unit
CLK	Cycle frequency	1/tcLK	-	101	-	MHz
RinX (X=0,1,2,3)	0 data position	tRP0	-	1/7* t_{CLK}	-	ns
	1st data position	tRP1	-	0	-	
	2nd data position	tRP2	-	6/7* t_{CLK}	-	
	3rd data position	tRP3	-	5/7* t_{CLK}	-	
	4th data position	tRP4	-	4/7* t_{CLK}	-	
	5th data position	tRP5	-	3/7* t_{CLK}	-	
	6th data position	tRP6	-	2/7* t_{CLK}	-	

9.7 DATA INPUT for DISPLAY COLOR

9.7.1 MODE = Low

Input color		Red Data						Green Data						Blue Data					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
		MSB					LSB	MSB					LSB	MSB					LSB
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

Note 1: Definition of gray scale: Color(n) Number in parenthesis indicates gray scale level. Larger number corresponds to brighter level.

Note 2: Data Signal: 1 : High, 0 : Low

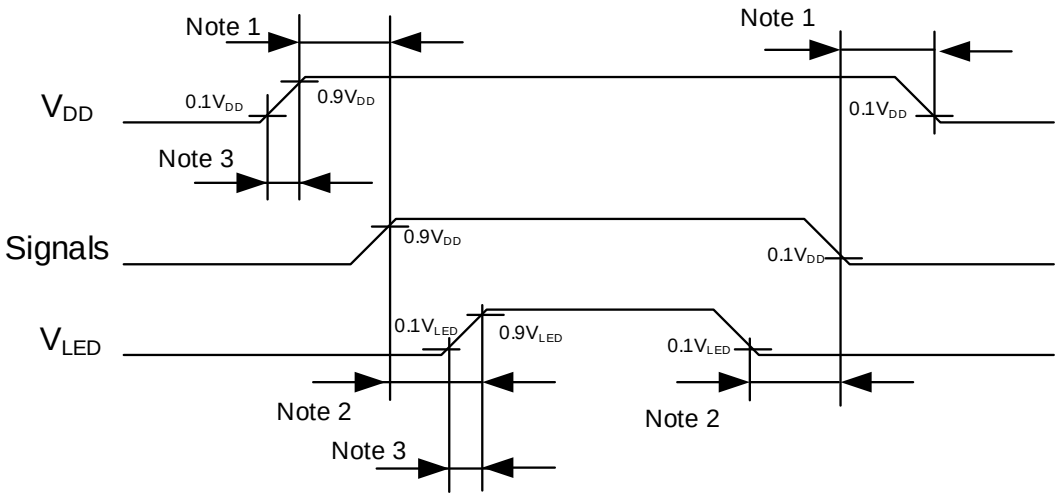
9.7.2 MODE = High(VESA)、Low(JEIDA)

Input color		Red Data								Green Data								Blue Data							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
		MSB							LSB	MSB							LSB	MSB							LSB
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note 1: Definition of gray scale : Color(n) Number in parenthesis indicates gray scale level. Larger number corresponds to brighter level.

Note 2: Data Signal : 1 : High, 0 : Low

9.8 POWER SEQUENCE

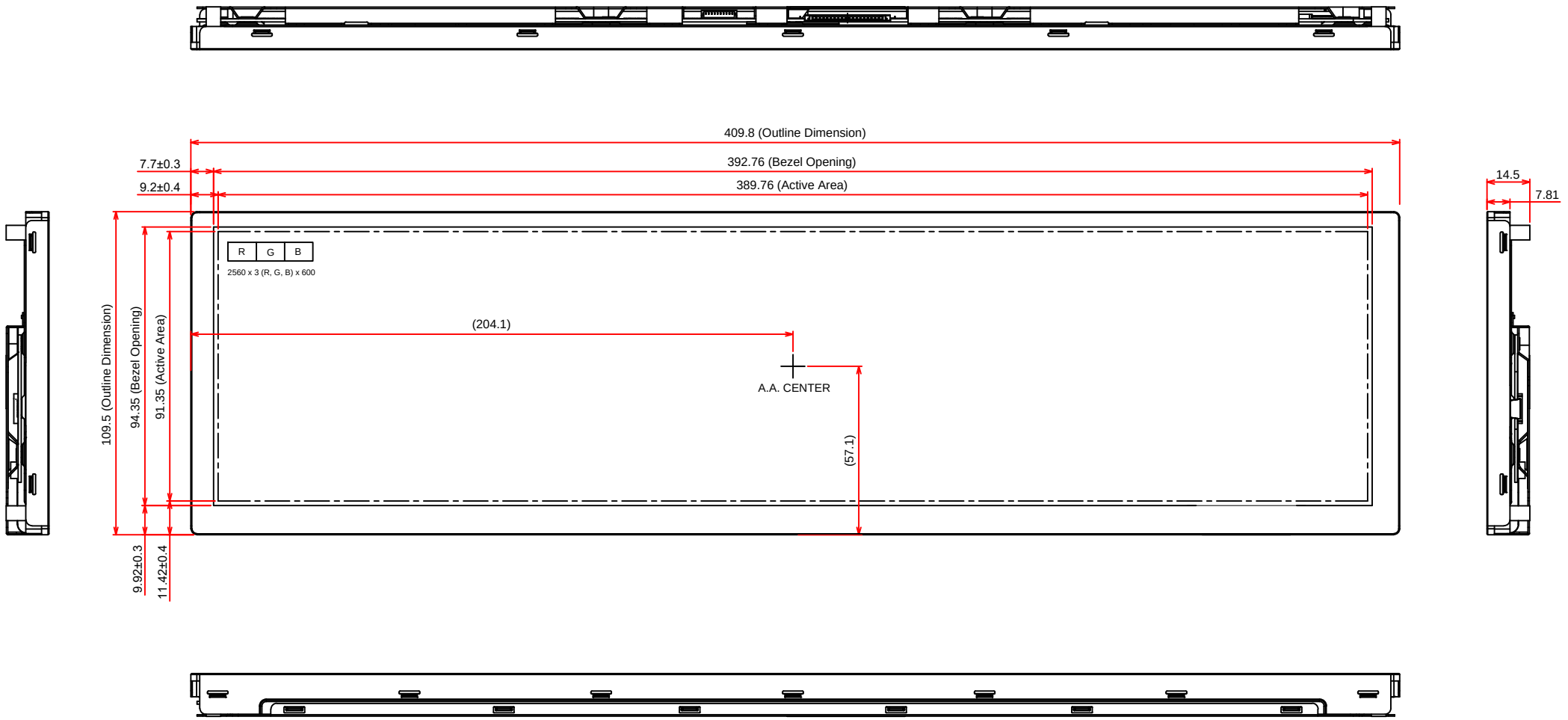


Note 1: In order to avoid any damages, V_{DD} has to be applied before all other signals. The opposite is true for power off where V_{DD} has to be remained on until all other signals have been switch off. The recommended time period is within 1 second. Hot plugging might cause display damage due to incorrect power sequence, please pay attention on interface connecting before power on.

Note 2: In order to avoid showing uncompleted patterns in transient state. It is recommended that switching the backlight on is delayed for 1 second after the signals have been applied. The opposite is true for power off where the backlight has to be switched off 1 second before the signals are removed.

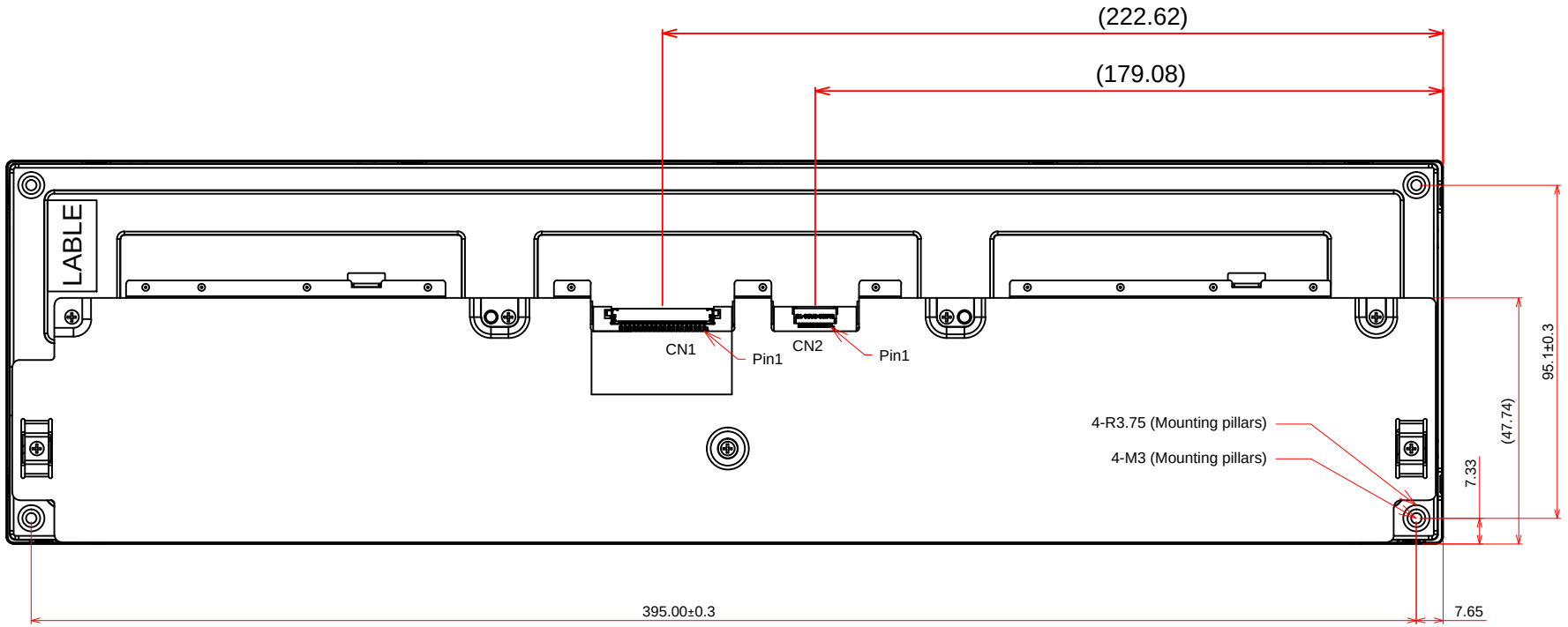
10. OUTLINE DIMENSIONS

10.1 FRONT VIEW



General Tolerance:±0.5mm
Scale : NTS
Unit : mm

10.2 REAR VIEW



General Tolerance:±0.5mm
Scale : NTS
Unit : mm

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11. APPEARANCE STANDARD

The appearance inspection is performed in a room around 500~1000 lx based on the conditions as below:

- The distance between inspector's eyes and display is 30 cm.
- The viewing zone is defined with angle θ shown in Fig. 11.1 The inspection should be performed within 45° when display is shut down. The inspection should be performed within 5° when display is power on.

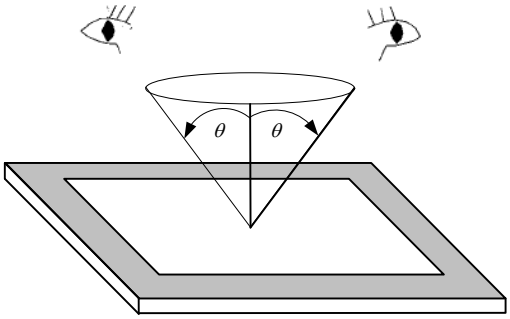


Fig. 11.1

11.1 THE DEFINITION OF LCD ZONE

LCD panel is divided into 2 areas as shown in Fig.11.2 for appearance specification in next section.

A zone is the LCD active area (dot area).

B zone is the area between A zone and Cover Lens V.A.

In terms of housing design, B zone is the recommended window area customers' housing should be located in.

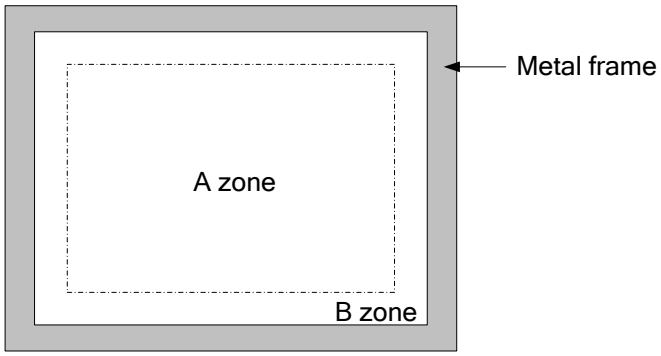


Fig. 11.2

11.2 LCD APPEARANCE SPECIFICATION

The specification as below is defined as the amount of unexpected phenomenon or material in different zones of LCD panel. The definitions of length, width and average diameter using in the table are shown in Fig. 11.3 and Fig. 11.4.

<table>
<tr><td>Item</td><td colspan="5">Criteria</td><td>Applied zone</td></tr>
<tr><td rowspan="5">Scratches</td><td>Length (mm)</td><td>Width (mm)</td><td>Maximum number</td><td>Minimum space</td><td rowspan="5">A, B</td></tr>
<tr><td>Ignored</td><td>W≦0.02</td><td>Ignored</td><td>-</td></tr>
<tr><td>L≦20</td><td>0.02＜W≦0.04</td><td>10</td><td>-</td></tr>
<tr><td>L≦5</td><td>0.04＜W≦0.1</td><td>3</td><td>-</td></tr>
<tr><td>-</td><td>0.1＜W</td><td>Not allowed</td><td>-</td></tr>
<tr><td rowspan="3">Polarizer r Bulge / Dent</td><td colspan="2">D≦0.3</td><td colspan="2">Ignored</td><td rowspan="3">A</td></tr>
<tr><td colspan="2">0.3＜D≦0.5</td><td colspan="2">4</td></tr>
<tr><td colspan="2">0.5＜D</td><td colspan="2">0</td></tr>
<tr><td>Wrinkles in polarizer</td><td colspan="5">Serious one is not allowed</td><td>A</td></tr>
<tr><td rowspan="5">Bubbles on polarizer</td><td colspan="2">Average diameter (mm)</td><td colspan="2">Maximum number</td><td rowspan="5">A</td></tr>
<tr><td colspan="2">D≦0.2</td><td colspan="2">Ignored</td></tr>
<tr><td colspan="2">0.2＜D≦0.3</td><td colspan="2">4</td></tr>
<tr><td colspan="2">0.3＜D≦0.5</td><td colspan="2">2</td></tr>
<tr><td colspan="2">0.5＜D</td><td colspan="2">Not allowed</td></tr>
<tr><td rowspan="13">1) Stains
2) Foreign Materials
3) Dark Spot</td><td colspan="5">Filamentous (Line shape)</td><td rowspan="6">A, B</td></tr>
<tr><td>Length (mm)</td><td>Width (mm)</td><td>Maximum number</td><td>Minimum space</td></tr>
<tr><td>L≦2</td><td>W≦0.03</td><td>Ignored</td><td>-</td></tr>
<tr><td>L≦3</td><td>0.03＜W≦0.05</td><td>6</td><td>-</td></tr>
<tr><td>L≦2.5</td><td>0.05＜W≦0.1</td><td>1</td><td>-</td></tr>
<tr><td>-</td><td>0.1＜W</td><td>Not allowed</td><td>-</td></tr>
<tr><td colspan="5">Round (Dot shape)</td><td rowspan="7">A, B</td></tr>
<tr><td colspan="2">Average diameter (mm)</td><td>Maximum number</td><td colspan="2">Minimum Space</td></tr>
<tr><td colspan="2">D≦0.2</td><td>Ignored</td><td colspan="2">-</td></tr>
<tr><td colspan="2">0.2＜D≦0.3</td><td>10</td><td colspan="2">10 mm</td></tr>
<tr><td colspan="2">0.3＜D≦0.4</td><td>5</td><td colspan="2">30 mm</td></tr>
<tr><td colspan="2">0.4＜D</td><td>Not allowed</td><td colspan="2">-</td></tr>
<tr><td colspan="2">In total</td><td colspan="3">Filamentous+ Round=10(Max.)</td></tr>
<tr><td colspan="5">Those wiped out easily are acceptable</td></tr>
<tr><td rowspan="4">Dot-Defect
(Note 1)</td><td colspan="2">Type</td><td>Area①</td><td>Area②</td><td>Maximum number</td><td rowspan="4">A</td></tr>
<tr><td colspan="2">Bright dot-defect</td><td>1 dot</td><td>3 dot</td><td>4 dot</td></tr>
<tr><td colspan="2">Dark dot-defect</td><td>2 dot</td><td>3 dot</td><td>5 dot</td></tr>
<tr><td colspan="2">In total</td><td>3 dot</td><td>6 dot</td><td>9 dot</td></tr>
</table>

Note 1: The Dot-Defect inspection within A zone (active area) would be divided into area ①, ② as Fig. 11.3 shown.

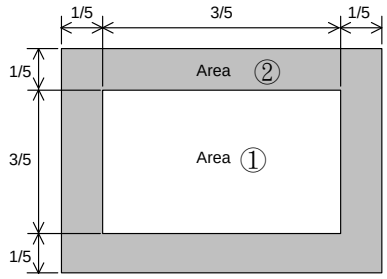


Fig. 11.3

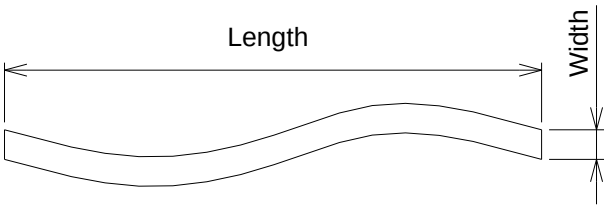
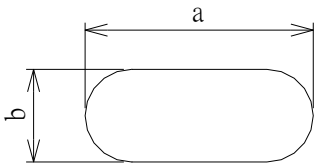


Fig 11.4



Average diameter $=\frac{a+b}{2}$

Fig 11.5

Note 2: The definitions of dot defect are as below:

- For bright dot-defect, showing black pattern, defect size over 1/2 dot area is defined.
- For dark dot-defect, showing white pattern, defect size over 1/2 dot area is defined.
- The definition of 1-dot-defect is the defect-dot, which is isolated and no adjacent defect-dot.
- The definition of adjacent dot is shown as Fig. 11.6.
- The Density of dot defect is defined in the area within diameter ϕ =10mm.

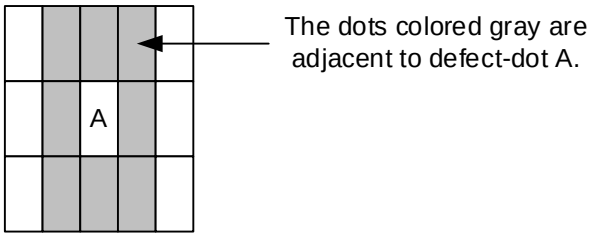


Fig. 11.6

12. PRECAUTIONS

12.1 PRECAUTIONS of ESD

1) Before handling the display, please ensure your body has been connected to ground to avoid any damages by ESD. Also, do not touch display's interface directly when assembling.

2) Please remove the protection film very slowly before turning on the display to avoid generating ESD.

12.2 PRECAUTIONS of HANDLING

1) In order to keep the appearance of display in good condition, please do not rub any surfaces of the displays by sharp tools harder than 3H, especially touch panel, metal frame and polarizer.

2) Please do not pile the displays in order to avoid any scars leaving on the display. In order to avoid any injuries, please pay more attention for the edges of glasses and metal frame, and wear finger cots to protect yourself and the display before working on it.

3) Touching the display area or the terminal pins with bare hand is prohibited. This is because it will stain the display area and cause poor insulation between terminal pins, and might affect display's electrical characteristics furthermore.

4) Do not use any harmful chemicals such as acetone, toluene, and isopropyl alcohol to clean display's surfaces.

5) Please use soft cloth or absorbent cotton with ethanol to clean the display by gently wiping. Moreover, when wiping the display, please wipe it by horizontal or vertical direction instead of circling to prevent leaving scars on the display's surface, especially polarizer.

6) Please wipe any unknown liquids immediately such as saliva, water or dew on the display to avoid color fading or any permanently damages.

7) Maximum pressure to the surface of the display must be less than 1.96×10^4 Pa. If the area of adding pressure is less than $1\,cm^2$, the maximum pressure must be less than 1.96N.

12.3 PRECAUTIONS OF OPERATING

1) Please input signals and voltages to the displays according to the values defined in the section of electrical characteristics to obtain the best performance. Any voltages over than absolute maximum rating will cause permanent damages to this display. Also, any timing of the signals out of this specification would cause unexpected performance.

2) When the display is operating at significant low temperature, the response time will be slower than it at $25\,C^\circ$. In high temperature, the color will be slightly dark and blue compared to original pattern. However, these are temperature-related phenomenon of LCD and it will not cause permanent damages to the display when used within the operating temperature.

3) The use of screen saver or sleep mode is recommended when static images are likely for long periods of time. This is to avoid the possibility of image sticking.

4) Spike noise can cause malfunction of the circuit. The recommended limitation of spike noise is no bigger than $\pm$ 100 mV.

12.4 PRECAUTIONS of STORAGE

If the displays are going to be stored for years, please be aware the following notices.

1) Please store the displays in a dark room to avoid any damages from sunlight and other sources of UV light.
2) The recommended long term storage temperature is between 10℃~35℃ and 55%~75% humidity to avoid causing bubbles between polarizer and LCD glasses, and polarizer peeling from LCD glasses.
3) It would be better to keep the displays in the container, which is shipped from JDI, and do not unpack it.
4) Please do not stick any labels on the display surface for a long time, especially on the polarizer.

13. DESIGNATION of LOT MARK

1) The lot mark is showing in Fig.13.1. First 4 digits are used to represent production lot, T represented made in Taiwan, and the last 6 digits are the serial number.

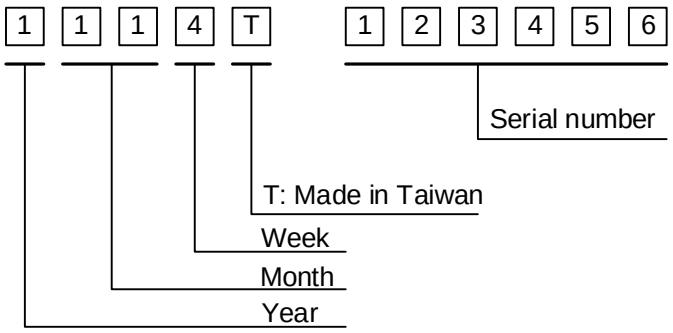


Fig. 13.1

2) The tables as below are showing what the first 4 digits of lot mark are shorted for.

Year	Lot Mark
2023	3
2024	4
2025	5
2026	6
2027	7

Month	Lot Mark	Month	Lot Mark
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

Week	Lot Mark
1~7 days	1
8~14 days	2
15~21 days	3
22~28 days	4
29~31 days	5

3) The location of the lot mark is on the back of the display shown in Fig. 14.2

Label example :

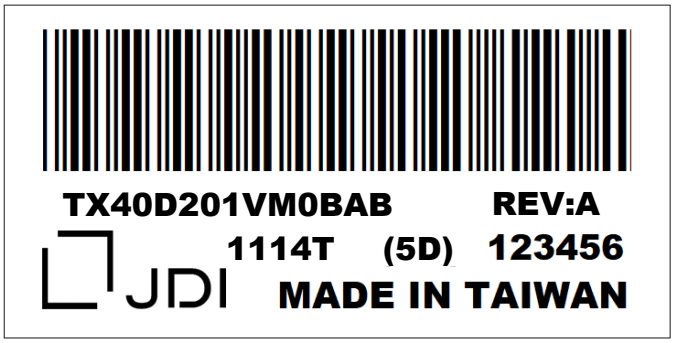


Fig. 14.2