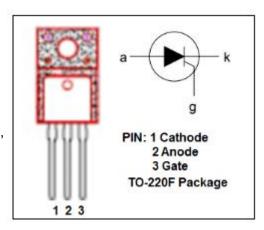


# isc Thyristors TYN616F

### **APPLICATIONS**

- It is suitable to fit all modes of control found in applications such as over voltage crowbar protection, motor control circuits in power tools and kitchen aids, in-rush current limiting circuits, Capacitive discharge ignition, voltage regulation circuits etc.
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



## ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

| SYMBOL              | PARAMETER  | MIN     | UNIT       |
|---------------------|--|---------|------------|
| $V_{DRM}$           | Repetitive peak off-state voltage                  | 600     | V          |
| $V_{RRM}$           | Repetitive peak reverse voltage                    | 600     | V          |
| I <sub>T(AV)</sub>  | Average on-stage current T <sub>C</sub> =110℃      | 10      | Α          |
| I <sub>T(RMS)</sub> | RMS on-state current T <sub>C</sub> =110 ℃         | 16      | Α          |
| P <sub>G(AV)</sub>  | Average gate power dissipation $T_j=125^{\circ}$ C | 1       | W          |
| Tj                  | Operating junction temperature                     | -40~125 | $^{\circ}$ |
| T <sub>stg</sub>    | Storage temperature                                | -40~150 | $^{\circ}$ |

## ELECTRICAL CHARACTERISTICS ( $T_C=25\,^{\circ}C$ unless otherwise specified)

| SYMBOL               | PARAMETER                         | CONDITIONS                                |                      | MIN | MAX | UNIT       |
|----------------------|-----------------------------------|---|----------------------|-----|-----|------------|
| I <sub>RRM</sub>     | Repetitive peak reverse current   | $V_{RM}=V_{RRM},R_{GK}=220\Omega$ ,       | T <sub>j</sub> =25℃  |     | 5   | μ <b>A</b> |
|                      |                                   |   | T <sub>j</sub> =125℃ |     | 2   | mA         |
| I <sub>DRM</sub>     | Repetitive peak off-state current | $V_{DM}=V_{DRM}$ , $R_{GK}=220 \Omega$    | T <sub>j</sub> =25℃  |     | 5   | μ <b>Α</b> |
|                      |                                   |   | T <sub>j</sub> =25℃  |     | 2   | mA         |
| $V_{TM}$             | On-state voltage                  | I <sub>TM</sub> = 32A                     |                      |     | 1.6 | V          |
| I <sub>GT</sub>      | Gate-trigger current              | $V_D = 12V; R_L = 33\Omega$               |                      | 2   | 25  | mA         |
| $V_{GT}$             | Gate-trigger voltage              | V <sub>D</sub> = 12V; R <sub>L</sub> =33Ω |                      |     | 1.3 | V          |
| I <sub>H</sub>       | Holding current                   | I <sub>T</sub> = 0.5A; Gate Open          |                      |     | 40  | mA         |
| R <sub>th(j-c)</sub> | Thermal resistance (DC)           | Junction to case                          |                      |     | 1.1 | °C/W       |



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