



ZHEJIANG UNIU-NE Technology CO., LTD

浙江宇力微新能源科技有限公司

U2103C-6C Data Sheet

V 1.3

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High Current IO+/- 0.4/0.6A HALF-BRIDGE DRIVER

General Description

The U2103C/U2106C Fully operated to +600V is high voltage, high speed power MOSFET and IGBT driver with dependent high and low side referenced output channels.

The logic input is compatible with standard CMOS or LSTTL output, down to 3.3V logic. The output drivers feature a high pulse current buffer stage designed for minimum driver cross-conduction. The floating channel can be used to drive an N-channel power MOSFET or IGBT in the high side configuration which operates up to 600 volts.

Product Summary

V _{OFFSET}	600V max
I _{O+/-}	0.4A / 0.6A
V _{OUT}	4.8V~20V
t _{on/off} (typ.)	420/200ns
Deadtime (typ.)	220ns
Work Tem	-40 ~150 °C

Key Features

- Integrated bootstrap Diode⁽¹⁾
- Floating channel designed for bootstrap operation
- Fully operational to +600V
- Tolerant to negative transient voltage dV/dt immune
- Gate drive supply range from 4.8 to 20V
- Undervoltage lockout
- 3.3V, 5V and 15V input logic compatible
- Cross-conduction prevention logic
- Matched propagation delay for both channels

Applications

- Home appliances
- Industrial applications and drives
- Motor drivers
- DC- AC Converter, PMDC and PMAC motors
- Induction heating
- HVAC

Packages



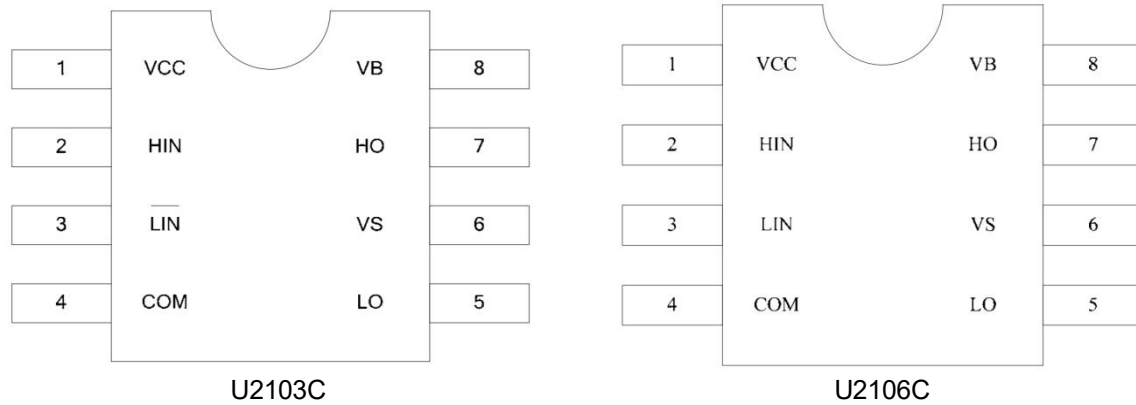
8-Lead SOP

Product information

Base Part Number	Package Type	Standard OUT		V _{OFFSET}	Logic Control
		I _{O+}	I _{O-}		
U2103C	SOP8	0.4A	0.6A	600V	HIN & $\overline{\text{LIN}}$
U2106C	SOP8	0.4A	0.6A	600V	HIN & LIN

Note: (1) When using internal diode bootstrap power supply, please match the capacitor and MOS, and fully test and verify

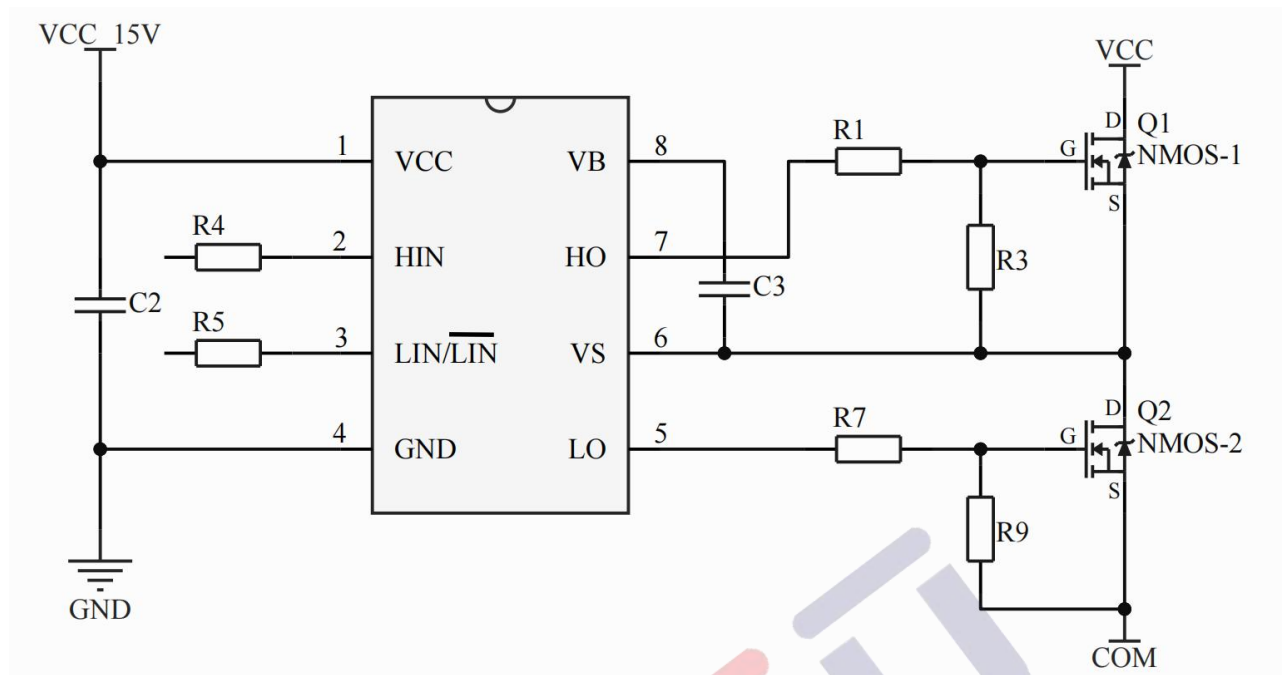
Pin Assignments



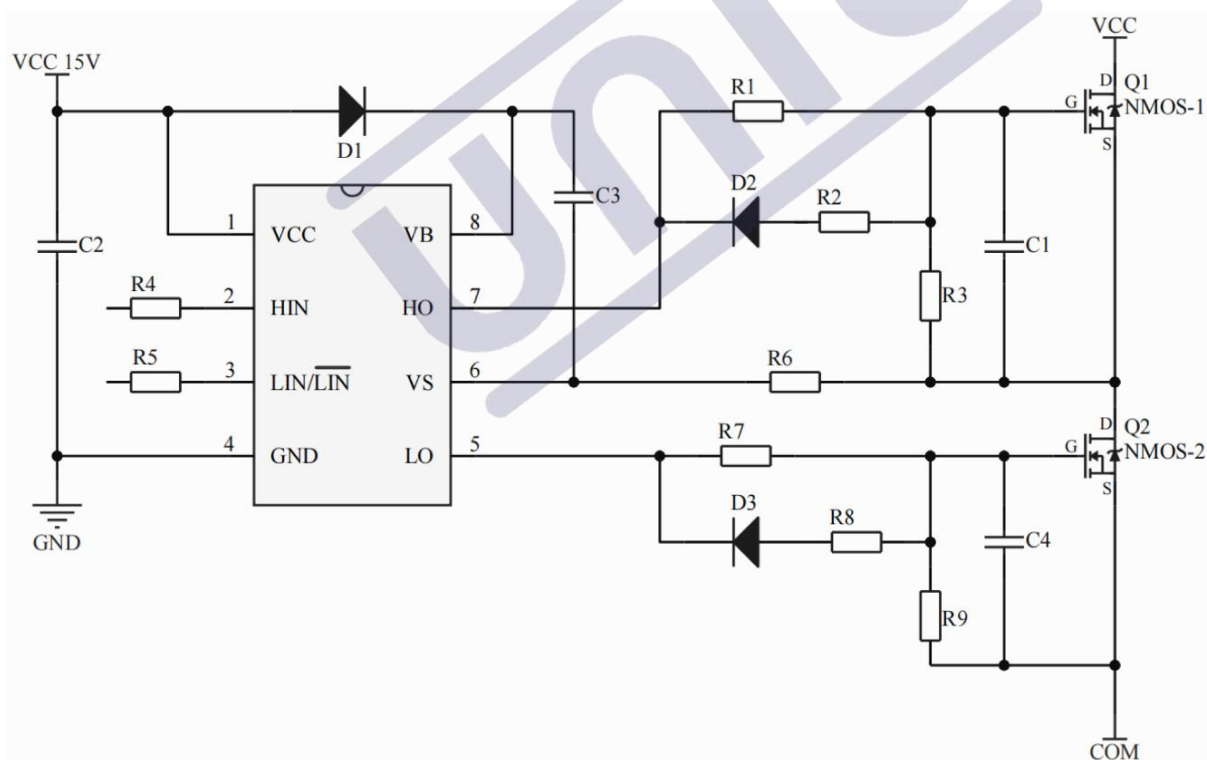
Pin Function

Number	Symbol	Description
1	VCC	Low side and logic fixed supply
2	HIN	Logic input for high side gate driver outputs (HO), in phase
3	LIN	Logic input for low side gate driver outputs (LO), in phase
	$\overline{\text{LIN}}$	Logic input for low side gate driver outputs (LO), out of phase
4	COM	Low side return
5	LO	Low side gate drive output
6	VS	High side floating supply return
7	HO	High side gate drive output
8	VB	High side floating supply

Typical Connection



Air duct circuit application diagram



Typical application

Note: 1.If Q1, Q2 use AP3N50K, it is recommended that R1, R7 resistors choose 200~300R;.
If Q1, Q2 use AP5N50K, it is recommended that R1, R7 resistors choose 100~150R.

2.If other similar MOSFET, you can refer to this parameter.

3.The above circuits and parameters are for reference only. The actual application circuit should be designed with the measured results in setting the parameters.

Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are absolute voltages referenced to COM. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions.

Symbol	Definition	Min.	Max.	Units
VB	High side floating absolute voltage	-0.3	600	V
VS	High side floating supply offset voltage	VB-25	VB+0.3	
VHO	High side floating output voltage	VS-0.3	VB+0.3	
VLO	Low side output voltage	-0.3	Vcc+0.3	
Vcc	Low side and logic fixed supply voltage	-0.3	25	
VIN	Logic input voltage (HIN & LIN)	-0.3	Vcc+0.3	
dVS/dt	Allowable offset supply voltage transient	—	55	V/ns
P _D	Package power dissipation @ TA ≤ +25°C	—	0.625	W
RthJA	Thermal resistance, junction to ambient	—	200	°C/W
TJ	Junction temperature	-35	150	°C
TS	Storage temperature	-55	175	
TL	Lead temperature (soldering, 10 seconds)	—	300	

Recommended Operating Conditions

The Input/Output logic timing diagram is shown in Figure 1. For proper operation the device should be used within the recommended conditions. The VS offset rating is tested with all supplies biased at 15V differential.

Symbol	Definition	Min.	Max.	Units
VB	High side floating supply absolute voltage	VS +4.8	VS + 20	V
VS	High side floating supply offset voltage	-25	600	
VHO	High side floating output voltage	VS	VB	
VLO	Low side output voltage	0	VCC	
VCC	Low side and logic fixed supply voltage	4.8	20	
VIN	Logic input voltage (HIN&LIN)	0	VCC	
TA	Ambient temperature	-40	125	°C

Electrical Characteristic

$V_{CC} = V_{BS} = V_{BIAS} = 15V$, $C_L = 1000pF$, $T_A = 25^\circ C$, unless otherwise specified.

Symbol	Definition	Min.	Typ.	Max.	Units	Test Conditions
tr	Turn-on rise time	—	85	—	ns	
tf	Turn-off fall time	—	40	—		
ton	Turn-on propagation delay	—	420	—		VS = 0V
toff	Turn-off propagation delay	—	200	—		VS = 400V
DT	Deadtime, LS turn-off to HS turn-on & HS turn-on to LS turn-off	—	220	—		
MT	Delay matching, HS & LS turn-on/off	—	—	70		

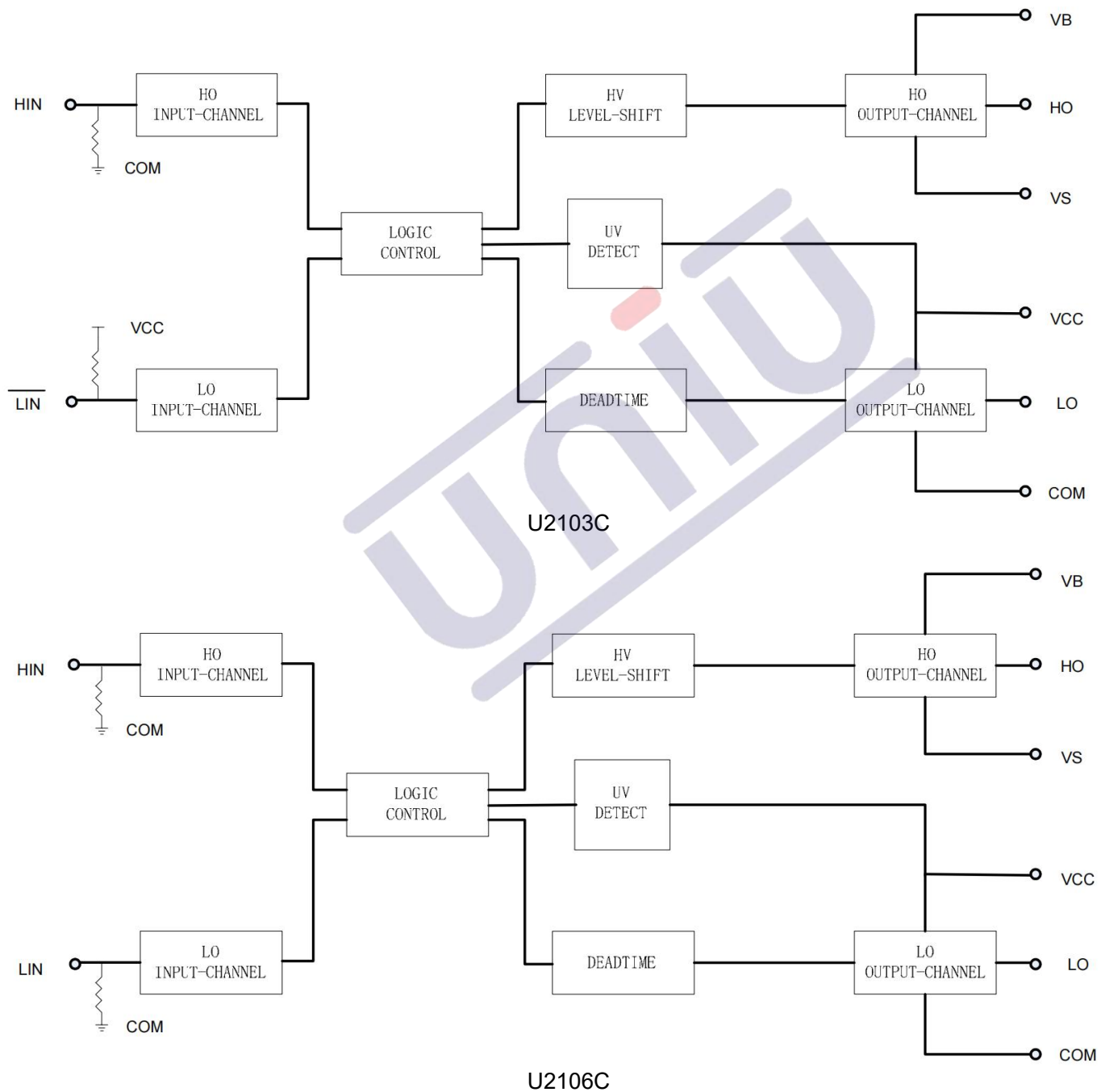
Electrical Characteristic

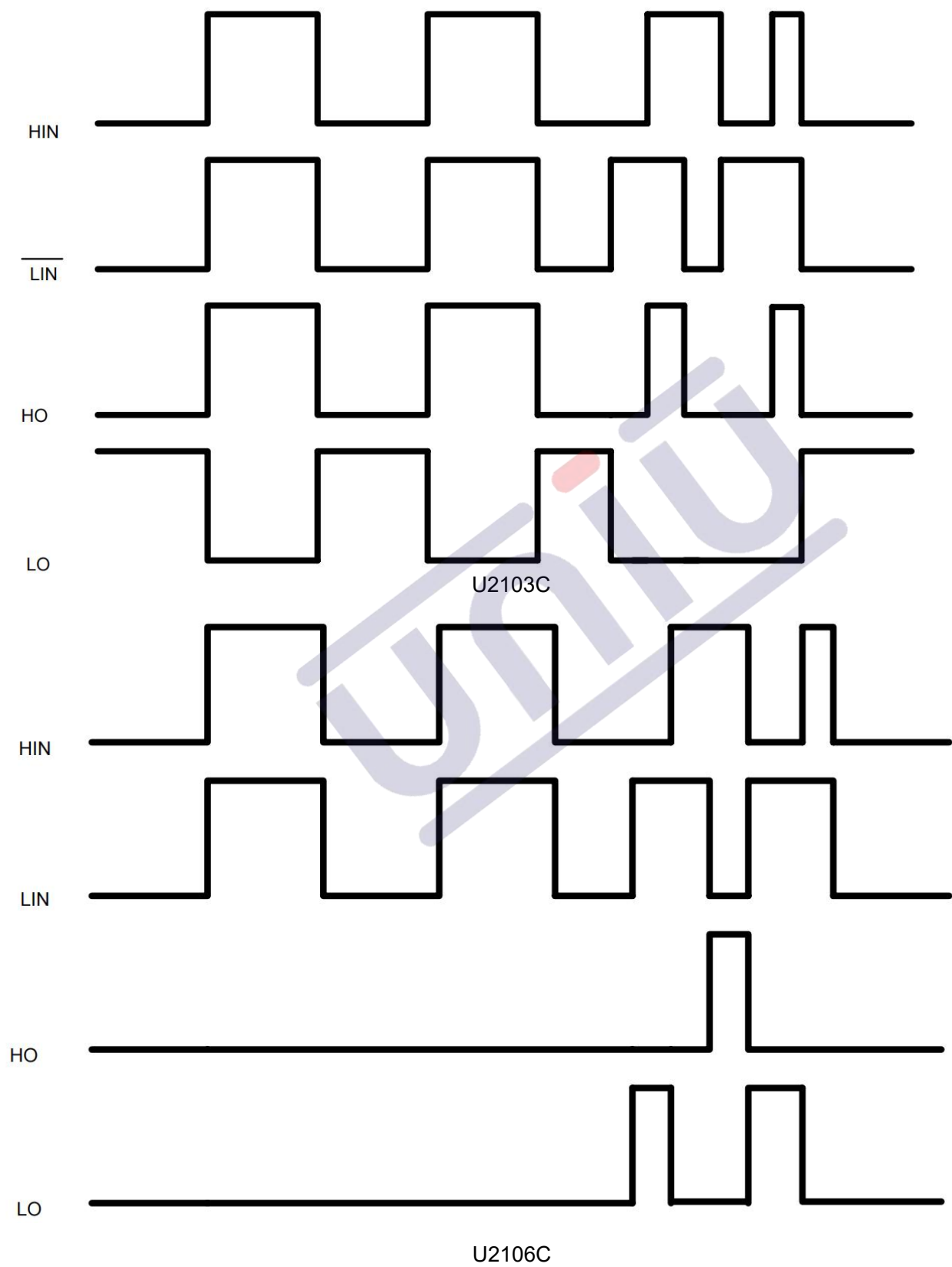
$V_{CC} = V_{BS} = V_{BIAS} = 15V$, $T_A = 25^\circ C$, unless otherwise specified.

Symbol	Definition		Min.	Typ.	Max.	Units	Test Conditions	
Low Side Power Supply Characteristics								
V _{CCUV+}	VCC supply undervoltage positive going threshold		4.0	4.7	5.2			
V _{CCUV-}	VCC supply undervoltage negative going threshold		3.8	4.5	5.0			
V _{CCHYS}	VCC supply under-voltage lockout hysteresis		0	0.2	—			
High Side Floating Power Supply Characteristics								
V _{BSUV+}	VBS supply undervoltage positive going threshold		4.0	4.7	5.2			
V _{BSUV-}	VBS supply undervoltage negative going threshold		3.8	4.5	5.0			
V _{BSUVHS}	High side VBS supply under-voltage lockout hysteresis ⁶		0	0.2	—			
IQCC	Quiescent VCC supply current	U2103C	—	60	—	uA		V _{IN} =0V or 5V
		U2106C	—	20	—			
IQBS	Quiescent VBS supply current	U2103C	—	15	—		V _{IN} =0V or 5V	
		U2106C	—	15	—			
ILK	Offset supply leakage current		—	—	3		VB =VS =400V	
VIH	Logic “1” (HO) & Logic “0” (LO) input voltage		3	—	—	V	VCC =5V to 20V	
VIL	Logic “0” (HO) & Logic “1” (LO) input voltage		—	—	0.8		VCC =5V to 20V	
IIN+	Logic “1” input bias current		—	10	—	uA	V _{IN} =5V	
IIN-	Logic “0” input bias current		—	—	1		V _{IN} =0V	

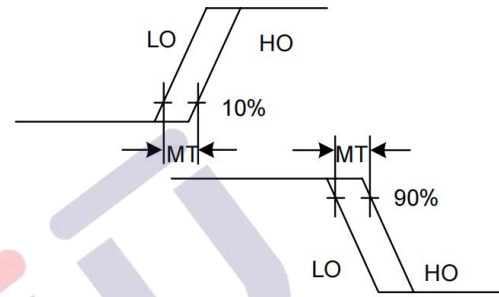
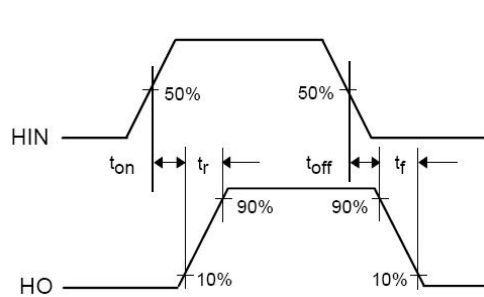
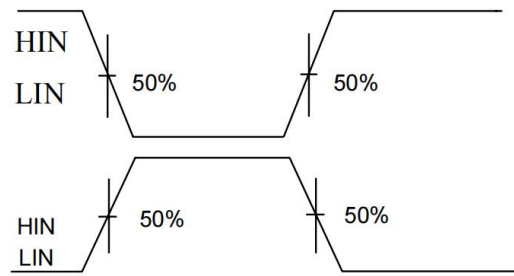
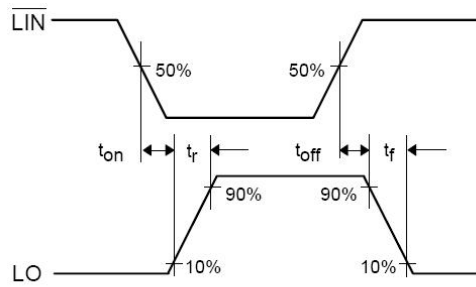
VOH	High level output voltage, $V_{BIAS} - V_O$	—	—	100	mV	$I_o = 0A$
VOL	Low level output voltage, V_O	—	—	100		$I_o = 0A$
IO+	Output high short circuit pulsed current	—	400	—	mA	$V_O = 0V, V_{IN} = V_{IH}$ $PW \leq 10 \mu s$
IO-	Output low short circuit pulsed current	—	600	—		$V_O = 15V, V_{IN} = V_{IL}$ $PW \leq 10 \mu s$

Block Diagram



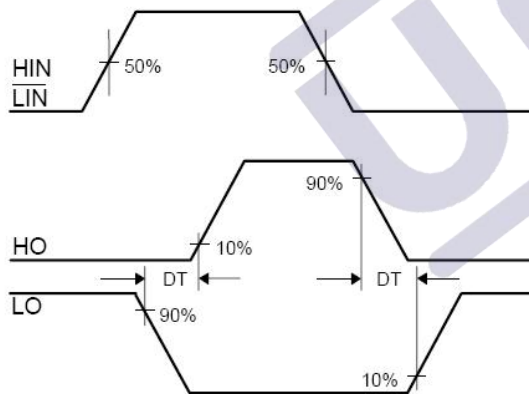
Time waveform

Time waveform



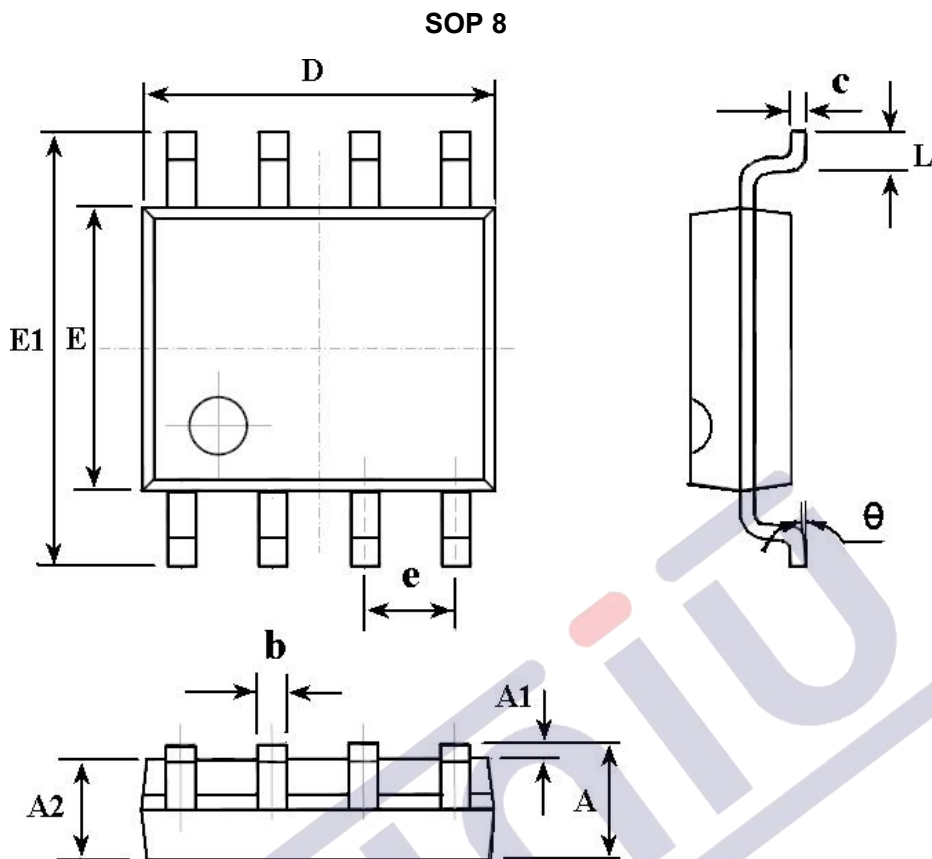
Switching Time Waveform Definitions

Delay matching time Definitions



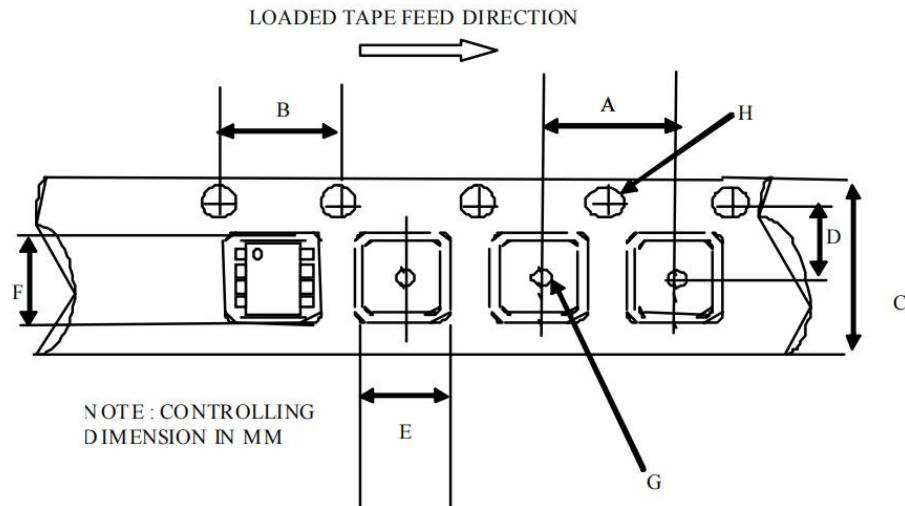
Deadtime Waveform Definitions

Packaging information



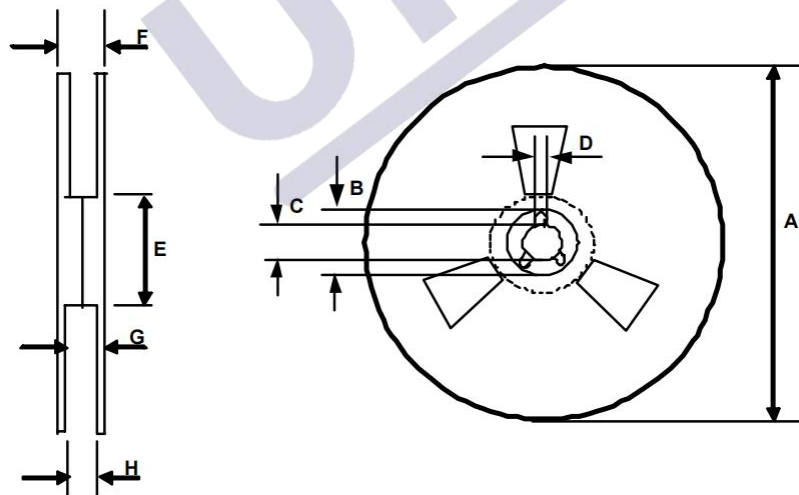
Symbol	Dimensions In Millimeters	
	Min	Max
A	1.350	1.750
A1	0.100	0.250
A2	1.350	1.550
b	0.330	0.510
c	0.170	0.250
D	4.700	5.100
E	3.800	4.000
E1	5.800	6.200
e	1.270(BSC)	
L	0.400	1.270
θ	0°	8°

Tape & Reel 8-lead SOP



CARRIER TAPE DIMENSION FOR 8SOP

Code	Metric		Imperial	
	Min	Max	Min	Max
A	7.90	8.10	0.311	0.318
B	3.90	4.10	0.153	0.161
C	11.70	12.30	0.46	0.484
D	5.45	5.55	0.214	0.218
E	6.30	6.50	0.248	0.255
F	5.10	5.30	0.200	0.208
G	1.50	n/a	0.059	n/a
H	1.50	1.60	0.059	0.062



REEL DIMENSIONS FOR 8SOP

Code	Metric		Imperial	
	Min	Max	Min	Max
A	329.60	330.25	12.976	13.001
B	20.95	21.45	0.824	0.844
C	12.80	13.20	0.503	0.519
D	1.95	2.45	0.767	0.096
E	98.00	102.00	3.858	4.015
F	n/a	18.40	n/a	0.724
G	14.50	17.10	0.570	0.673
H	12.40	14.40	0.488	0.566

1、版本记录

DATE	REV.	DESCRIPTION
2023/6/02	1.0	拆分规格书U2103和U2106
2023/10/9	1.1	更新应用原理图及应用参数
2023/12/15	1.2	新增MOS匹配参数
2024/2/15	1.3	合并U2103C和U2106C的规格书

2、免责声明

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