

VHF/ UHF-Tuner-IC

Description

This tuner IC requires a power supply of 5 V and performs the function of three separate oscillators an mixers, SAWF-driver, L.O.-output and tri-state band switch.

This tuner IC is decigned for high performance tuners for TV, VCR and PCs.

Features

- 5 V supply voltage
- Frequency range from 48 to 860 MHz
- Band A: balanced high impedance mixer input and amplitude controlled oscillator
- Band B + C: balanced low impedance mixer input and symmetrical oscillator
- Balanced L. O.-outputs for prescalers or PLL
- SAW filter driver with low impedance output
- Voltage regulator for stable operating characteristics
- ESD protection on all pins except oscillator pins and RF-inputs

Benefits

 The integration of 3 bands and the small SSO28 package allows to design very small and economical 3-band tuners with high performance.

Block Diagram

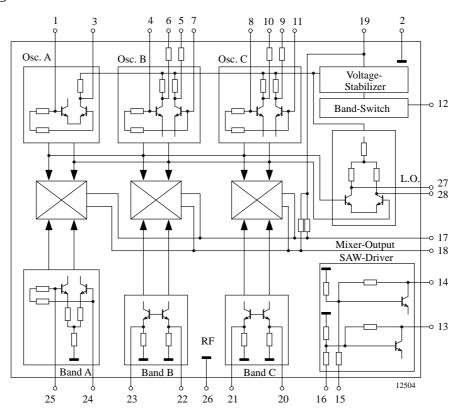


Figure 1. Block diagram

Rev. A1, 10-Sep-96

U2305B



Pin Description

1				
Osc A, base	1		28	L.O. out
GND (common)	2		27	L.O. out
Osc A, coll.	3		26	GND (RF)
Osc B, base	4		25	RF in, A
Osc B, coll.	5		24	RF in, A
Osc B, coll.	6		23	RF in, B
Osc B, base	7		22	RF in, B
Osc C, base	8		21	RF in, C
Osc C, coll.	9		20	RF in, C
Osc C, coll.	10		19	V_S
Osc C, base	11		18	Mix out
Band sw.	12		17	Mix out
SAWF, out	13		16	SAWF, inp.
SAWF, out	14		15	SAWF, inp.
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Pin	Symbol	Function
1	Osc A, base	Oscillator band A, base
2	GND (common)	Ground, common
3	Osc A, coll.	Oscillator band A, collector
4, 7	Osc B, base	Oscillator band B, bases
5, 6	Osc B, coll.	Oscillator band B, collectors
8, 11	Osc C, base	Oscillator band C, bases
9, 10	Osc C, coll.	Oscillator band C, collectors
12	Band sw.	Tri-state band switch
13, 14	SAWF, out	SAW filter driver outputs
15, 16	SAWF, inp.	SAW filter driver inputs
17, 18	Mix out	Mixer outputs, open collector
19	V_{S}	Supply voltage V _s
20, 21	RF in, C	RF inputs, band C
22, 23	RF in, B	RF inputs, band B
24, 25	RF in, A	RF inputs, band A
26	GND (RF)	Ground, RF part
27, 28	L.O. out	L.Ooutputs

Ordering Information

Extended Type Number	Package	Remarks
U2305B-AFSG3	SSO28	Taped and reeled



Absolute Maximum Ratings

All voltages are referred to GND, Pin 2

Parameters		Symbol	Min.	Тур.	Max.	Unit
Supply voltage	Pin 19	V_{S}			5.25	V
RF inputs	Pin (20-25)				V_{S}	V
IF outputs	Pin 17-18				5.5	V
Tri-state switch voltage	Pin 12	ViTRI			V_{S}	V
Junction temperature		T_{j}			125	°C
Storage temperature		T _{stg}	-40		125	°C

Operating Range

All voltages are referred to GND, Pin 2

Parameters	Test Conditions / Pins	Symbol	Min	Тур	Max	Unit
Supply voltage	Pin 17-19	V_{S}	4.5	5.0	5.25	V
Ambient temperature		T _{amb}	-25		75	°C

Thermal Resistance

Parameters	Symbol	Тур	Unit
Junction ambient			
Package SSO28 soldered to PCB (see layout page 5)	R_{thJA}	128	K/W

Electrical Characteristics

Test conditions (unless otherwise specified): $V_s = 5$ V. $T_{amb} = 25$ °C. Reference point Pin 2. Referred to test circuit page 5.

Parameters	Test Conditions / Pins	Symbol	Min.	Тур.	Max.	Unit	
Supply voltage	Pin 17-19	V_{S}	4.5	5.0	5.25	V	
Supply current	Pin 17-19	I _S		42		mA	
Band switch							
Voltage band A	Pin 12	VSWA	0		20%	V_{S}	
Voltage band B	Pin 12	VSWB	25%		50%	V_{S}	
Voltage band C	Pin 12	VSWC	55%		100%	V_{S}	
Switching current	VSW = 5 V Pin 12	ISW			100	μΑ	
LO output							
LO level each output	$RL = 50 \Omega$ Pin 27, 28	PLO	-25		-17	dBm	
SAW filter driver fi = 36 M	SAW filter driver fi = 36 MHz						
Input impedance	Pin 15, 16	ZiSAW		450		Ohm	
Output impedance	Pin 13, 14	ZoSAW		70		Ohm	
Voltage gain	Pin 15, $16 \rightarrow 13$, 14	GvSAW		17		dB	



Electrical Characteristics (continued)

Parameters	Test Conditions /	Pins	Symbol	Min	Тур	Max	Unit
Band A	•						
Input frequency range		Pin 24		48		170	MHz
Input impedance	Figure 3	Pin 24	S11A				
Gain (note 4)	Pin I	/P to O/P	GA		28		dB
Noise figure DSB (note 2)	Pin I	/P to O/P					
	fiA = 50 MHz		NF		11.5		dB
	fiA = 150 MHz		NF		12		dB
Input level for (note 3):	Each carrier						
IM3 (interm. of 3rd order	fiA = 71 MHz	Pin I/P	ViA		-23		dBm
IM2 (interm. of 2nd order)	fiA = 71 MHz	Pin I/P	ViA		-22		dBm
Band B (note 1)							
Input frequency range	Pin 22, 23		fiA	170		470	MHz
Input impedance	J	Pin 22, 23	S11B		see Fig. 3		
Gain (note 4)	Pin I	/P to O/P	GB		32		dB
Noise figure DSB (note 2)	Pin I/P to O/P						
	fiB = 200 MHz		NF		9.5		dB
	fiB = 450 MHz		NF		10		dB
Input level for (note 3):	Each carrier						
IM3 (interm. of 3rd order)	fiB = 300 MHz	Pin I/P	ViB		-28		dBm
Band C (note 1)							
Input frequency range	I	Pin 20, 21	fiC	470		860	MHz
Input impedance	Figure 3	Pin 20, 21	S11C				
Gain	Pin I	/P to O/P	GC		32		dB
Noise figure DSB (note 2)	Pin I/P to O/P						
	fiC = 500 MHZ		NF		10.5		dB
	fiC = 800 MHz		NF		11.5		dB
Input level for IM3	Each carrier		ViC		-28		dBm
(interm. of 3rd order, note 3)	fiC = 600 MHz	Pin I/P					

Notes

The RF inputs B and C are symmetrical driven by means of a hybrid for 180° phase shifting, consequently the source impedance is 100Ω . All other impedance for RF tests is 50Ω .

²⁾ The noise figure (NF) is the value for double-side-band measurement.

The intermodulation test (2-carrier-method) which is made on IF-centre is in reference to a signal-to-IM ratio of 60 dB.

Gain is the ratio of the voltage at the primary coil of L5 to the available voltage at the input.



Test and Principle Application Circuit

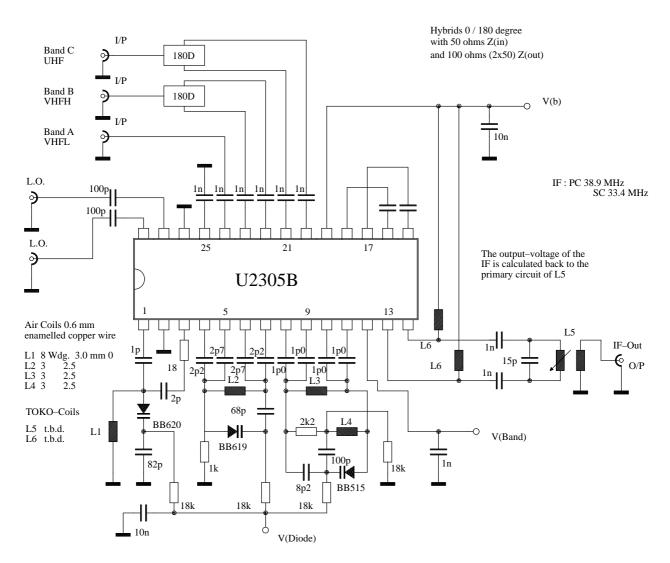


Figure 2. Test and principle application circuit

PCB for the R_{thJA} -Measurement

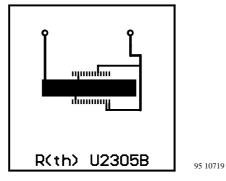


Figure 3. PCB for the R_{thJA} -measurement

Material: 35 μm one-sided Cu-coated epoxy PCB, 40 mm \times 40 mm \times 1.5 mm



Input Impedance Mixer Band A (S11A), B and C (S11B/C)

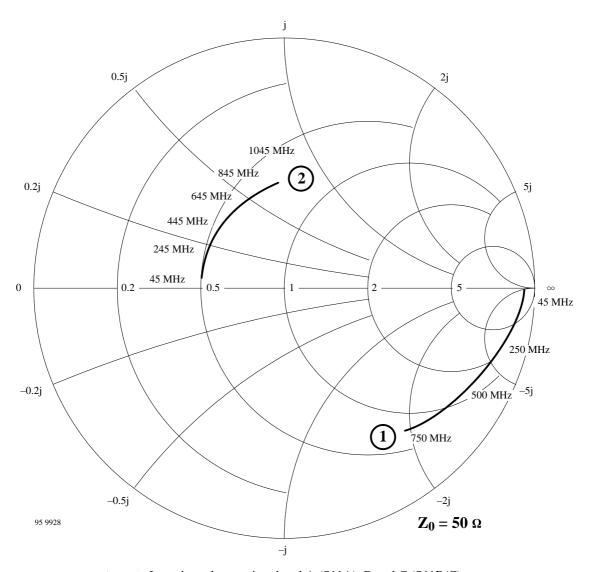


Figure 4. Input impedance mixer band A (S11A), B and C (S11B/C)

$^{1)}$ VHF-low

Normalised to 50 Ω , measuring range 45 MHz to 750 MHz.

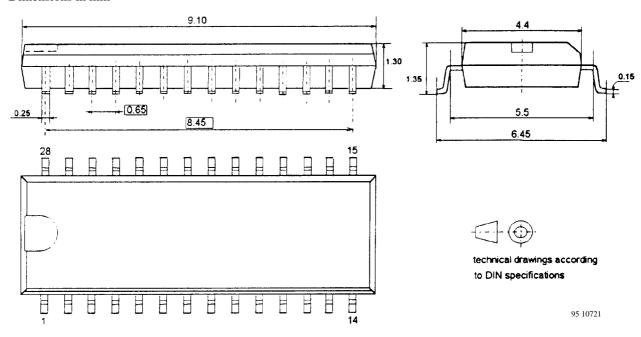
2) VHF-high and UHF

Normalised to 50 Ω , measuring range 45 MHz to 1045 MHz. Both inputs are driven symmetrical. The output impedance of hybrid is 100 Ω , the measured levels are then calculated in reference to 50 Ω .



Package Dimensions

Super small outline plastic package, 28 pin – SSO28 Dimensions in mm



U2305B



Ozone Depleting Substances Policy Statement

It is the policy of TEMIC TELEFUNKEN microelectronic GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

TEMIC TELEFUNKEN microelectronic GmbH semiconductor division has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

TEMIC can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

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TEMIC TELEFUNKEN microelectronic GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany Telephone: 49 (0)7131 67 2831, Fax number: 49 (0)7131 67 2423