

## U74AUP1G00

CMOS IC

## SINGLE 2-INPUT NAND GATE

## ■ DESCRIPTION

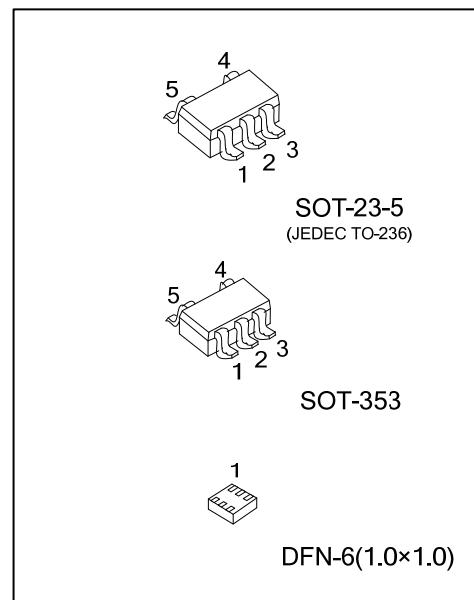
The **U74AUP1G00** is a 2-input NAND gate which provides the Function  $Y=A \cdot B$  or  $Y=A + \bar{B}$  in positive logic.

This device ensures a very low static and dynamic power consumption across the entire  $V_{CC}$  range from 0.8V to 3.6V.

This device has power-down protective circuit, preventing device destruction when it is powered down.

## ■ FEATURES

- \* Wide supply voltage range from 0.8V to 3.6V
- \* Inputs accept voltages up to 3.6V
- \*  $I_{OFF}$  supports partial-power-down mode
- \* Low static power consumption;  $I_{CC}=0.5\mu A$  (Max.)
- \* Optimized for 3.3V Operation

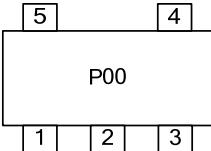
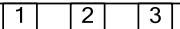


## ■ ORDERING INFORMATION

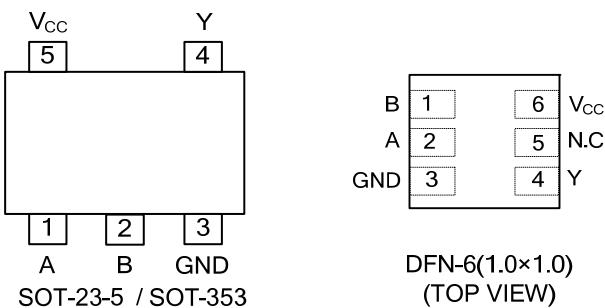
Ordering Number	Package	Packing
U74AUP1G00G-AE5-R	SOT-23-5	Tape Reel
U74AUP1G00G-AL5-R	SOT-353	Tape Reel
U74AUP1G00G-K06-1010-R	DFN-6(1.0x1.0)	Tape Reel

U74AUP1G00G-AE5-R  [Diagram showing the breakdown of the ordering code: AE5 is the packing type, R is the package type, and G is the green package indicator.]	(1) R: Tape Reel (2) AE5: SOT-23-5, AL5: SOT-353, K06-1010: DFN-6(1.0x1.0) (3) G: Halogen Free and Lead Free
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## ■ MARKING

SOT-23-5 / SOT-353	DFN-6(1.0x1.0)
SOT-23-5 / SOT-353  P00 	DFN-6(1.0x1.0)  P0 •

### ■ PIN CONFIGURATION



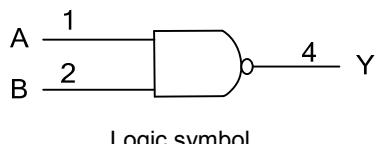
### ■ FUNCTION TABLE

INPUT(A)	INPUT(B)	OUTPUT(Y)
H	H	L
H	L	H
L	H	H
L	L	H

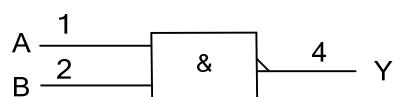
Note: H: HIGH voltage level; L: LOW voltage level.

### ■ LOGIC DIAGRAM (positive logic)

For SOT-23-5/SOT-353

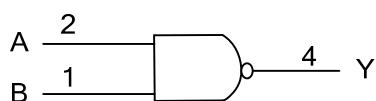


Logic symbol

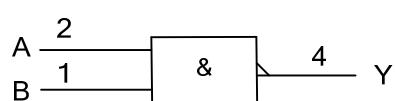


IEC logic symbol

For DFN-6(1.0×1.0)



Logic symbol



IEC logic symbol

### ■ ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	CONDITIONS	RATINGS	UNIT
Supply Voltage	V <sub>CC</sub>		-0.5 ~ +4.6	V
Input Voltage	V <sub>IN</sub>		-0.5 ~ +4.6	V
Output Voltage	V <sub>OUT</sub>	Output in the high or low state	-0.5 ~ V <sub>CC</sub> +0.5	V
		Output in the power-off state	-0.5 ~ +4.6	V
Continuous V <sub>CC</sub> or GND Current	I <sub>CC</sub>		±50	mA
Continuous Output Current	I <sub>OUT</sub>	V <sub>OUT</sub> =0 ~ V <sub>CC</sub>	±20	mA
Input Clamp Current	I <sub>IK</sub>	V <sub>IN</sub> <0	-50	mA
Output Clamp Current	I <sub>OK</sub>	V <sub>O</sub> >V <sub>CC</sub> or V <sub>OUT</sub> <0	-50	mA
Storage Temperature Range	T <sub>STG</sub>		-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### ■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V <sub>CC</sub>	Operating	0.8		3.6	V
Input Voltage	V <sub>IN</sub>		0		3.6	V
Output Voltage	V <sub>OUT</sub>	High or low state	0		V <sub>CC</sub>	V
Operating Temperature	T <sub>A</sub>		-40		85	°C
Input Transition Rise or Fall Rate	Δt/Δv	V <sub>CC</sub> =0.8V ~ 3.6V			200	ns/V

### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub> =25°C , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
High-level Input Voltage	V <sub>IH</sub>	V <sub>CC</sub> =0.8V		V <sub>CC</sub>		V
		V <sub>CC</sub> =1.1V ~ 1.95V		0.65×V <sub>CC</sub>		V
		V <sub>CC</sub> =2.3V ~ 2.7V		1.6		V
		V <sub>CC</sub> =3V ~ 3.6V		2		V
Low-level Input Voltage	V <sub>IL</sub>	V <sub>CC</sub> =0.8V			0	V
		V <sub>CC</sub> =1.1V ~ 1.95V			0.35×V <sub>CC</sub>	V
		V <sub>CC</sub> =2.3V ~ 2.7V			0.7	V
		V <sub>CC</sub> =3V ~ 3.6V			0.9	V
High-Level Output Voltage	V <sub>OH</sub>	V <sub>CC</sub> =0.8 ~ 3.6V, I <sub>OH</sub> =-20μA		V <sub>CC</sub> -0.1		V
		V <sub>CC</sub> =1.1V, I <sub>OH</sub> =-1.1mA		0.75×V <sub>CC</sub>		V
		V <sub>CC</sub> =1.4V, I <sub>OH</sub> =-1.7mA		1.11		V
		V <sub>CC</sub> =1.65V, I <sub>OH</sub> =-1.9mA		1.32		V
		V <sub>CC</sub> =2.3V	I <sub>OH</sub> =-2.3mA	2.05		V
			I <sub>OH</sub> =-3.1mA	1.9		V
		V <sub>CC</sub> =3V	I <sub>OH</sub> =-2.7mA	2.72		V
			I <sub>OH</sub> =-4mA	2.6		V
Low-Level Output Voltage	V <sub>OL</sub>	V <sub>CC</sub> =0.8 ~ 3.6V, I <sub>OH</sub> =-20μA			0.1	V
		V <sub>CC</sub> =1.1V, I <sub>OH</sub> =-1.1mA			0.3×V <sub>CC</sub>	V
		V <sub>CC</sub> =1.4V, I <sub>OH</sub> =-1.7mA			0.31	V
		V <sub>CC</sub> =1.65V, I <sub>OH</sub> =-1.9mA			0.31	V
		V <sub>CC</sub> =2.3V	I <sub>OH</sub> =2.3mA		0.31	V
			I <sub>OH</sub> =3.1mA		0.44	V
		V <sub>CC</sub> =3V	I <sub>OH</sub> =2.7mA		0.31	V
			I <sub>OH</sub> =4mA		0.44	V

## ■ ELECTRICAL CHARACTERISTICS (Cont.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Leakage Current	$I_{I(LEAK)}$	$V_{CC}=0 \sim 3.6V, V_{IN}=GND \sim 3.6V$			$\pm 0.1$	$\mu A$
Power OFF Leakage Current	$I_{off}$	$V_{CC}=0V, V_{IN} \text{ or } V_{OUT}=0 \sim 3.6V$			$\pm 0.2$	$\mu A$
Additional Power OFF Leakage Current	$\Delta I_{off}$	$V_{CC}=0V \sim 0.2V, V_{IN} \text{ or } V_{OUT}=0 \sim 3.6V$			$\pm 0.2$	$\mu A$
Quiescent Supply Current	$I_{cc}$	$V_{CC}=0.8 \sim 3.6V, V_{IN}=V_{CC} \text{ or } GND, I_{OUT}=0$			0.5	$\mu A$
Additional Quiescent Supply Current Per Input Pin	$\Delta I_{cc}$	$V_{CC}=3.3V, V_{IN}=V_{CC}-0.6V, I_{OUT}=0$			40	$\mu A$
Input Capacitance	$C_I$	$V_{CC}=0V, V_{IN}=V_{CC} \text{ or } GND$ $V_{CC}=3.6V, V_{IN}=V_{CC} \text{ or } GND$		1.5		$pF$
Output Capacitance	$C_{OUT}$	$V_{CC}=0V, V_{OUT}=GND$		3		$pF$

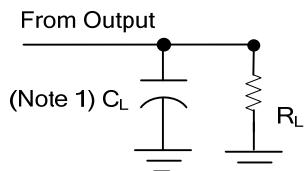
■ SWITCHING CHARACTERISTICS ( $T_A = 25^\circ C$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Propagation delay from input (A or B) to output(Y)	$t_{PLH} / t_{PHL}$	$C_L=5pF$ $R_L=1M\Omega$	$V_{CC}=0.8V$	16.6		ns
			$V_{CC}=1.2 \pm 0.1V$	2.6	7	ns
			$V_{CC}=1.5 \pm 0.1V$	2.9	5	ns
			$V_{CC}=1.8 \pm 0.15V$	2	4	ns
			$V_{CC}=2.5 \pm 0.2V$	1.3	2.9	ns
			$V_{CC}=3.3 \pm 0.3V$	1	2.4	ns
		$C_L=10pF$ $R_L=1M\Omega$	$V_{CC}=0.8V$	18.9		ns
			$V_{CC}=1.2 \pm 0.1V$	1.5	8	ns
			$V_{CC}=1.5 \pm 0.1V$	2.9	5.8	ns
			$V_{CC}=1.8 \pm 0.15V$	2	4.7	ns
			$V_{CC}=2.5 \pm 0.2V$	1.3	3.4	ns
			$V_{CC}=3.3 \pm 0.3V$	1	2.9	ns
		$C_L=15pF$ $R_L=1M\Omega$	$V_{CC}=0.8V$	21.3		ns
			$V_{CC}=1.2 \pm 0.1V$	3.6	9	ns
			$V_{CC}=1.5 \pm 0.1V$	2.9	6.5	ns
			$V_{CC}=1.8 \pm 0.15V$	2	5.3	ns
			$V_{CC}=2.5 \pm 0.2V$	1.3	3.9	ns
			$V_{CC}=3.3 \pm 0.3V$	1	3.3	ns
		$C_L=30pF$ $R_L=1M\Omega$	$V_{CC}=0.8V$	28.4		ns
			$V_{CC}=1.2 \pm 0.1V$	4.9	11.9	ns
			$V_{CC}=1.5 \pm 0.1V$	2.9	8.6	ns
			$V_{CC}=1.8 \pm 0.15V$	2	7.1	ns
			$V_{CC}=2.5 \pm 0.2V$	1.3	5.3	ns
			$V_{CC}=3.3 \pm 0.3V$	1	4.5	ns

■ OPERATING CHARACTERISTICS ( $f=10MHz, T_A = 25^\circ C$ , unless otherwise specified)

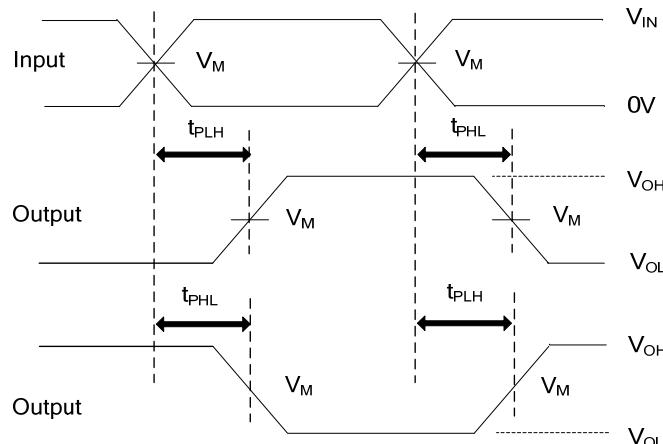
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Dissipation Capacitance	$C_{PD}$	$V_{CC}=0.8V$		4		$pF$
		$V_{CC}=1.2 \pm 0.1V$		4		$pF$
		$V_{CC}=1.5 \pm 0.1V$		4		$pF$
		$V_{CC}=1.8 \pm 0.15V$		4		$pF$
		$V_{CC}=2.5 \pm 0.2V$		4		$pF$
		$V_{CC}=3.3 \pm 0.3V$		4		$pF$

■ TEST CIRCUIT AND WAVEFORMS



TEST CIRCUIT

$V_{CC}$	$V_{IN}$	$t_R / t_F$	$V_M$	$C_L$	$R_L$
0.8V	$V_{CC}$	$\leq 3\text{ns}$	$V_{CC}/2$	5,10,15,30pF	$1\text{M}\Omega$
$1.2V \pm 0.1V$	$V_{CC}$	$\leq 3\text{ns}$	$V_{CC}/2$	5,10,15,30pF	$1\text{M}\Omega$
$1.5V \pm 0.1V$	$V_{CC}$	$\leq 3\text{ns}$	$V_{CC}/2$	5,10,15,30pF	$1\text{M}\Omega$
$1.8V \pm 0.15V$	$V_{CC}$	$\leq 3\text{ns}$	$V_{CC}/2$	5,10,15,30pF	$1\text{M}\Omega$
$2.5V \pm 0.2V$	$V_{CC}$	$\leq 3\text{ns}$	$V_{CC}/2$	5,10,15,30pF	$1\text{M}\Omega$
$3.3V \pm 0.3V$	$V_{CC}$	$\leq 3\text{ns}$	$V_{CC}/2$	5,10,15,30pF	$1\text{M}\Omega$



PROPAGATION DELAY TIMES

Notes: 1.  $C_L$  includes probe and jig capacitance.

2. All input pulses are supplied by generators having the following characteristics: PRR  $\leq 10\text{MHz}$ ,  $Z_O = 50\Omega$ .

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