

μ A104 • μ A304

NEGATIVE VOLTAGE REGULATORS

FAIRCHILD LINEAR INTEGRATED CIRCUITS

Muller SE

GENERAL DESCRIPTION — The 104 family of Precision Negative Voltage Regulators is constructed using the Fairchild Planar* epitaxial process. This device can be programmed by a single external resistor to supply any voltage from 0 V to 30 V from a single unregulated supply. When used with a separate floating bias supply, the 104/304 can provide 0.01% regulation with the output voltage limited only by the breakdown of external pass transistors. The 104 and 304 provide complementary operation with the 105 positive regulator family. Although primarily designed as a linear series regulator, the 104 family can be used as a current regulator, switching regulator, or in control applications. Without external pass elements, the device can supply currents up to 25 mA; with external pass transistors, the output current is limited only by the capacity of the pass transistors. External resistors establish the output voltage and either constant or fold-back current limiting.

- 1 mV REGULATION WITH FULL LOAD
- 0.01%/V LINE REGULATION
- 0.2 mV/V RIPPLE REJECTION
- 0.3% TEMPERATURE STABILITY OVER FULL TEMPERATURE RANGE

ABSOLUTE MAXIMUM RATINGS

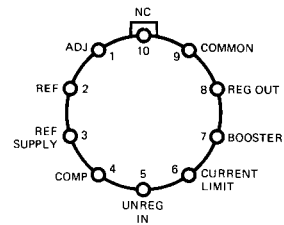
Input Voltage	
μ A104	50 V
μ A304	40 V
Input/Output Voltage Differential	
μ A104	50 V
μ A304	40 V
Power Dissipation (Note 1)	500 mW
Operating Temperature Range	
Military grade (μ A104)	-55°C to +125°C
Commercial grade (μ A304)	0°C to +70°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (Soldering, 10 s)	300°C

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CONNECTION DIAGRAM

10-LEAD METAL CAN

(TOP VIEW)



Note: Pin 5 connected to case.

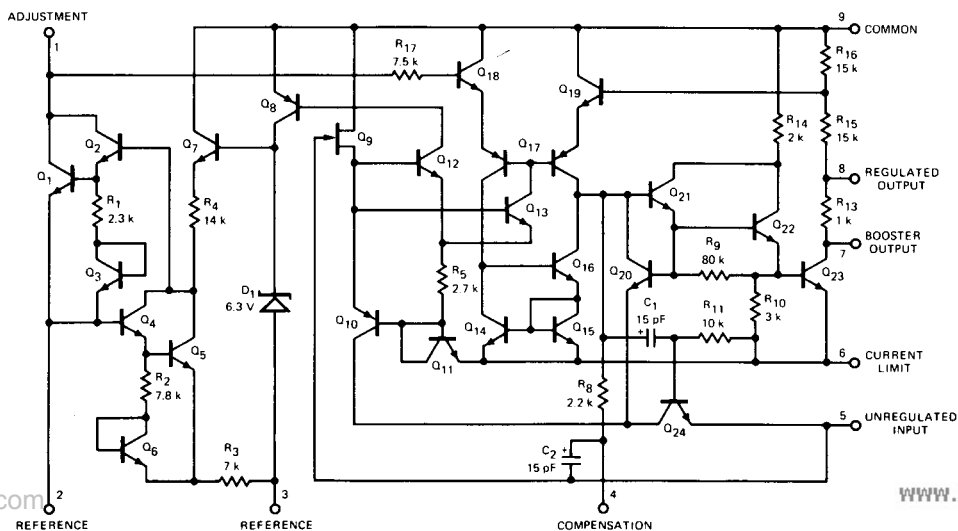
ORDER INFORMATION

TYPE	PART NO.
μ A104	μ A104HM
μ A304	μ A304HC

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EQUIVALENT CIRCUIT



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Notes on following pages.

*Planar is a patented Fairchild process.

μ A104**ELECTRICAL CHARACTERISTICS:** $V_{IN} = -40$ V to -8.0 V, $T_A = 0^\circ$ C to 70° C, unless otherwise specified, Note 2

CHARACTERISTICS	CONDITIONS	MIN	TYP	MAX	UNITS
Input Voltage Range		-50		-8.0	V
Output Voltage Range		-40		-0.015	V
Output/Input Voltage Differential (Note 3)	$I_L = 20$ mA	2.0		50	V
	$I_L = 5$ mA	0.5		50	V
Load Regulation (Note 4)	$0 \leq I_L \leq 20$ mA, $R_{SC} = 15 \Omega$		1.0	5.0	mV
Line Regulation (Note 5)	$V_O \leq -5$ V, $\Delta V_{IN} = 0.1 V_{IN}$		0.056	0.1	%
Ripple Rejection	$C_2 = 10 \mu$ F, $f = 120$ Hz, $V_{IN} \geq -15$ V		0.2	0.5	mV/V
	-7 V $\geq V_{IN} \geq -15$ V		0.5	1.0	mV/V
Output Voltage Scale Factor V_O/R_2	$R_1 = 2.4$ k Ω	1.8	2.0	2.2	V/k Ω
Temperature Stability	$V_O \leq -1$ V, -55° C $\leq T_A \leq 125^\circ$ C		0.3	1.0	%
Output Noise Voltage	10 Hz $\leq f \leq 10$ kHz, $C_2 = 0$		0.007		%
	$V_O \leq -5$ V, $C_2 = 10 \mu$ F		15		μ V
Standby Current Drain	$I_L = 5$ mA, $V_O = 0$		1.7	2.5	mA
	$V_O = -40$ V		3.6	5.0	mA
Long Term Stability	$V_O \leq -1$ V		0.1	1.0	%

 μ A304**ELECTRICAL CHARACTERISTICS:** $V_{IN} = -50$ V to -8.0 V, $T_A = -55^\circ$ C to 125° C, unless otherwise specified, Note 2

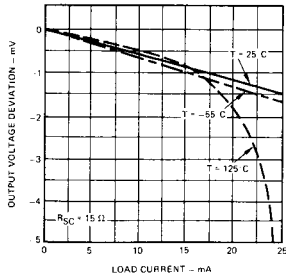
CHARACTERISTICS	CONDITIONS	MIN	TYP	MAX	UNITS
Input Voltage Range		-40		-8.0	V
Output Voltage Range		-30		-0.035	V
Output/Input Voltage Differential (Note 3)	$I_L = 20$ mA	2.0		40	V
	$I_L = 5$ mA	0.5		40	V
Load Regulation (Note 4)	$0 \leq I_L \leq 20$ mA, $R_{SC} = 15 \Omega$		1.0	5.0	mV
Line Regulation (Note 5)	$V_O \leq -5$ V, $\Delta V_{IN} = 0.1 V_{IN}$		0.056	0.1	%
Ripple Rejection	$C_2 = 10 \mu$ F, $f = 120$ Hz, $V_{IN} < -15$ V		0.2	0.5	mV/V
	-7 V $\geq V_{IN} \geq -15$ V		0.5	1.0	mV/V
Output Voltage Scale Factor V_O/R_2	$R_1 = 2.4$ k Ω	1.8	2.0	2.2	V/k Ω
Temperature Stability	$V_O \leq -1$ V, 0° C $\leq T_A \leq 70^\circ$ C		0.3	1.0	%
Output Noise Voltage	10 Hz $\leq f \leq 10$ kHz, $C_2 = 0$		0.007		%
	$V_O \leq -5$ V, $C_2 = 10 \mu$ F		15		μ V
Standby Current Drain	$I_L = 5$ mA, $V_O = 0$		1.7	2.5	mA
	$V_O = -30$ V		3.6	5.0	mA
Long Term Stability	$V_O \leq -1$ V		0.1	1.0	%

NOTES:

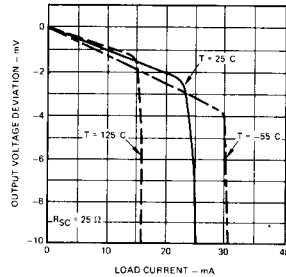
- Rating applies to ambient temperatures up to 70° C. Above 70° C ambient derate linearly at 6.3 mW/ $^\circ$ C.
- The load and line regulation specifications are for constant junction temperature. Temperature drift effects must be taken into account separately when the unit is operating under conditions of high dissipation. See Basic Regulator Circuit.
- When external booster transistors are used, the minimum output-input voltage differential is increased, in the worst case, by approximately 1 V.
- The output currents given, as well as the load regulation, can be increased by the addition of external transistors. The improvement factor will be roughly equal to the composite current gain of the added transistors.
- With zero output, the dc line regulation is determined from the ripple rejection. Hence, with output voltages between 0 V and -5 V, a dc output variation, determined from the ripple rejection, must be added to find the worst-case line regulation.

TYPICAL PERFORMANCE CURVES FOR $\mu A104$

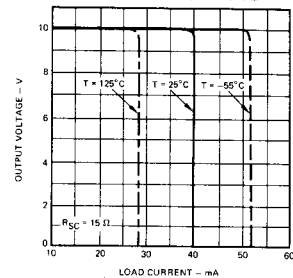
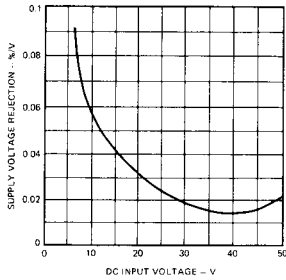
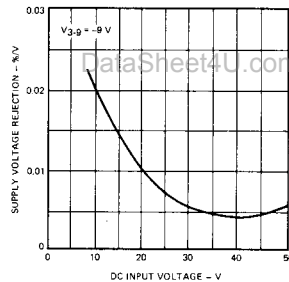
LOAD REGULATION



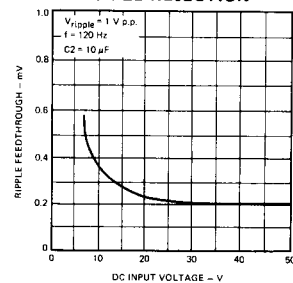
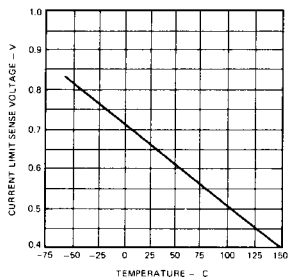
LOAD REGULATION



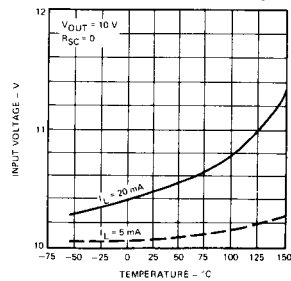
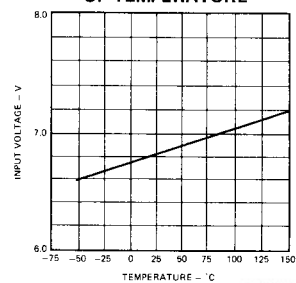
CURRENT LIMITING

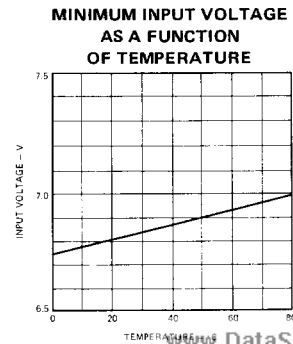
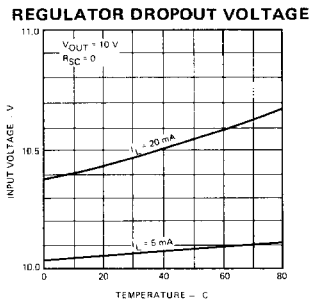
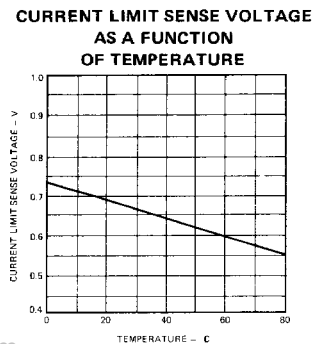
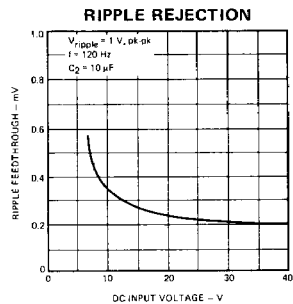
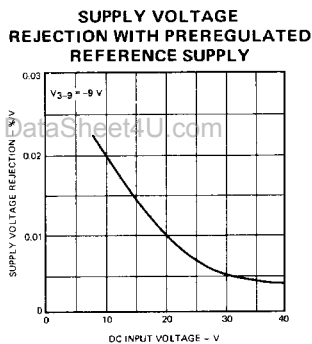
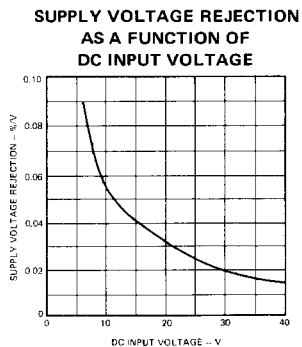
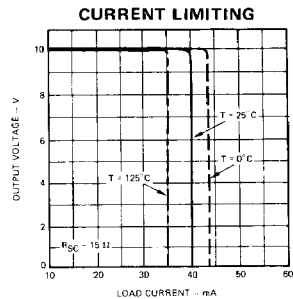
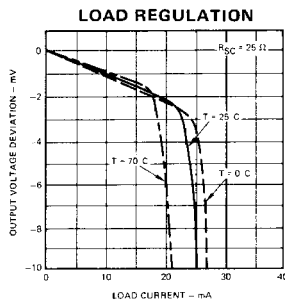
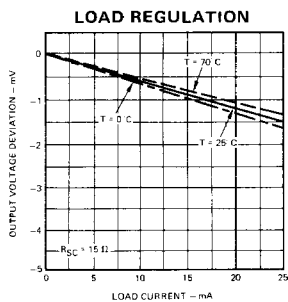
SUPPLY VOLTAGE REJECTION
AS A FUNCTION OF
DC INPUT VOLTAGESUPPLY VOLTAGE
REJECTION WITH PREREGULATED
REFERENCE SUPPLY

RIPPLE REJECTION

CURRENT LIMIT SENSE VOLTAGE
AS A FUNCTION
OF TEMPERATURE

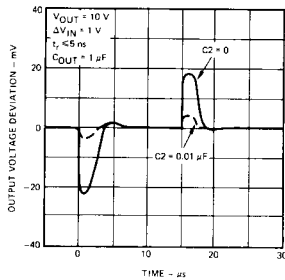
REGULATOR DROPOUT VOLTAGE

MINIMUM INPUT VOLTAGE
AS A FUNCTION
OF TEMPERATURE

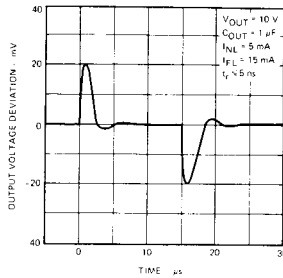
TYPICAL PERFORMANCE CURVES FOR μ A304

TYPICAL PERFORMANCE CURVES FOR μ A104 AND μ A304

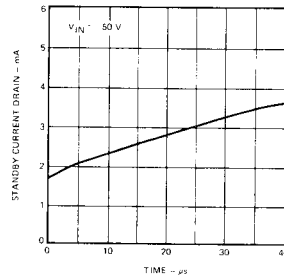
LINE TRANSIENT RESPONSE



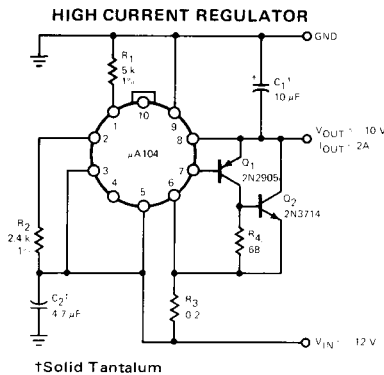
LOAD TRANSIENT RESPONSE



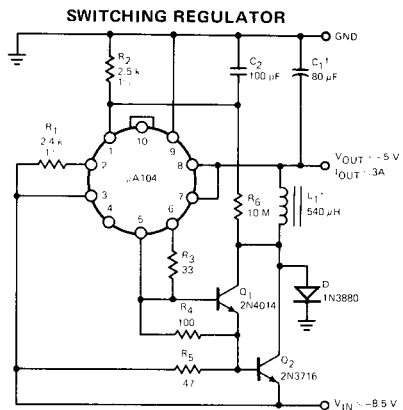
STANDBY CURRENT DRAIN AS A FUNCTION OF TIME



TYPICAL APPLICATIONS

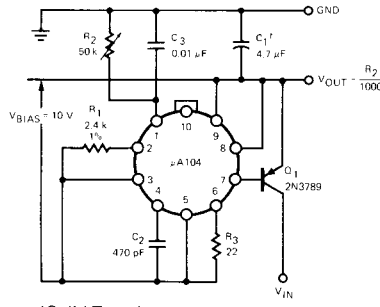


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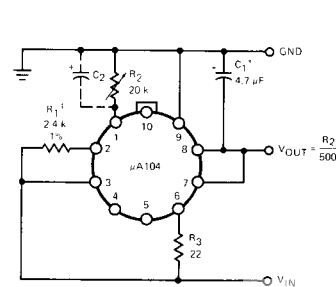


†Solid Tantalum
*60 Turns #20 on Arnold Engineering A930157-2 Molybdenum Permalloy Core.

OPERATING WITH SEPARATE BIAS SUPPLY



BASIC REGULATOR CIRCUIT



†Solid Tantalum
‡Trim R_1 for exact scale factor

NOTE:

A 0.01 μ F capacitor may be required across the input if long leads are used from the unregulated power source. Line transient response, noise and ripple rejection can be improved by shunting R_2 with a 10 μ F capacitor C_2 .