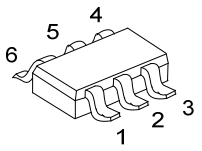
UA6K

DUAL TRANSISTOR

GENERAL PURPOSE
(DUAL DIGITAL TRANSISTOR)



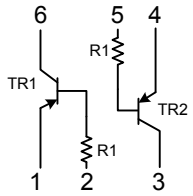
SOT-363

*Pb-free plating product number: UA6KL

■ FEATURES

* Two DTA144T chips in a SOT-363 package.
* Mounting cost and area can be cut in half.

■ EQUIVALENT CIRCUIT

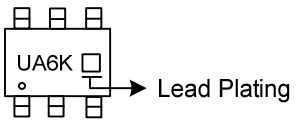


■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment						Packing
Normal	Lead Free Plating		1	2	3	4	5	6	
UA6K-AL6-R	UA6KL-AL6-R	SOT-363	E1	B1	C2	E2	B2	C1	Tape Reel

UA6KL-AL6-R (1)Packing Type (2)Package Type (3)Lead Plating	(1) R: Tape Reel (2) AL6: SOT-363 (3) L: Lead Free Plating, Blank: Pb/Sn

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Base Voltage	V_{CBO}	-50	V
Collector-Emitter Voltage	V_{CEO}	-50	V
Emitter-Base Voltage	V_{EBO}	-5	V
Collector Current	I_C	-100	mA
Collector Power dissipation	P_C	200	mW
Junction Temperature	T_J	150	°C
Storage Temperature	T_{STG}	-55~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-base breakdown voltage	BV_{CBO}	I_C =-50μA	-50			V
Collector-emitter breakdown voltage	BV_{CEO}	I_C =-1mA	-50			V
Emitter-base breakdown voltage	BV_{EBO}	I_E =-50μA	-5			V
Collector cutoff current	I_{CBO}	V_{CB}=-50V			-0.5	μA
Emitter cutoff current	I_{EBO}	V_{EB}=-4V			-0.5	μA
Collector-emitter saturation voltage	$V_{CE(SAT)}$	I_C =-5mA, I_B = -0.5mA			-0.3	V
DC current transfer ratio	h_{FE}	V_{CE}=-5V, I_C = -1mA	100	250	600	
Transition frequency	f_T	V_{CE}=-10V, I_E=5mA, f=100MHz (Note)		250		MHz
Input resistance	R1		32.9	47	61.1	kΩ

Note: Transition frequency of the device

■ TYPICAL CHARACTERISTICS