

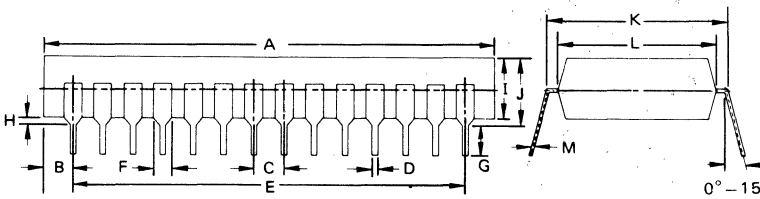
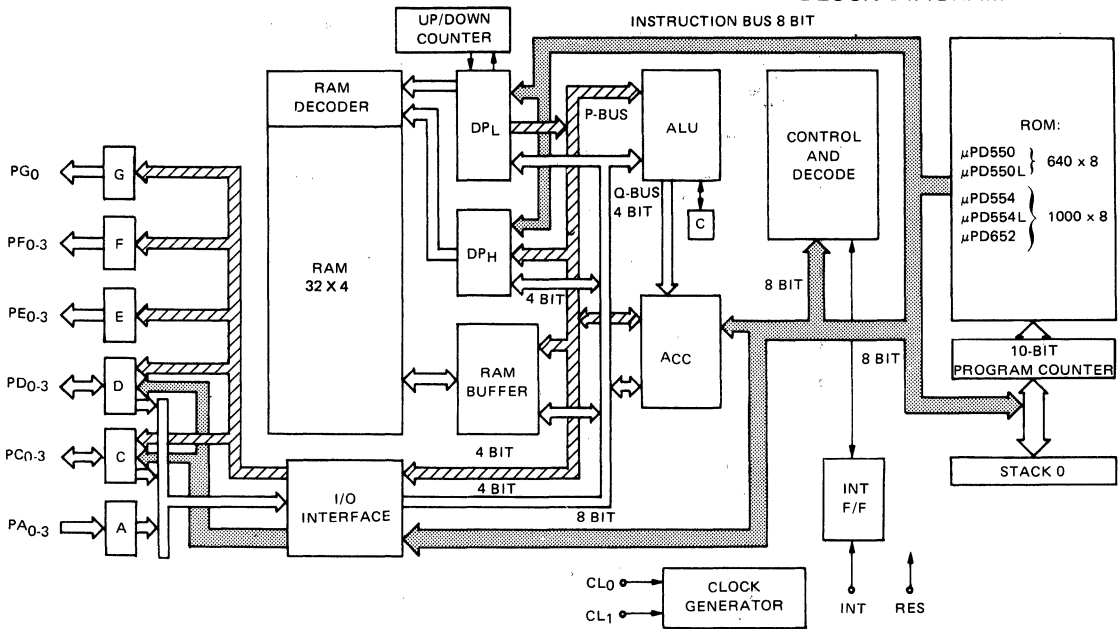
4-BIT SINGLE CHIP MICROCOMPUTERS

DESCRIPTION The μCOM-45 4-bit single chip microcomputers described below comprise the lower-performance portion of the μCOM-4 Microcomputer Family. They are distinguished from other μCOM-4 products by their smaller ROM and RAM, and their reduced 21 line I/O capability.

- FEATURES**
- 1000 x 8 ROM (μPD550 and μPD550L have 640 x 8 ROM)
 - 32 x 4 RAM
 - 10 μs Instruction Cycle Time, Typical
 - 58 Powerful Instructions
 - Table Look-Up Capability with CZP and JPA Instructions
 - Single Bit Manipulation of RAM or I/O Ports
 - 1-Level Subroutine Stack
 - Extensive I/O Capability
 - One 4-Bit Input Ports
 - Two 4-Bit I/O Ports
 - Two 4-Bit Output Ports
 - One 1-Bit Output Port
 - Software Testable Interrupt
 - Built-In Clock Signal Generation Circuitry
 - Built-In RESET Circuitry
 - Single Power Supply
 - Low Power Consumption
 - PMOS or CMOS Technologies
 - 28-Pin Plastic DIP
 - Choice of 5 Different Products to Suit a Variety of Applications

Part #	Technology	Power Supply	Package	Features
μPD550	PMOS	-10V	28-pin DIP	35V Vacuum Fluorescent Display Drive
μPD550L	PMOS	-8V	28-pin DIP	
μPD554	PMOS	-10V	28-pin DIP	
μPD554L	PMOS	-8V	28-pin	
μPD652	CMOS	-15V	28-pin	

**μCOM-45
BLOCK DIAGRAM**



**μCOM-45 PACKAGE
OUTLINE**

28-PIN DIP
 μPD550C
 μPD550LC
 μPD554C
 μPD554LC
 μPD652C

ITEM	MILLIMETERS	INCHES
A	38.0 MAX.	1.496 MAX.
B	2.49	0.098
C	2.54	0.10
D	0.5 ± 0.1	0.02 ± 0.004
E	33.02	1.3
F	1.5	0.059
G	2.54 MIN.	0.10 MIN.
H	0.5 MIN.	0.02 MIN.
I	5.22 MAX.	0.205 MAX.
J	5.72 MAX.	0.225 MAX.
K	15.24	0.6
L	13.2	0.52
M	0.25 +0.10 -0.05	0.01 +0.004 -0.002