

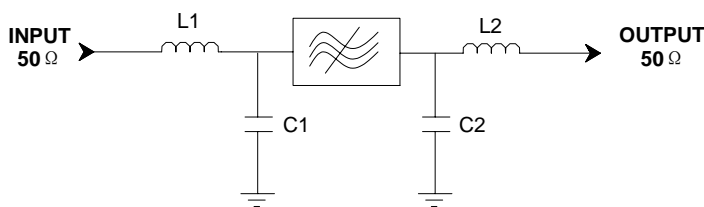
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	279	280	281
Insertion Loss	dB	-	2.4	4.5
1 dB Bandwidth	MHz	8	9.6	-
3 dB Bandwidth	MHz	10	11	-
40 dB Bandwidth	MHz	-	30	35
Passband Variation	dB	-	0.5	1
Ultimate Rejection ($f_0-100\text{MHz} \sim f_0-37.5\text{MHz}$)	dB	50	53	-
Ultimate Rejection ($f_0+100\text{MHz} \sim f_0+37.5\text{MHz}$)	dB	50	56	-
Material Temperature coefficient	KHz/°C	-22.96		
Ambient Temperature	°C	25		
Package Size	F-11			

Notes:


1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration

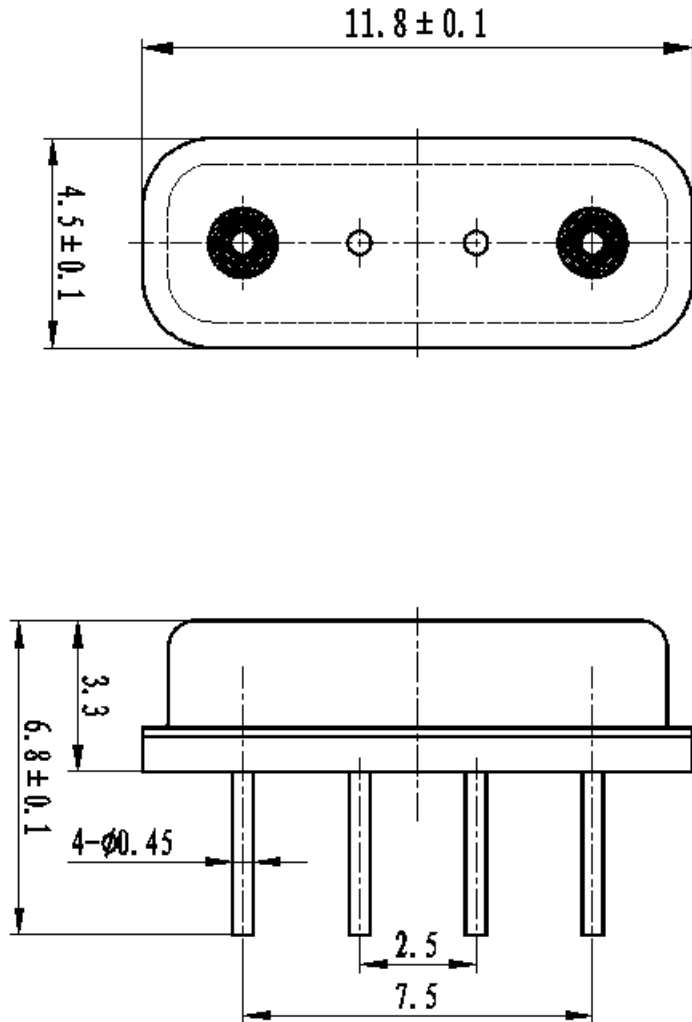


L1=L2=33nH
C1=2.8pF C2=2.8pF
Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.

	SIPAT Co., Ltd. (CETC No. 26 Research Institute) Nanping Huayuan Road No. 14 Chongqing, China, 400060	Part Number	UE280
		Rev. Date	2006-5-23
		Rev.	1.0

Package Dimension



Package: F-11

Unit: mm

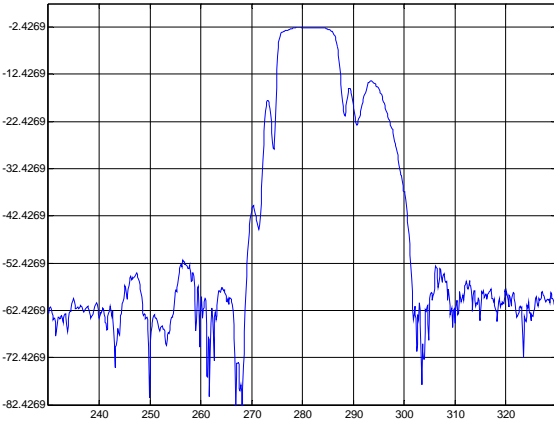


SIPAT Co., Ltd.
(CETC No. 26 Research Institute)
Nanping Huayuan Road No. 14
Chongqing, China, 400060

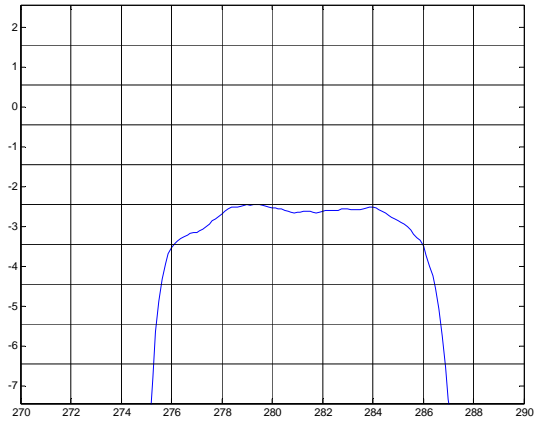
Part Number	UE280	
Rev. Date	2006-5-23	
Rev.	1.0	Page 2/3

Typical Performance

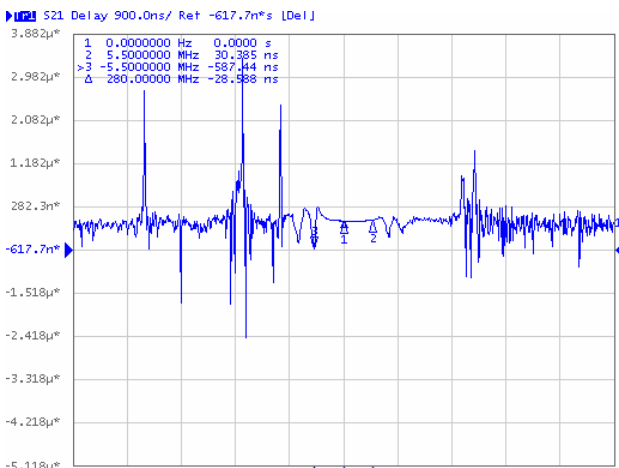
Frequency Respond



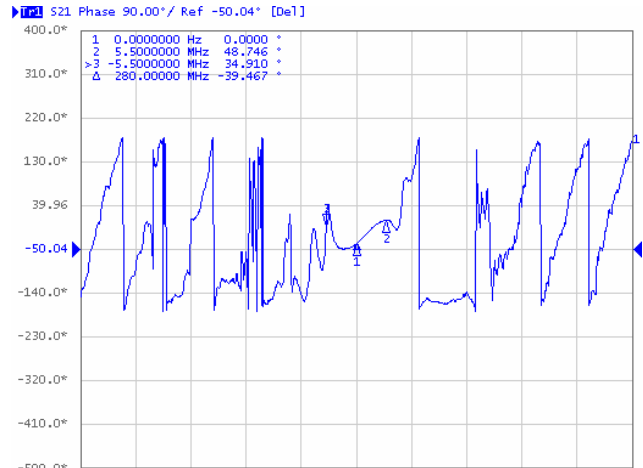
Passband Respond



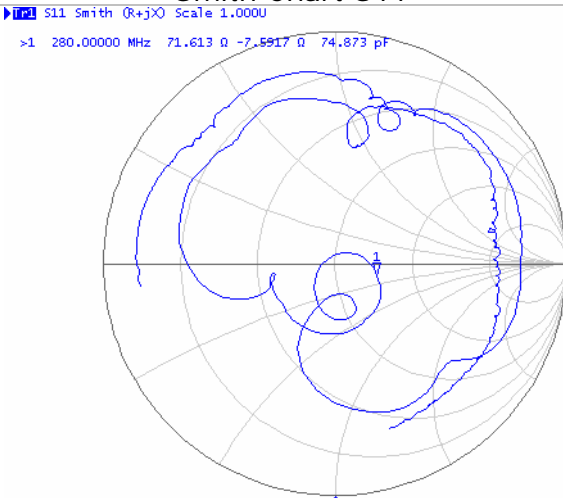
Group Delay Variation($f_0 \pm 5.5\text{MHz}$)



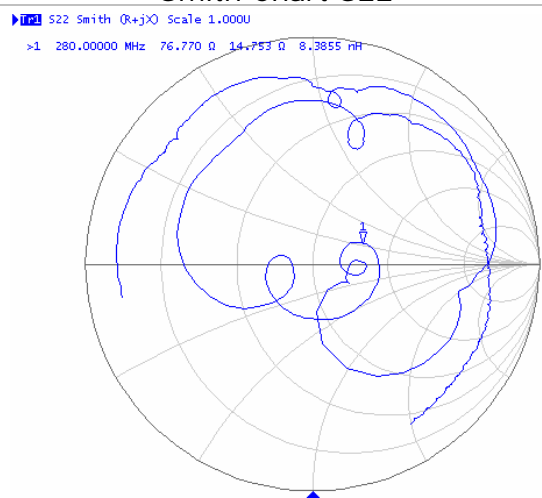
Phase Linearity($f_0 \pm 5.5\text{MHz}$)



Smith Chart S11



Smith Chart S22



SIPAT Co., Ltd.
(CETC No. 26 Research Institute)
Nanping Huayuan Road No. 14
Chongqing, China, 400060

Part Number	UE280	
Rev. Date	2006-5-23	
Rev.	1.0	Page 3/3