



# UF5305

## POWER MOSFET

### -31A, -55V P-CHANNEL POWER MOSFET

■ DESCRIPTION

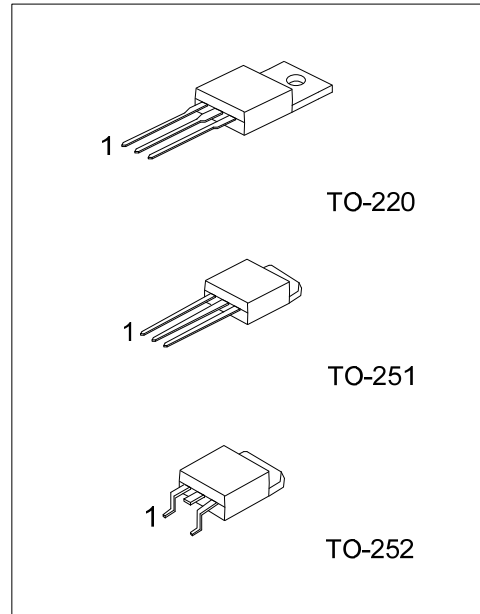
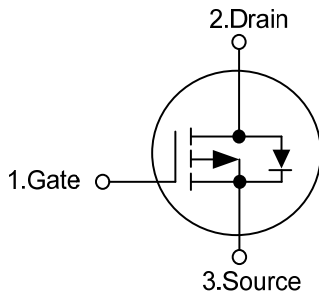
The UTC **UF5305** is a P-channel Power MOSFET, it uses UTC's advanced technology to provide the customers with high switching speed and a minimum on-state resistance.

The UTC **UF5305** is suitable for all commercial-industrial applications, etc.

■ FEATURES

- \*  $R_{DS(ON)} \leq 0.06 \Omega @ V_{GS} = -10V, I_D = -16A$
- \* High Switching Speed
- \* Dynamic dv/dt Rating

■ SYMBOL



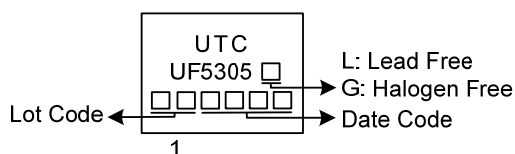
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UF5305L-TA3-T	UF5305G-TA3-T	TO-220	G	D	S	Tube
UF5305L-TM3-T	UF5305G-TM3-T	TO-251	G	D	S	Tube
UF5305L-TN3-T	UF5305G-TN3-T	TO-252	G	D	S	Tube
UF5305L-TN3-R	UF5305G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>UF5305G-TA3-T</p>	<p>(1) T: Tube, R: Tape Reel                  (2) TA3: TO-220, TM3: TO-251, TN3: TO-252                  (3) G: Halogen Free and Lead Free, L: Lead Free</p>
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■ MARKING



## ■ ABSOLUTE MAXIMUM RATING

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		$V_{DSS}$	-55	V	
Gate-Source Voltage		$V_{GSS}$	±20	V	
Drain Current	Continuous	$I_D$	$V_{GS}=-10V, T_C=25^{\circ}C$	-31	A
			$V_{GS}=-10V, T_C=100^{\circ}C$	-22	A
	Pulsed (Note 2)		$I_{DM}$	-110	A
Avalanche Energy	Single Pulse (Note 3)		$E_{AS}$	180	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	-8.2	V/ns	
Power Dissipation ( $T_C=25^{\circ}C$ )	TO-220	$P_D$	110	W	
	TO-251/TO-252		42	W	
Junction Temperature		$T_J$	-55 ~ +150	°C	
Storage Temperature Range		$T_{STG}$	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $L=0.1mH, I_{AS}=-60A, V_{DD}=-25V, R_G=25\Omega$ , Starting  $T_J=25^{\circ}C$

4.  $I_{SD}\leq-16A, di/dt\leq-280A/\mu s, V_{DD}\leq BV_{DSS}, T_J\leq 150^{\circ}C$

## ■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220	$\theta_{JA}$	62	°C/W
	TO-251/TO-252		90	°C/W
Junction to Case	TO-220	$\theta_{JC}$	1.1	°C/W
	TO-251/TO-252		2.97 (Note)	°C/W

Note: Device mounted on FR-4 substrate  $P_C$  board, 2oz copper, with 1inch square copper plate.

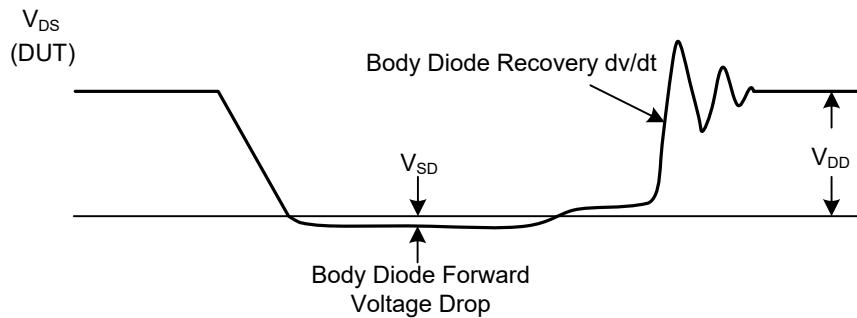
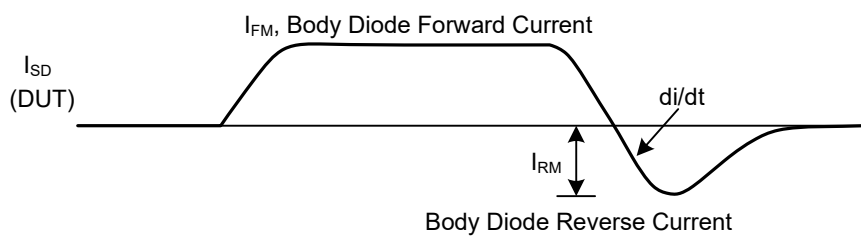
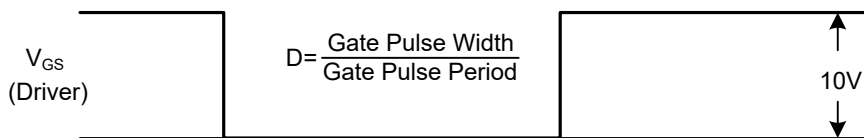
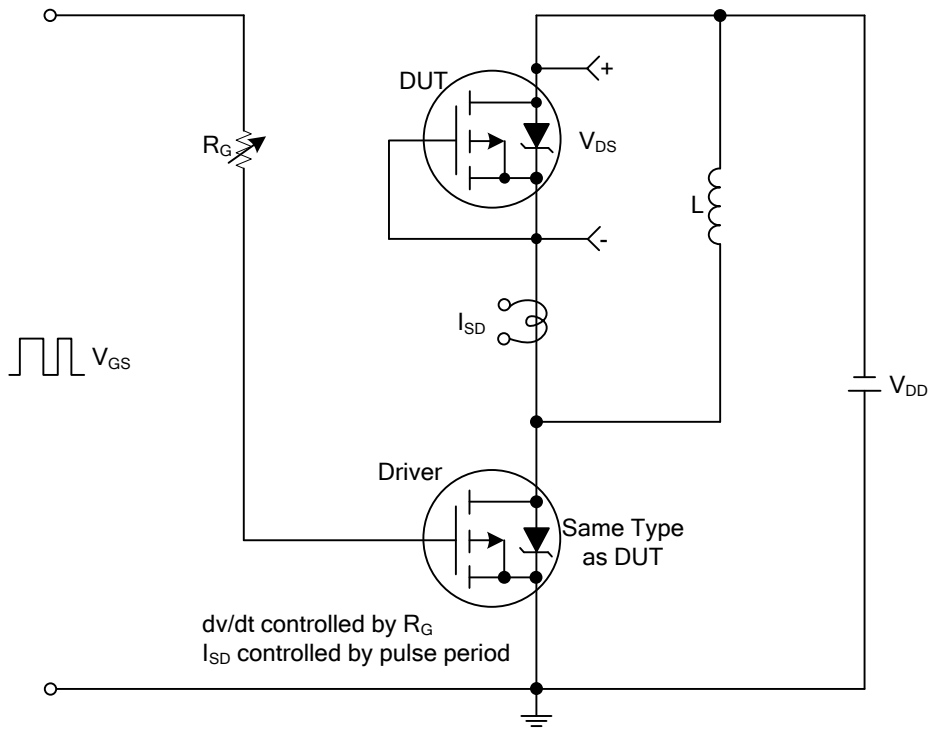
■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-55			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =-55V, V <sub>GS</sub> =0V			-25	μA
Gate-Source Leakage Current	Forward	V <sub>GS</sub> =20V, V <sub>DS</sub> =0V			100	nA
	Reverse		V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V			-100
<b>ON CHARACTERISTICS</b>						
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-16A (Note 2)			0.06	Ω
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-2.0		-4.0	V
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =-25V, f=1.0MHz		1497		pF
Output Capacitance	C <sub>OSS</sub>			411		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			90		pF
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge	Q <sub>G</sub>	I <sub>D</sub> =-16A, V <sub>DS</sub> =-44V, V <sub>GS</sub> =-10V (Note 2)		37		nC
Gate-to-Source Charge	Q <sub>GS</sub>			10.4		nC
Gate-to-Drain ("Miller") Charge	Q <sub>GD</sub>			7.2		nC
Turn-ON Delay Time	t <sub>D(ON)</sub>	V <sub>DD</sub> =-28V, I <sub>D</sub> =-16A, R <sub>G</sub> =6.8Ω R <sub>D</sub> =1.6Ω (Note 2)		10		ns
Rise Time	t <sub>R</sub>			18		ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>			44		ns
Fall Time	t <sub>F</sub>			20		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Body Diode Continuous Source Current	I <sub>S</sub>				-31	A
Maximum Body-Diode Pulsed Current (Note 1)	I <sub>SM</sub>				-110	A
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	T <sub>J</sub> =25°C, I <sub>S</sub> =-16A, V <sub>GS</sub> =0V (Note 2)			-1.4	V
Body Diode Reverse Recovery Time	t <sub>RR</sub>	T <sub>J</sub> =25°C, I <sub>F</sub> =-16A, di/dt=-100A/μs (Note 2)		71		ns
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>				170	

Notes: 1. Repetitive rating; pulse width limited by max. junction temperature.

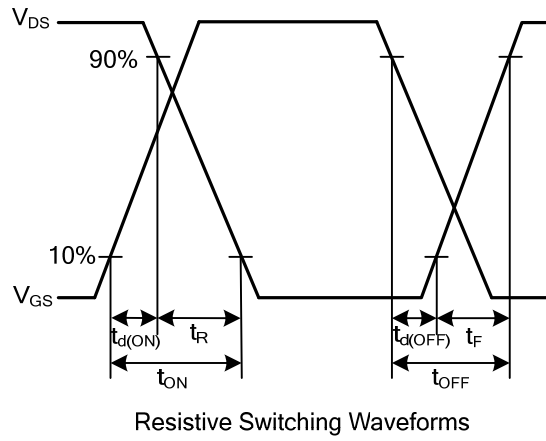
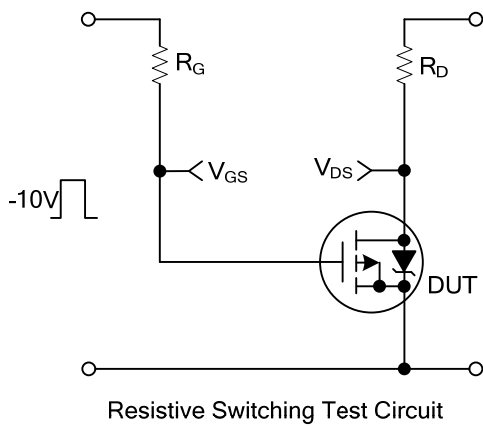
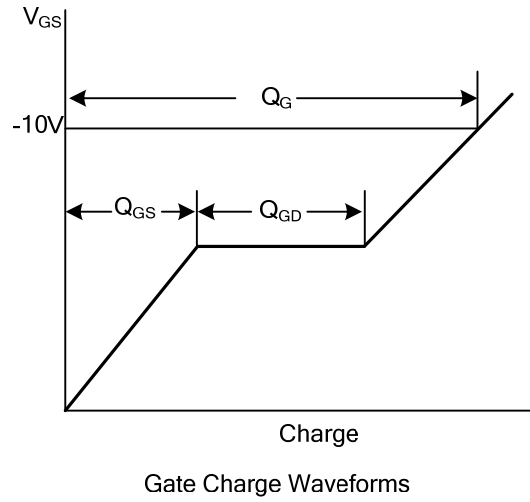
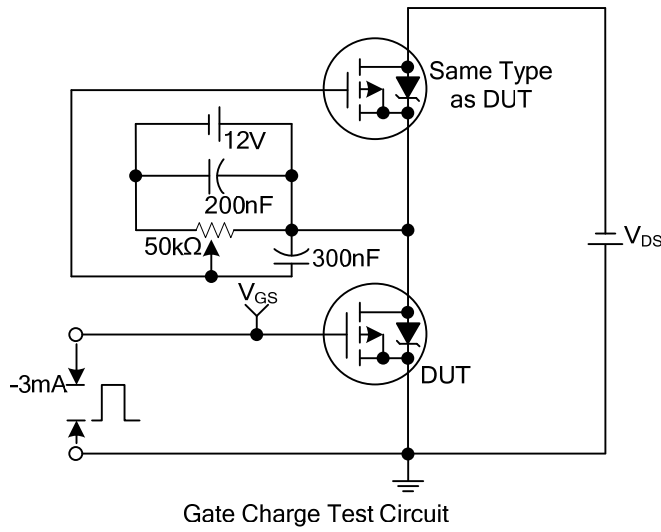
2. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

■ TEST CIRCUITS AND WAVEFORMS

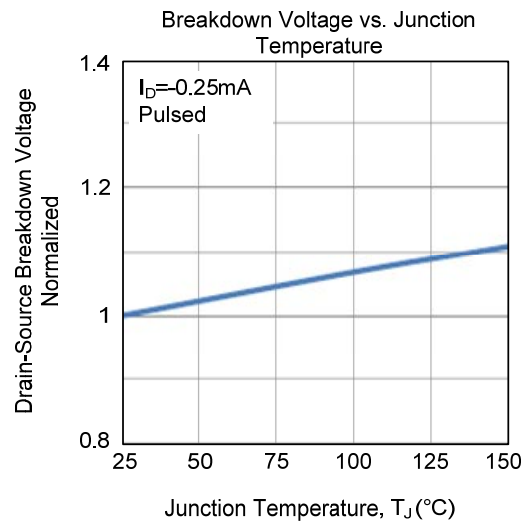
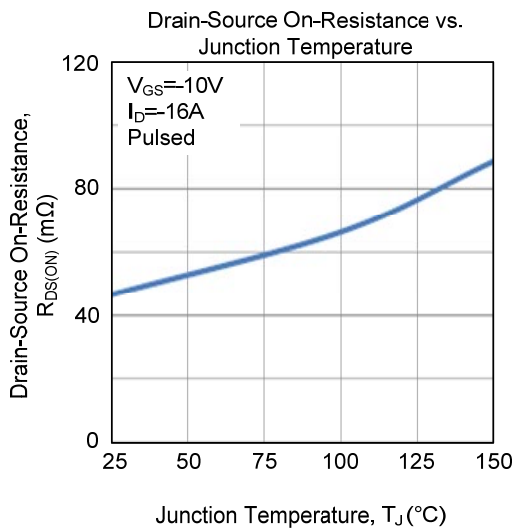
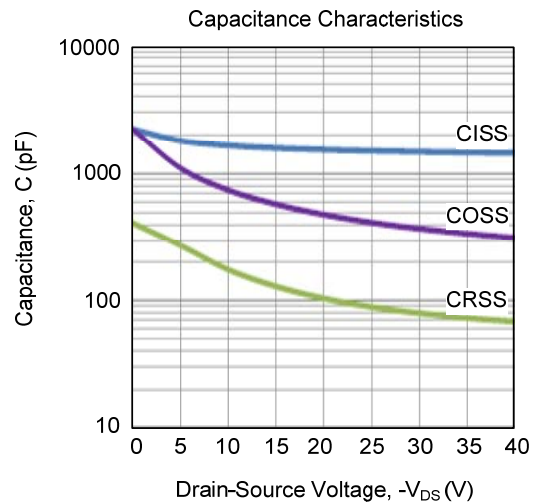
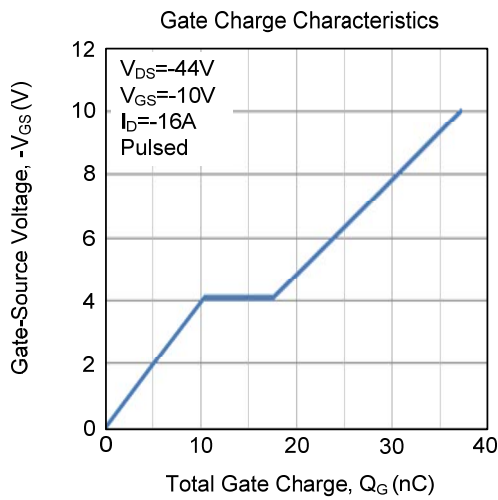
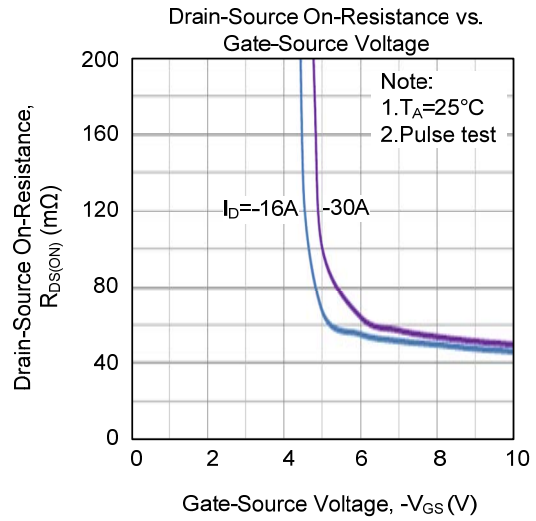
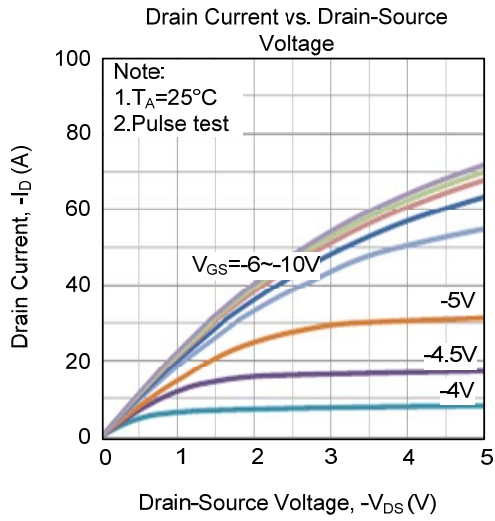


Peak Diode Recovery dv/dt Test Circuit and Waveforms

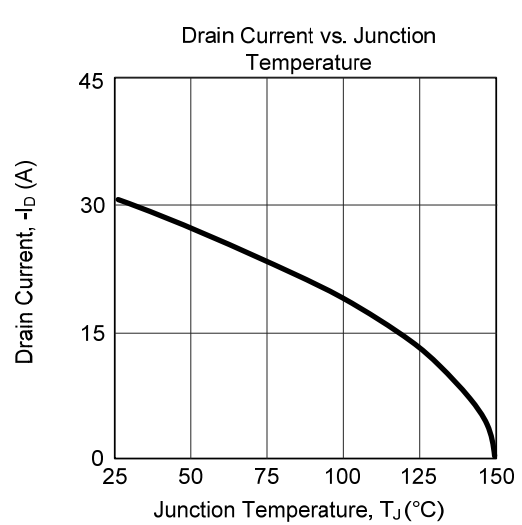
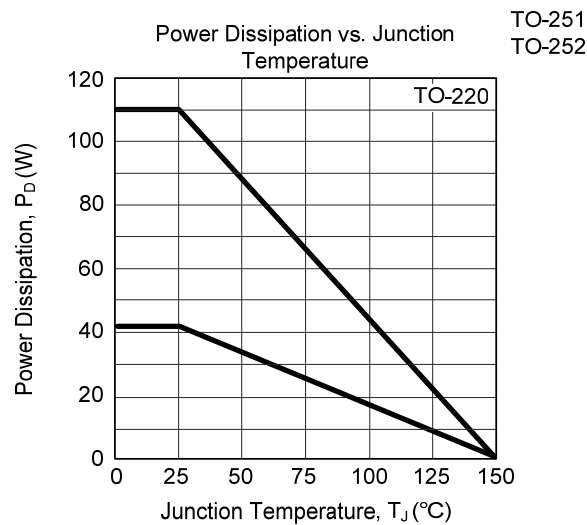
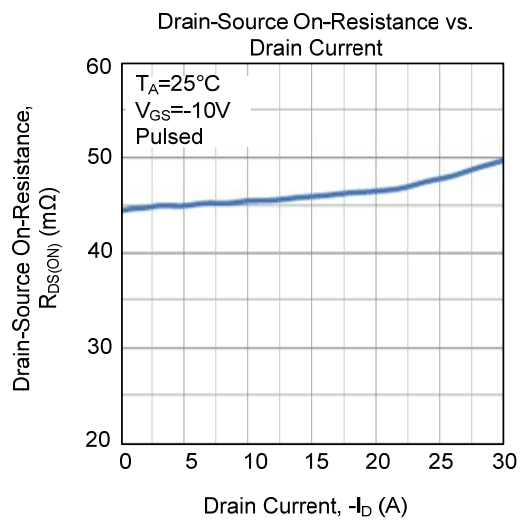
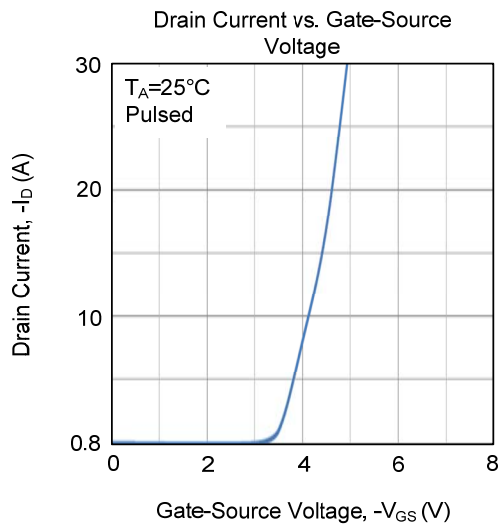
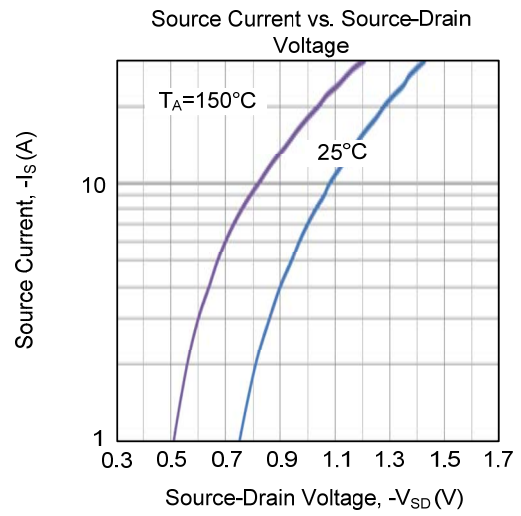
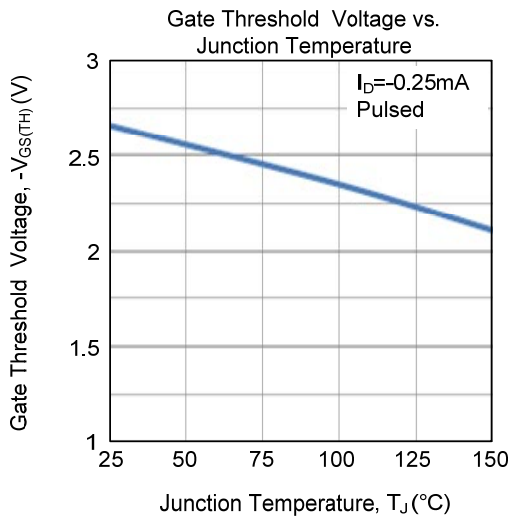
■ TEST CIRCUITS AND WAVEFORMS



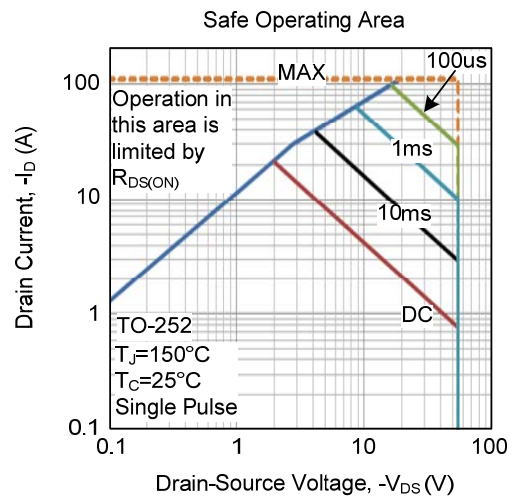
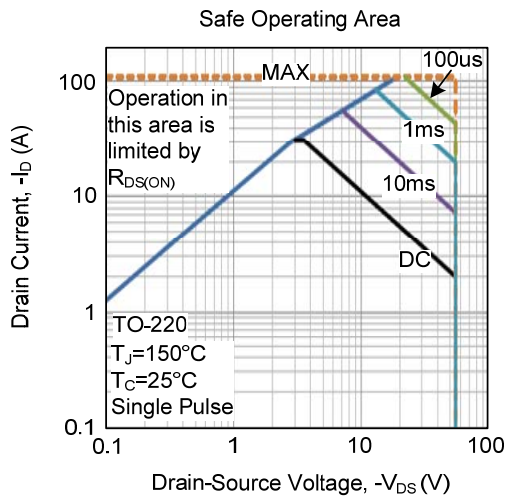
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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