

Features

- High Density Cell Design for Low RDS(ON)
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

DUAL N-Channel MOSFET

Maximum Ratings

- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 416°C/W Junction to Ambient(Note 2)

Parameter	Symbol	Rating	Unit		
Drain-Source Voltage		V_{DS}	30	V	
Gate-Source Voltage		V _{GS}	±20	V	
Continuous Drain Current	T _A =25°C	I _D	500	mA	
	T _A =100°C	'D	316		
Pulsed Drain Current ^(Note 3)		I _{DM}	2	Α	
Total Power Dissipation ^(Note 4)		P _D	300	mW	

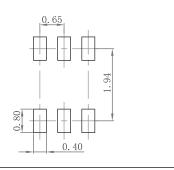
Note:

- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The value of R0JA is measured with the device mounted on the minimum recommend pad size, in the still air environment with TA =25 $\rm C$.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4. P_D is based on max. junction temperature, using junction-ambient thermal resistance.

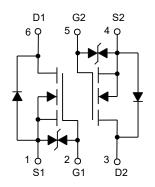
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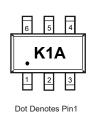
DIMENSIONS						
DIM	INCHES		MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOTE	
Α	0.006	0.014	0.15	0.35		
В	0.045	0.053	1.15	1.35		
С	0.079	0.096	2.00	2.45		
D	0.026		0.65		TYP.	
G	0.047	0.055	1.20	1.40		
Н	0.071	0.087	1.80	2.20		
J		0.004		0.10		
K	0.031	0.043	0.80	1.10		
L	0.010	0.018	0.26	0.46		
М	0.003	0.006	0.08	0.15		

Suggested Solder Pad Layout



Internal Structure and Marking Code







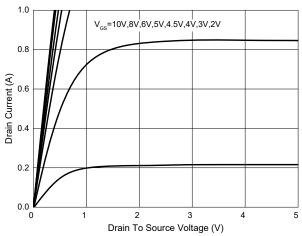
Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics				,			
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250uA	30			V	
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±10	μA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V			1	μA	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	0.7	1.1	1.5	V	
Dunin Course On Denistance	Ь	V _{GS} =10V, I _D =300mA		424	750	mΩ	
Drain-Source On-Resistance	$R_{DS(on)}$	V _{GS} =4.5V, I _D =200mA		596	960		
Forward Transconductance	g _{FS}	V _{DS} =5V, I _D =0.5A		579		mS	
Gate Resistance	R _g	V _{GS} =0V, f=1MHz		86		Ω	
Diode Characteristics			·				
Continuous Body Diode Current	Is				0.5	Α	
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =250mA			1.2	V	
Reverse Recovery Time	t _{rr}	I _F =0.5A, dI _F /dt=100A/μs		8.6		ns	
Reverse Recovery Charge	Q _{rr}	i _F -0.3A, αi _F /αί-100A/μS		1.6		nC	
Dynamic Characteristics							
Input Capacitance	C _{iss}			13.9			
Output Capacitance	C _{oss}	V _{DS} =15V,V _{GS} =0V,f=1MHz		6		pF	
Reverse Transfer Capacitance	C _{rss}			1.8		1	
Total Gate Charge	Q_g			0.8			
Gate-Source Charge	Q_{gs}	V _{DS} =15V,V _{GS} =10V,I _D =0.5A		0.2		nC	
Gate-Drain Charge	Q_{gd}			0.1			
Turn-On Delay Time	t _{d(on)}			2.2			
Turn-On Rise Time	t _r	V _{DD} =15V,V _{GS} =10V,		2.9		20	
Turn-Off Delay Time	t _{d(off)}	$I_D=0.5A,R_G=6\Omega$		7		- ns	
Turn-Off Fall Time	t _f			7.3			



Curve Characteristics

Fig.1 - Typical Output Characteristics



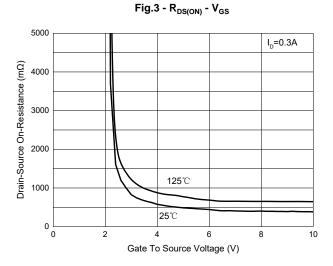


Fig.5 - Capacitance Characteristics

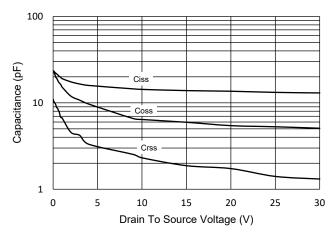


Fig.2 - Transfer Characteristic

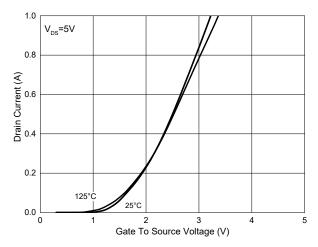


Fig.4 - R_{DS(ON)} - I_D

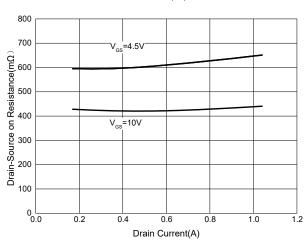
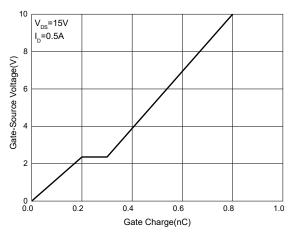
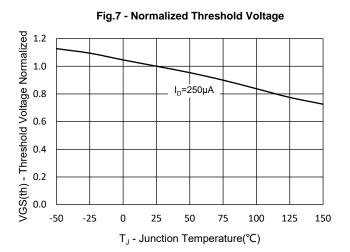


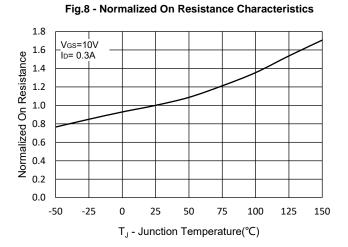
Fig.6 - Gate Charge

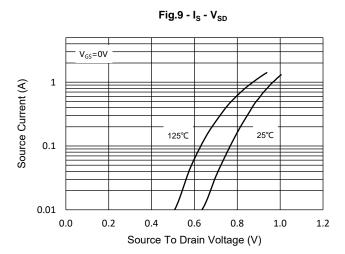


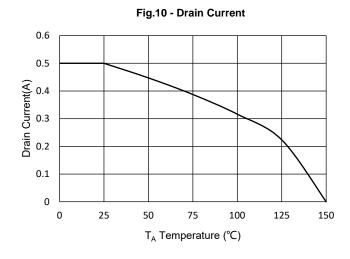


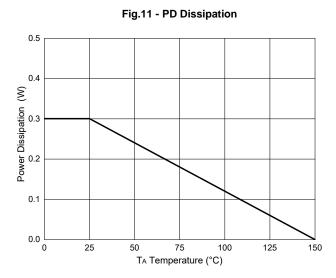
Curve Characteristics













Curve Characteristics

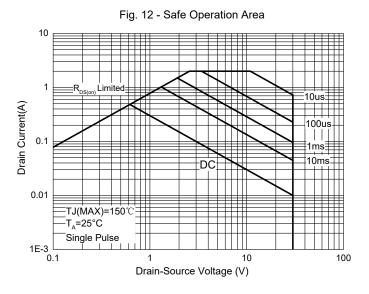
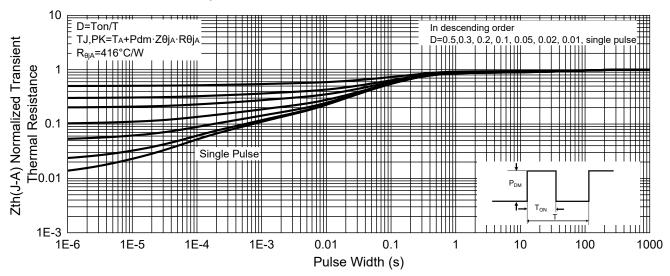


Fig. 13 -Normalized Transient Thermal Impedance





Ordering Information

Device	Packing	
Part Number-TP	Tape&Reel:3Kpcs/Reel	
Part Number-TPQ2	Tape&Reel:3Kpcs/Reel	

For packaging details, go to our website at https://www.mccsemi.com/pdf/ProductPackaging/SOT-363%20Package.pdf

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