

UM91603C

Memory Pulse Dialer





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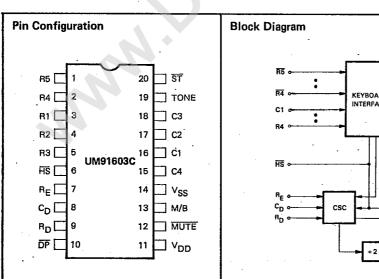
- Three 16-digit memories (direct dialing) plus a 16digit last number redial memory
- Low voltage CMOS process for direct operation from telephone line
- Inexpensive RC oscillator design with accuracy better than ±5% over temperature and unit-unit variations
- Independent select for make/break ratio (331/3/662/3 or 40-60)
- Interfaces with inexpensive XY matrix keyboard
- Output pulse dial rate 10 pps with interdigit pause
- Independent select input for storage mode
- 3.2-second access pause feature
- Pacifier tone output for valid key input
- \* and # keys available in high speed master mode

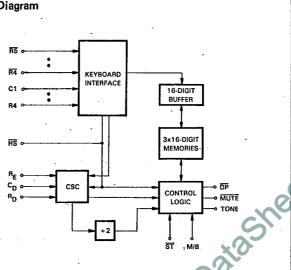
## **General Description**

The UM91603C pulse dialer is a monolithic CMOS integrated circuit which converts pushbutton inputs into a series of pulses suitable for telephone dialing. The UM91603C has three 16-digit memories which provide one-touch dialing operation, a great convenience for telephone dialing. Also, the UM91603C protects the memories from improper keyboard operation with a

storage switch (ST).

CMOS technology is used to produce this device, resulting in very low power consumption, high noise immunity, and easy interface to a variety of telephones and requires few external components.





# 9325812 UNITED MICROELECTRONICS

92D 00353

DT-75-07-07



UM91603C

## Absolute Maximum Ratings\*

Supply Voltage ..... -0.3V to +4.0V

Applied Voltage on Any Pin (V<sub>IN</sub>) . . . . . . . . .  $V_{SS}$  –0.3 to  $V_{DD}$  +0.3V

#### \*Comments

Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

#### **Electrical Characteristics**

(Specifications apply over the operating temperature range, and  $V_{SS}$  = 0V and 2.0V  $\leq$   $V_{DD}$   $\leq$  3.5V, unless otherwise specified.)

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions	
OPERATING VOI	TAGE		I —				
Data Retention	V <sub>DD</sub>	1.0			٧	On Hook (HS = V <sub>DD</sub> )	
Nondialing	V <sub>DD</sub>	1.5		3.5	V	Off Hook, Oscillator not running	
Dialing	V <sub>DD</sub>	2.0		3.5	٧	Off Hook, Oscillator running	
OPERATING CUP	RRENT						
Data Retention	I <sub>DD</sub>			200	nA	On Hook V <sub>DD</sub> = 1.0V @ 25°C	
	l <sub>DD</sub>			750	nA	On Hook V <sub>DD</sub> = 1.0V @ 40°C	
Nondialing	I <sub>DD</sub>			10	μΑ	Off Hook V <sub>DD</sub> = 1.5V Oscillator not running, outputs not loaded	
Dialing	I <sub>DD</sub>			500	μA	Off Hook V <sub>DD</sub> = 3.5V Oscillator running, outputs not loaded	
OUTPUT CURRE	NT			•			
DP Sink Current	loldp	125			μΑ	V <sub>DD</sub> = 3.5V, V <sub>OUT</sub> = 0.5V	
DP Source Current	I <sub>OHDP</sub>	20			μΑ	V <sub>DD</sub> = 1.5V, V <sub>OUT</sub> = 1.0V	
	I <sub>OHDP</sub>	125			μΑ	V <sub>DD</sub> = 3.5V, V <sub>OUT</sub> = 2.5V	
MUTE Sink Current	I <sub>OLM</sub>	125			μΑ	V <sub>DD</sub> = 3.5V, V <sub>OUT</sub> = 0.5V	
MUTE Source Current	Гонм	20			μΑ	V <sub>DD</sub> = 1.5V, V <sub>OUT</sub> = 1.0V	
	Гонм	125			μΑ.	V <sub>DD</sub> = 3.5V, V <sub>OUT</sub> = 2.5V	
TONE Drive Current	Тонт	250			μΑ	V <sub>DD</sub> = 2.0V, V <sub>OUT</sub> = 1.5V	
Input High Voltage	V <sub>tH</sub>	80% V <sub>DD</sub>		V <sub>DD</sub> + 0.3	V		
Input Low Voltage	V <sub>IL</sub>	-0.3		20% V <sub>DD</sub>	٧		
Oscillator Frequency (F <sub>0</sub> ) Stability vs. Supply Voltage	Δf <sub>b</sub> /f <sub>e</sub>	-4		+4	%	$V_{DD} = 2.0 \text{V to } V_{DD} = 2.75 \text{V}$	
	Δf <sub>0</sub> /f <sub>0</sub>	-4		+4	%	V <sub>DD</sub> = 2.75V to V <sub>DD</sub> = 3.5V	

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## Pin Description

## Keyboard (R1, R2, R3, R4, R5, C1, C2, C3, C4)

These pins can be interfaced to an XY matrix keyboard. Each row input has a pull-up resistor. When any key is depressed, scanning signals will be presented at the 4 column outputs. Debouncing is provided to avoid false entry (typ.: 16.7ms).

#### Make/Break (M/B)

This input provides the selection of the Make/Break ratio (33.3:66.7/40:60) when M/B connects to  $V_{SS}/V_{DD}$ . In addition to the normal M/B ratio control function, this pin provides a high speed master mode function which enables fast dial pulse output to drive a UM9160 Slave Pulse/Tone Dialer (see next page).

#### Dial Pulse Out (DP)

Output drive is provided to turn on a transistor at the dial pulse rate. The normal putput will be "low" during "break" and "high" during "make."

### Mute Out (MUTE)

A negative going low pulse is available to turn off an external transistor to mute the receiver during dial pulsing.

## Dial Rate Oscillator (R<sub>E</sub>, C<sub>D</sub>, R<sub>D</sub>)

These pins are provided to connect external resistors RE and RD and Capacitor CD to form an RC oscillator that generates the time base for this chip. (The recommended value of RC components are  $R_E$  = 1.5  $M\Omega$ ,  $R_D = 470 \text{ K}\Omega$ ,  $C_D = 330 \text{ pF}$ ,  $F_0 = 2.4 \text{ KHz}$ )

## Store Switch (ST)

This input consists of a pull-up resistor. When this input is switched to the V<sub>SS</sub>, the chip will go into storage mode. When this input is floating or connected to VDD, the 91603C will stay in dialing mode unless the ST key is depressed.

## Pacifier Tone (TONE)

This pin provides a direct piezo drive capability to produce a 1/2 fo frequency tone output when a valid key/press is detected.

## Heek Switch (HK)

This input detects the state of the hook switch contact. "Off Hook" corresponds to the VSS condition.

# Power (V<sub>DD</sub>, V<sub>SS</sub>)

These are the power supply inputs. This device is designed to operate at 2.0 to 3.5 volts.

## **Keyboard Definition**

#### NORMAL MODE

				_
R <sub>1</sub>	1	2	з	Eı
R <sub>2</sub>	4	5.	6	E2
R₃	7	8	9	Ез
R <sub>4</sub>	Ρ	0	R	ST
		•		

## C1 C2 C3 C4

#### HIGH-SPEED MASTER MODE

Ri	1	2	3	Eı
R <sub>2</sub>	4	5	6	E <sub>2</sub>
R <sub>3</sub>	7	8	9	E <sub>3</sub>
R <sub>4</sub>	*	0	#	ST
R <sub>5</sub>	Р	0	R	ST

R: Redial

ST: Store P: Automatic access pause

C1 C2 C3 C4

E<sub>1</sub>/E<sub>2</sub>/E<sub>3</sub>: Repertory direct access dialing key

# HIGH-SPEED MASTER MODE

The UM91603C provides a High-speed Master Mode (PDR = 300 pps, IDP = 6.7 ms) which signals a UM9160 Slave Pulse/Tone Dialer. In this mode of operation, \* and # signals are available.

The M/B pin is used for triggering the UM91603C into the High-speed master Mode by applying a high to low transition when HS is low. The UM91603C will stay in the High-speed Master Mode until "On Hook."

The operation is the same as in normal mode except that the speed is faster (PDR = 300 pps, IDP = 6.7 ms). Another difference is that the Pause key signals a 13 pulse train to provide an acceptable input code for the UM9160 Slave Pulse/Tone Dialer. ..

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## **Operating Characteristics**

## **Normal Dialing**

OFF Hook, D1 ..... Dn; n ≤ 16 (See Note) Dial pulsing starts as soon as first digit is entered (debounced and detected on chip). Pause may be entered in the dialing sequence by pressing the "P" key and can be recognized as one digit in redial memory. Pause access time is 3.2 seconds. The digit that is entered after the "P" key will be sent out after a 3.2-second delay.

Note: Digits which are entered after the 16th digit are ignored unless the first 16-digit dialing sequence is complete. The buffer memories are cleared when the 17th digit is entered and restored beginning with the 17th digit.

## Redialing

OFF HOOK, R .

Last number dialed (in normal dialing) can be redialed by pressing the "R" key.

## Repertory Dialing

OFF HOOK, EI , EJ , EK ; I, J, K ∈ {1, 2, 3} The telephone numbers that are stored in the repertory dialing memories can be directly dialed out by pressing the E1 , E2 or E3 keys. Any second key/press will be ignored unless the first dialing sequence is complete.

## Normal Dialing + Repertory Dialing

OFF HOOK, D1 ..... Dn , EI ..... Wait for normal dialing to complete before pressing the " EI " key.

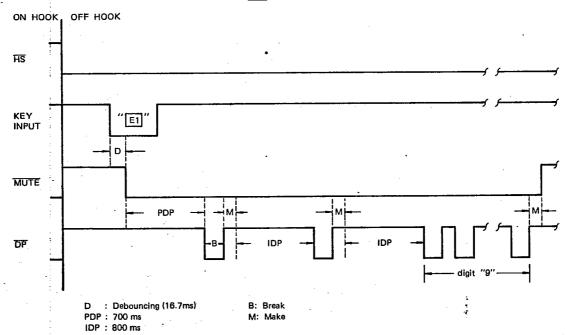
## Storing a Telephone Number

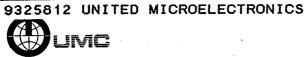
- a) OFF HOOK, ST , D1 .... Dn , El ; n ≤ 16, 1 = 1, 2 or 3.
- b) OFF HOOK, D1 ..... Dn , EI ; n ≤ 16, I = 1, 2, 3; ST pin connected to V<sub>SS</sub>.

Pause may be entered in the storing sequence by pressing " P " key and is recognized as one digit in repertory memories.

# **Timing Diagram**

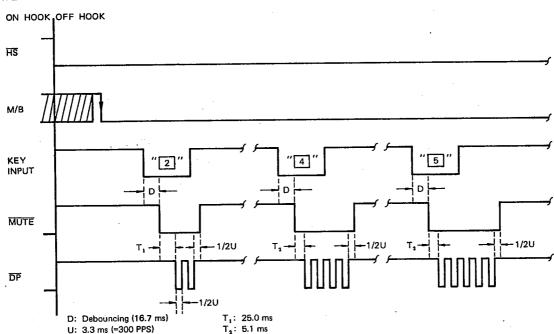
NORMAL MODE REPERTORY DIALING (ASSUMING THE NUMBER STORED IN [E1] IS 119)



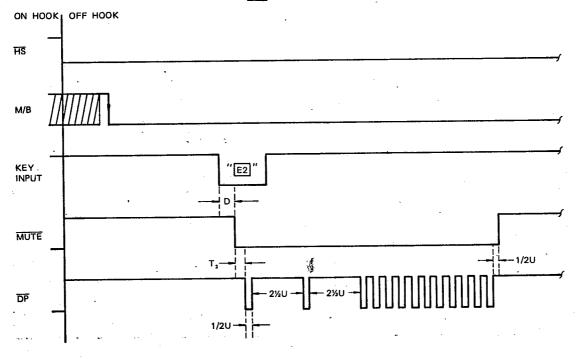


UM91603C

HIGH-SPEED MASTER MODE NORMAL DIALING



HIGH-SPEED MASTER MODE REPERTORY DIALING (ASSUMING THE NUMBER STORED IN [E2] IS 110)



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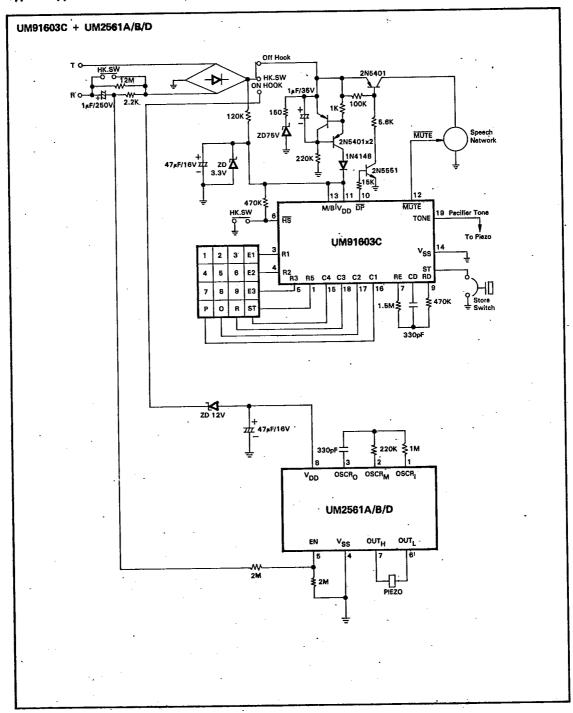
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UM91603C

Typical Applications (WITH TONE RINGER)



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UM91603C

# TONE/PULSE SWITCHABLE WITH DIRECT ACCESS & REDIAL FUNCTION

