

UMD9114 Preliminary CMOS IC

DC TAIL ROTOR MOTOR, STEERING GEAR MOTOR DRIVE CIRCUIT

DESCRIPTION

UTC **UMD9114** is an integrated brush DC motor drive solution for battery-powered toys, low-voltage or battery-powered motion control applications. It has H bridge driver and uses the PMOS and NMOS power transistors with low output resistance. Low on-resistance ensures the circuit to consume lower power in operating at a continuous current, and ensures the circuit to operate stably for a long time. The circuit has a wide working voltage range from 2.5V to 5V. The maximum continuous output current reaches 0.5A, and the maximum peak output current can be 0.8A when V_{DD} is 4V.

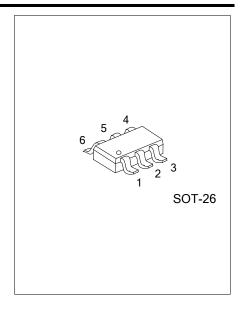
UTC **UMD9114** has on-chip temperature protection function. When load motor with low internal resistance is in locked rotor, UTC **UMD9114** output current will increase momentarily, power dissipation of the circuit will go up sharply, and the chip temperature will soar. But, when the chip temperature exceeds a maximum temperature point (typically 160°C) set by internal temperature protection circuit, the internal circuit will switch off the on-chip power switching transistor of UTC **UMD9114**, and switch off load current, preventing potential safety hazards such as fuming, igniting of plastic package caused by over temperature etc. Only after having confirmed that the circuit has returned to safety temperature, the on-chip temperature hysteresis circuit can be allowed to re-control the circuit.

■ FEATURES

- * Low standby current (0.1µA typ.)
- * PMOS and NMOS power transistors with low output resistance If I_O is 100mA, R_{ON} of power transistor is 1.5 Ω If I_O is 200mA, R_{ON} of power transistor is 1.6 Ω If I_O is 300mA, R_{ON} of power transistor is 1.7 Ω
- * Built-in Subsequent stream diode
- -No external diode required
- * Low input current

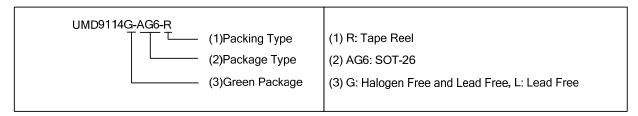
Pull-down resistance is $1.8M\Omega$ typical

- 1.6uA input current when input voltage is 3V
- * On-chip thermal shut down (TSD) with hysteresis

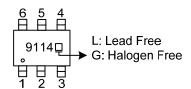


■ ORDERING INFORMATION

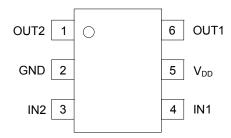
Ordering	Number	Dookona	Packing	
Lead Free	Halogen Free	Package		
UMD9114L-AG6-R	UMD9114G-AG6-R	SOT-26	Tape Reel	



■ MARKING

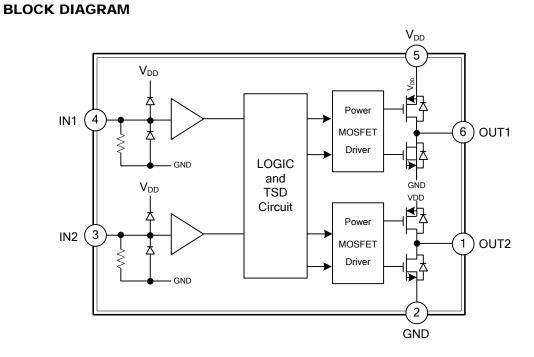


■ PIN CONFIGURATION



■ PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	OUT2	Reverse Drive Output Pin
2	GND	Ground
3	IN2	Reverse Control Input Pin
4	IN1	Forward Control Input Pin
5	V_{DD}	Supply Voltage
6	OUT1	Forward Drive Output Pin



■ LOGIC TRUTH TABLE

IN1	IN2	OUT1	OUT2	FUNCTION
L	L	Z	Z	Standby (Stop)
Н	L	Н	L	Forward rotation
L	Н	L	Н	Backward rotation
Н	Н	L	L	Brake

■ **ABSOLUTE MAXIMUM RATING** (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Maximum Supply Voltage	V_{DD}	5.5	V
Maximum External Output Voltage	V_{OUT}	V_{DD}	V
Maximum External Input Voltage	V _{IN}	V_{DD}	V
Peak Output Current/Channel	I _{OUT PEAK}	0.8	Α
Maximum continuous output current	I _{OUT}	0.55	Α
Maximum power	P_{D}	0.45	W
Junction Temperature	T_J	+150	°C
Operational Temperature Range	T _{OPR}	-20 ~ +85	°C
Storage Temperature	T _{STG}	-55 ~ +150	°C

Preliminary

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JAS}	278	°C/W

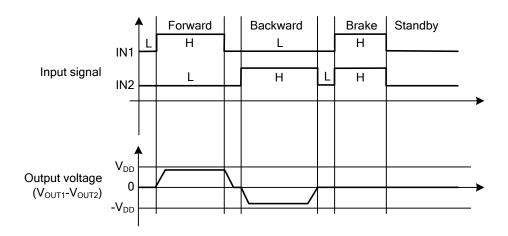
■ **RECOMMENDED OPERATIONAL CONDITIONS** (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Supply Voltage	V_{DD}	2.5		5	V
Input Voltage	V_{IN}	0		V_{DD}	V
Output current from OUT1 to OUT2 when V _{DD} =4.5V	I _{oc}		300	500	mA

■ **ELECTRICAL CHARACTERISTICS** (T_A=25°C, V_{DD}=3V, unless otherwise stated)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
The Power Supply Parameters							
VDD Standby Current	I _{VDDST}	IN1=IN2=L, V _{DD} =5.5V, Output opened			4	μΑ	
VDD Static Supply Current	I _{VDD}	IN1=H or IN2=H; Output opened		100		μΑ	
Input Logic Level							
Input High Level	V_{INH}	V _{DD} =3V	$0.7 \times V_{DD}$			V	
Input Low Level	V_{INL}	V _{DD} =3V			$0.2 \times V_{DD}$	V	
Input High Level Current	I _{INH}	V_{INH} =3V, V_{DD} =3V		2.0		μΑ	
Input the Pull-Down Resistor	R _{IN}	V_{INH} =3V, V_{DD} =3V		1.8		МΩ	
The Power Tube Leads to Inter	nal Resista	ince					
		$I_O=\pm 100$ mA, $V_{DD}=3V$		1.5			
Output Resistance	R _{ON}	I_O =±200mA, V_{DD} =3V		1.6		Ω	
		$I_O=\pm300$ mA, $V_{DD}=3$ V		1.7			
Protection Function Parameters							
Protection Temperature	TSD			160		°C	
TSD Hysteresis	TSDH			20		°C	

■ TYPICAL WAVEFORM

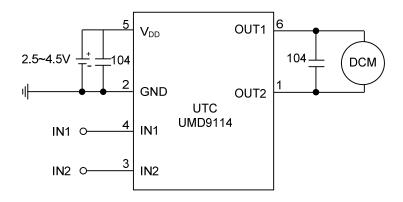


■ APPLICATION INFORMATION

Thermal shut down (TSD)

When Junction Temperature reaches 160°C, the internal circuit will switch off the on-chip power switching transistor of UTC **UMD9114**, preventing potential safety hazards caused by over temperature. The temperature hysteresis of TSD is 25°C typical.

■ TYPICAL APPLICATION CIRCUIT



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