



UML2502

Power MOSFET

N-CHANNEL POWER MOSFET

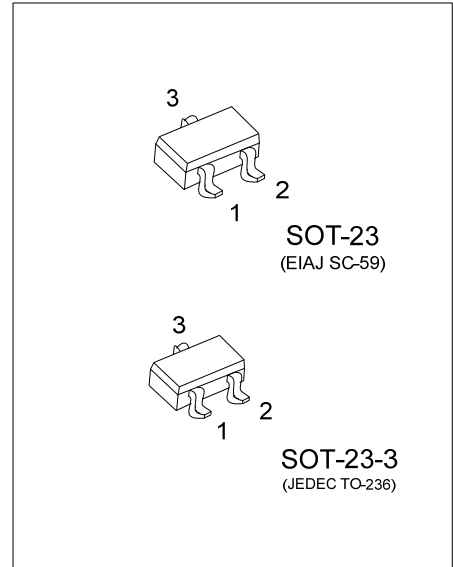
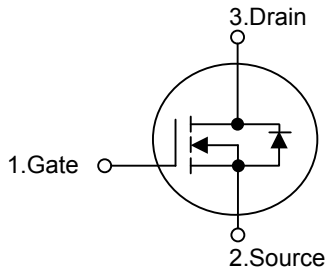
DESCRIPTION

The **UML2502** uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with low gate voltages. This device is suitable for use as a load switch or in PWM applications.

FEATURES

- * $R_{DS(ON)} \leq 45 \text{ m}\Omega$ @ $V_{GS}=4.5\text{V}$, $I_D=4.2\text{A}$
- * $R_{DS(ON)} \leq 80 \text{ m}\Omega$ @ $V_{GS}=2.5\text{V}$, $I_D=3.6\text{A}$
- * Ultra Low Gate Charge (Max. 12nC)
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability

SYMBOL



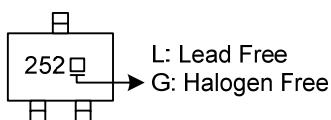
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UML2502L-AE2-R	UML2502G-AE2-R	SOT-23-3	G	S	D	Tape Reel
UML2502L-AE3-R	UML2502G-AE3-R	SOT-23	G	S	D	Tape Reel

Note: Pin Assignment: G: Gate S: Source D: Drain

<p>UML2502G-AE2-R</p> <p>(1)Packing Type (2)Package Type (3)Green Package</p>	<p>(1) R: Tape Reel (2) AE2: SOT-23-3, AE3: SOT-23 (3) G: Halogen Free and Lead Free, L: Lead Free</p>
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MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_C=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V_{DSS}	20	V
Gate-Source Voltage	V_{GSS}	± 12	V
Continuous Drain Current $V_{GS}=4.5\text{V}$	I_D	4.2	A
Pulsed Drain Current (Note 2)	I_{DM}	10	A
Maximum Power Dissipation ($T_A=25^\circ\text{C}$)	SOT-23-3	0.5	W
	SOT-23	0.6	W
Junction Temperature	T_J	+150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-23-3	250	$^\circ\text{C/W}$
	SOT-23	208	$^\circ\text{C/W}$

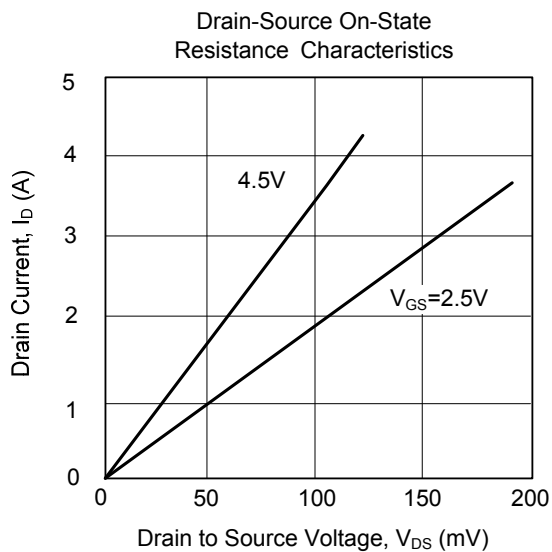
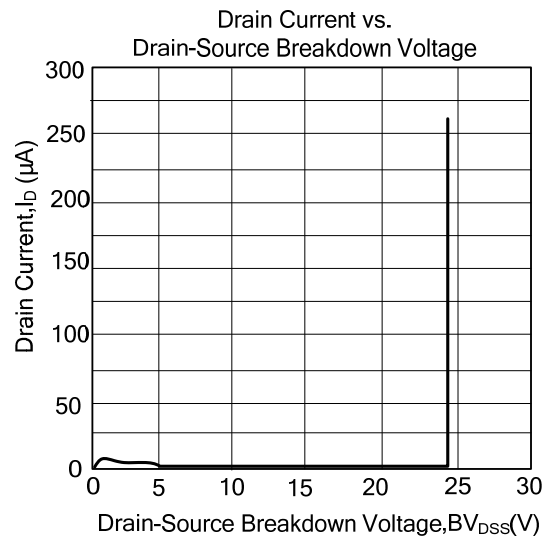
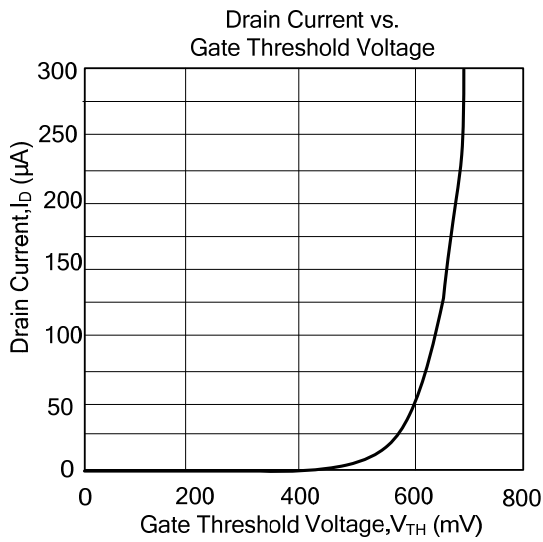
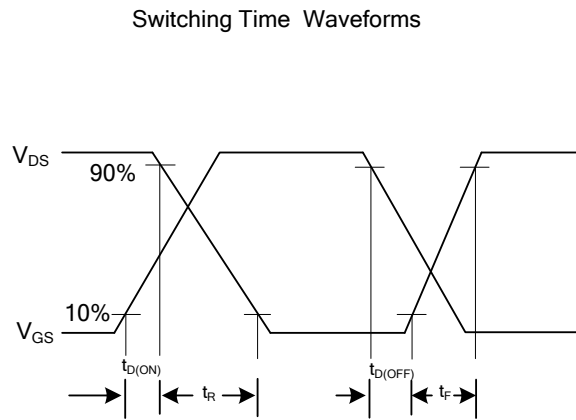
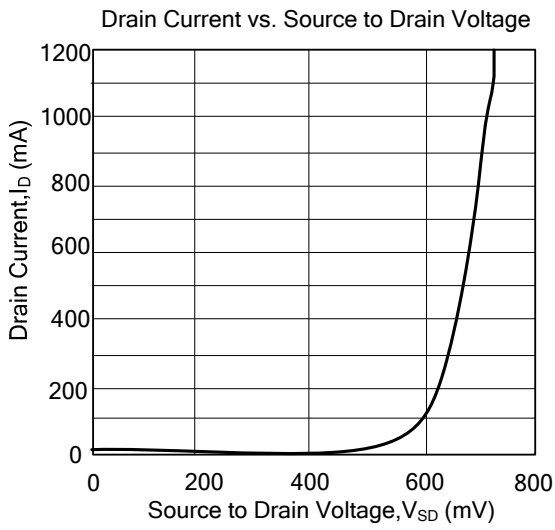
Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	20			V
Drain-Source Leakage Current	I_{DSS}	$V_{GS}=0\text{V}, V_{DS}=16\text{V}$			1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 12\text{V}, V_{DS}=0\text{V}$			± 100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.45		1.2	V
Drain-Source On-State Resistance (Note)	$R_{DS(ON)}$	$V_{GS}=4.5\text{V}, I_D=4.2\text{A}$		30	45	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}, I_D=3.6\text{A}$		50	80	$\text{m}\Omega$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$V_{GS}=0\text{V}, V_{DS}=15\text{V}, f=1.0\text{MHz}$		192		pF
Output Capacitance	C_{OSS}			55		pF
Reverse Transfer Capacitance	C_{RSS}			44		pF
SWITCHING PARAMETERS						
Total Gate Charge (Note)	Q_G	$V_{DS}=10\text{V}, V_{GS}=4.5\text{V}, I_D=3.0\text{A}$		8		nC
Gate Source Charge	Q_{GS}			1.2		nC
Gate Drain Charge	Q_{GD}			2.4		nC
Turn-ON Delay Time (Note)	$t_{D(ON)}$	$V_{DS}=10\text{V}, V_{GS}=4.5\text{V}, I_D=3.0\text{A}, R_G=3\Omega$		3.5		ns
Turn-ON Rise Time	t_R			17		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			11		ns
Turn-OFF Fall-Time	t_F			23		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Continuous Drain-Source Diode Forward Current	I_S				1.3	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}				10	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0\text{V}, I_S=1.3\text{A}, T_J=25^\circ\text{C}$ (Note)			1.2	V

Notes: Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.

■ TYPICAL CHARACTERISTICS



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