

## NPN SILICON HIGH FREQUENCY TRANSISTOR

### UPA814TF

### FEATURES

- **SMALL PACKAGE OUTLINE:**  
SOT-363 package measures just 2.0 mm x 1.25 mm
- **LOW HEIGHT PROFILE:**  
Just 0.60 mm high
- **HIGH COLLECTOR CURRENT:**  
 $I_c$  MAX = 100 mA

### DESCRIPTION

The UPA814TF contains two NE688 NPN high frequency silicon bipolar chips. NEC's new low profile TF package is ideal for all portable wireless applications where reducing component height is a prime consideration. Each transistor chip is independently mounted and easily configured for two stage cascade LNAs and other similar applications.

### ABSOLUTE MAXIMUM RATINGS<sup>1</sup> ( $T_A = 25^\circ\text{C}$ )

SYMBOLS	PARAMETERS	UNITS	RATINGS
$V_{CB0}$	Collector to Base Voltage	V	9
$V_{CE0}$	Collector to Emitter Voltage	V	6
$V_{EB0}$	Emitter to Base Voltage	V	2
$I_c$	Collector Current	mA	100
$P_T$	Total Power Dissipation		
	1 Die	mW	110
	2 Die	mW	200
$T_J$	Junction Temperature	$^\circ\text{C}$	150
$T_{STG}$	Storage Temperature	$^\circ\text{C}$	-65 to +150

Note: 1. Operation in excess of any one of these parameters may result in permanent damage.

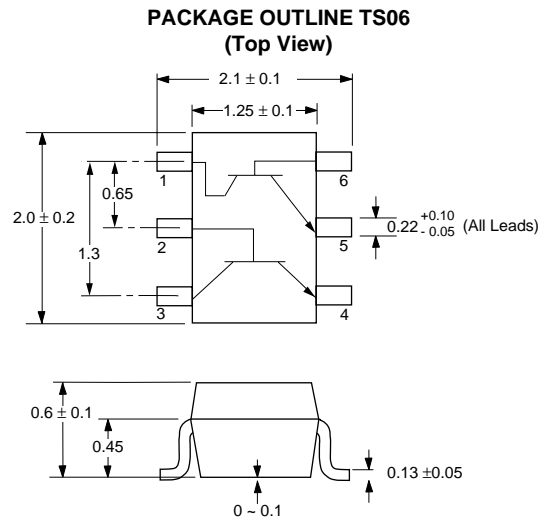
### ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ )

PART NUMBER PACKAGE OUTLINE			UPA814TF TS06		
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX
$I_{CBO}$	Collector Cutoff Current at $V_{CB} = 5\text{V}$ , $I_E = 0$	$\mu\text{A}$			0.1
$I_{EBO}$	Emitter Cutoff Current at $V_{EB} = 1\text{V}$ , $I_C = 0$	$\mu\text{A}$			0.1
$h_{FE}$	Forward Current Gain <sup>1</sup> at $V_{CE} = 1\text{V}$ , $I_C = 3\text{mA}$		80	110	160
$f_T$	Gain Bandwidth at $V_{CE} = 3\text{V}$ , $I_C = 20\text{mA}$ , $f = 2\text{GHz}$	GHz		9.0	
$C_{re}$	Feedback Capacitance <sup>2</sup> at $V_{CB} = 1\text{V}$ , $I_E = 0$ , $f = 1\text{MHz}$	pF		0.75	0.85
$ S_{21E} ^2$	Insertion Power Gain at $V_{CE} = 3\text{V}$ , $I_C = 20\text{mA}$ , $f = 2\text{GHz}$	dB		6.5	
NF	Noise Figure at $V_{CE} = 3\text{V}$ , $I_C = 7\text{mA}$ , $f = 2\text{GHz}$	dB		1.5	
$h_{FE1}/h_{FE2}$	$h_{FE}$ Ratio: $h_{FE1} = \text{Smaller Value of } Q_1, \text{ or } Q_2$ $h_{FE2} = \text{Larger Value of } Q_1 \text{ or } Q_2$		0.85		

Notes: 1. Pulsed measurement, pulse width  $\leq 350\ \mu\text{s}$ , duty cycle  $\leq 2\%$ .

2. The emitter terminal should be connected to the ground terminal of the 3 terminal capacitance bridge. For Tape and Reel version use part number UPA814TF-T1, 3K per reel.

### OUTLINE DIMENSIONS (Units in mm)



#### PIN OUT

1. Collector Transistor 1
2. Base Transistor 2
3. Collector Transistor 2
4. Emitter Transistor 2
5. Emitter Transistor 1
6. Base Transistor 1

#### Note:

Pin 1 is the lower left most pin as the package lettering is oriented and read left to right.

## California Eastern Laboratories

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