

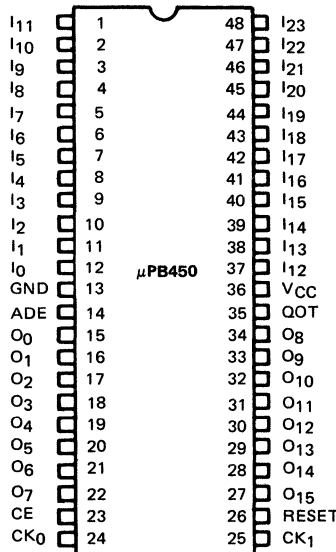
**9216 BIT FIELD PROGRAMMABLE LOGIC ARRAY**

**DESCRIPTION** The  $\mu$ PB450 is a bipolar, 9, 216-bit field programmable logic array. It includes 24 input and 16 output lines, 72 product terms, input 2-bit decoders, and 16-bit feedback registers. This provides an extremely versatile organization. Interconnection of internal AND-OR arrays is performed electrically by the proven, avalanche induced migration method which is widely used in NEC Bipolar PROM technology.

- FEATURES**
- 24 Input Terminals
  - 16 Output Terminals with Latches
  - 72 Product Terms
  - 16 Feedback Loops with J-K Flip Flops
  - 20 2704 Input Decoders
  - 80 x 72 AND-Array Elements
  - 72 x 48 OR-Array Elements
  - Scan Path (Shift Register Mode) Capability of J-K Flip Flops
  - TTL Compatible
  - Single +5V Supply
  - 48 Pin Ceramic Dual-In-Line Package



**PIN CONFIGURATION**



**PIN NAMES**

I <sub>0</sub> ~ I <sub>23</sub>	Input
O <sub>0</sub> ~ O <sub>15</sub>	Outputs
ADE	Mode Control
QOT	Shift Register Output (Mode 2)
CE	Output and Mode Control
CK0	Output Latch Control
CK1	Feed Back Register Clock
RESET	Feed Back Register Reset
VCC	Power Supply (+5V)
GND	Ground