

BIPOLAR ANALOG INTEGRATED CIRCUIT $\mu PC8232T5N$

SIGe:C LOW NOISE AMPLIFIER FOR GPS

DESCRIPTION

The μ PC8232T5N is a silicon germanium carbon (SiGe:C) monolithic integrated circuit designed as low noise amplifier for GPS. This device exhibits low noise figure and high power gain characteristics, so this IC can improve the sensitivity of GPS receiver. In addition, the μ PC8232T5N which is included output matching circuit contributes to reduce external components and system size.

The package is 6-pin plastic TSON (<u>Thin Small O</u>ut-line <u>N</u>on-leaded) suitable for surface mount.

This IC is manufactured using our UHS4 (<u>Ultra High Speed Process</u>) SiGe:C bipolar process.

FEATURES

- Low noise : NF = 0.95 dB TYP. @ fin = 1575 MHz
- High gain $: G_P = 17 \text{ dB TYP}$. @ fin = 1575 MHz
- Low current consumption : Icc = 3.0 mA TYP. @ Vcc = 3.0V
- Built-in power-save function
- + High-density surface mounting : 6-pin plastic TSON package ($1.5 \times 1.5 \times 0.37$ mm)
- Included output matching circuit
- Included very robust bandgap regulator (Small Vcc and TA dependence)
- Included protection circuits for ESD

APPLICATION

• Low noise amplifier for GPS

ORDERING INFORMATION

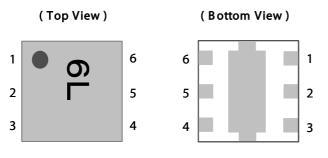
Part Number	Order Number	Package	Marking	S upplying Form
μΡC8232T5N-E2	μΡC8232T5N-E2-A	6-pin plastic TSON (Pb-Free)	6L	 8mm wide embossed taping P in 1, 6 face the perforation side of the tape Q ty 3 kpcs/reel

RemarkTo order evaluation samples, contact your nearby sales office.Part number for sample order: μ PC8232T5N

Caution Observe precautions when handling because these devices are sensitive electrostatic discharge.

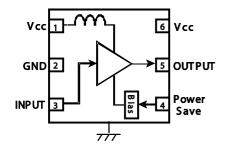
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PIN CONNECTIONS



P in No.	P in Name	
1	Vcc	
2	GND	
3	INPUT	
4	Power Save	
5	OUTPUT	
6	Vcc	
EXPOSED PAD	GND	

INTERNAL BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Parameter	S ymbol	T es t C onditions	R atings	Unit
S upply V oltage	Vcc	T _A = + 25°C	4.0	V
Power-Saving Voltage	Vps	T _A = + 25°C	4.0	V
Total Power Dissipation	P tot	T _A = + 85°C Note	150	mW
Operating Ambient Temperature	ΤA		- 40 to + 85	°C
S torage T emperature	T s tg		– 55 to + 150	°C
Input Power	Pin		+ 10	dBm

Note Mounted on double-sided copper-clad 50 \times 50 \times 1.6 mm epoxy glass PWB

RECOMMENDED OPERATING RANGE

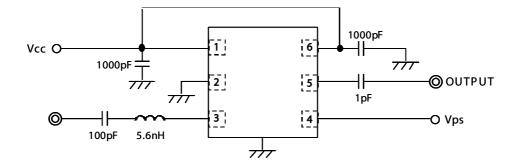
Parameter	S ymbol	MIN.	TYP.	MAX.	Unit
S upply Voltage	Vcc	2.5	3.0	3.3	V
Operating Ambient Temperature	ΤA	- 40	+ 25	+ 85	°C
Power Save Turn-on Voltage	VPSon	1.6	I	Vcc	V
Power Save Turn-off Voltage	VPSoff	0	_	0.4	V

ELECTRICAL CHARACTERISTICS

(T_A = +25°C, V_{CC} = V_{PS} = 3.0 V, fin = 1575MHz, unless otherwise specified)

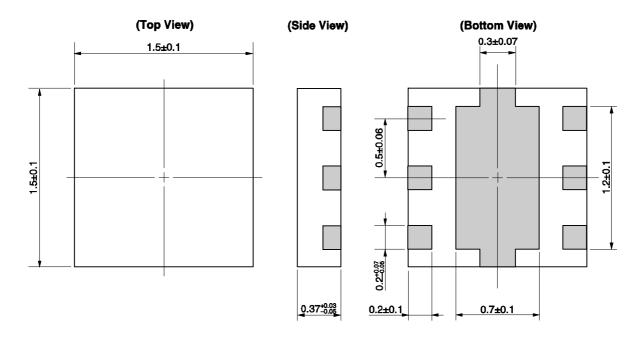
Parameter	S ymbol	T es t C onditions	MIN.	TYP.	MAX.	Unit
Circuit Current	lcc	No signal (V _{PS} = 3.0 V)	2.3	3.0	4.1	mA
		At Power-Saving Mode $(V_{PS} = 0 V)$	-	-	1	μA
Power Gain	Gp	Pin=-35dBm	15	17	19	dB
Noise Figure	NF		_	0.95	1.25	dB
Input R eturn Los s	R Lin		7	10	—	dB
Output R eturn Los s	R Lout		10	20	_	dB
Isolation	IS L		-	40	_	dB
Input 3rd Order Distortion I Intercept Point	IIP 3	fin1=1574MHz, fin2=1575MHz	_	- 8	_	dBm
Gain 1 dB Compression Intput Power	Pin(1 dB)		_	-21	_	dBm

TEST CIRCUITS



PACKAGE DIMENSIONS

6-PIN PLASTIC TSON (UNIT: mm)



NOTE ON CORRECT USE

- (1) Observe precautions for handling because of electro-static sensitive devices.
- (2) Form a ground pattern as widely as possible to minimize ground impedance (to prevent undesired oscillation). All the ground pins must be connected together with wide ground pattern to decrease impedance difference.
- (3) The bypass capacitor should be attached to Vcc line.
- (4) Do not supply DC voltage to INPUT pin.

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

S oldering Method	S oldering C onditions	Condition Symbol	
Infrared R eflow	P eak temperature (package surface temperature) T ime at peak temperature T ime at temperature of 220°C or higher P reheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of ros in flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(W t.) or below	IR 260
W ave S oldering	P eak temperature (molten solder temperature) T ime at peak temperature P reheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of ros in flux (% mass)	: 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2%(W t.) or below	W S 260
Partial Heating	P eak temperature (pin temperature) S oldering time (per side of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(W t.) or below	HS 350

Caution Do not use different soldering methods together (except for partial heating).



Subject: Compliance with EU Directives

CEL certifies, to its knowledge, that semiconductor and laser products detailed below are compliant with the requirements of European Union (EU) Directive 2002/95/EC Restriction on Use of Hazardous Substances in electrical and electronic equipment (RoHS) and the requirements of EU Directive 2003/11/EC Restriction on Penta and Octa BDE.

CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentratio in CEL		
Lead (Pb)	< 1000 PPM	-A Not Detected	-AZ (*)	
Mercury	< 1000 PPM	Not De	etected	
Cadmium	< 100 PPM	Not Detected		
Hexavalent Chromium	< 1000 PPM	Not Detected		
РВВ	< 1000 PPM	Not Detected		
PBDE	< 1000 PPM	Not Detected		

If you should have any additional questions regarding our devices and compliance to environmental standards, please do not hesitate to contact your local representative.

In no event shall CEL's liability arising out of such information exceed the total purchase price of the CEL part(s) at issue sold by CEL to customer on an annual basis.

See CEL Terms and Conditions for additional clarification of warranties and liability.

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