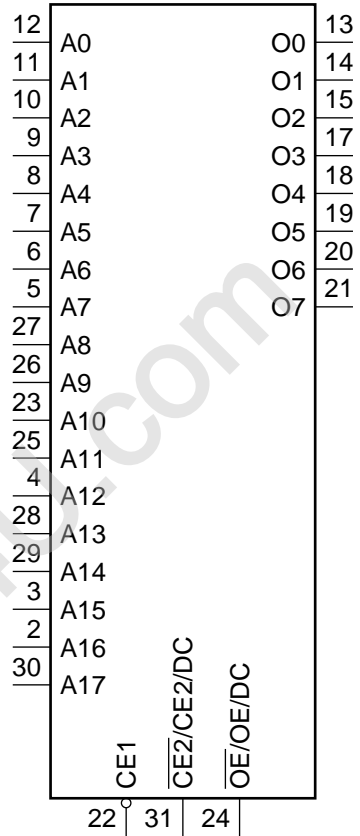
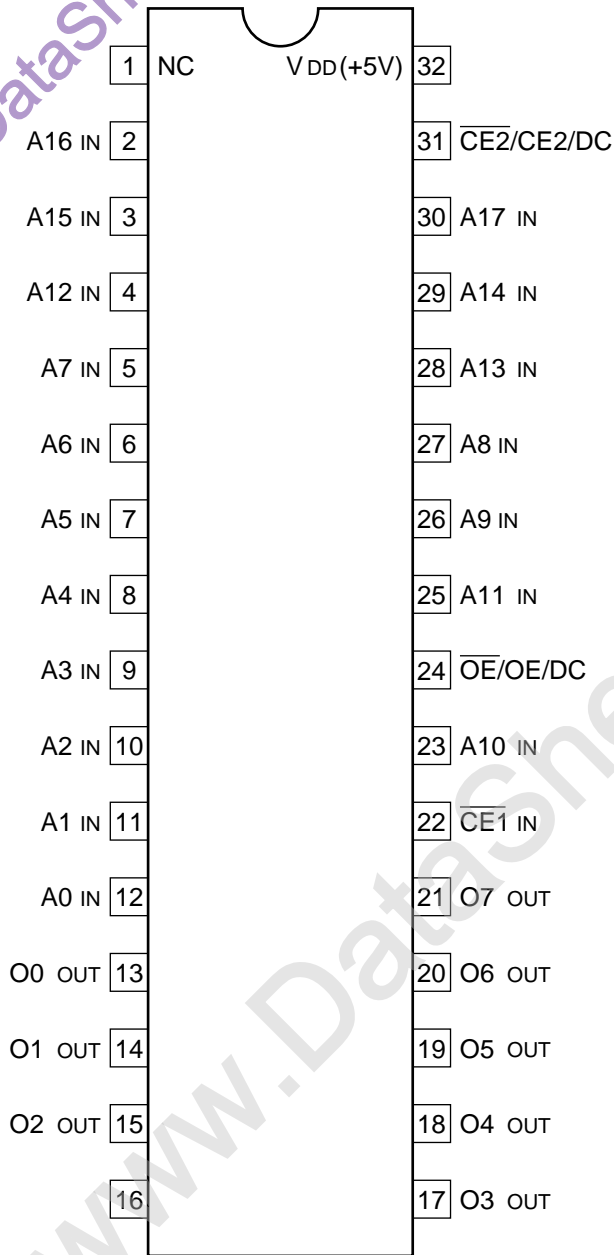
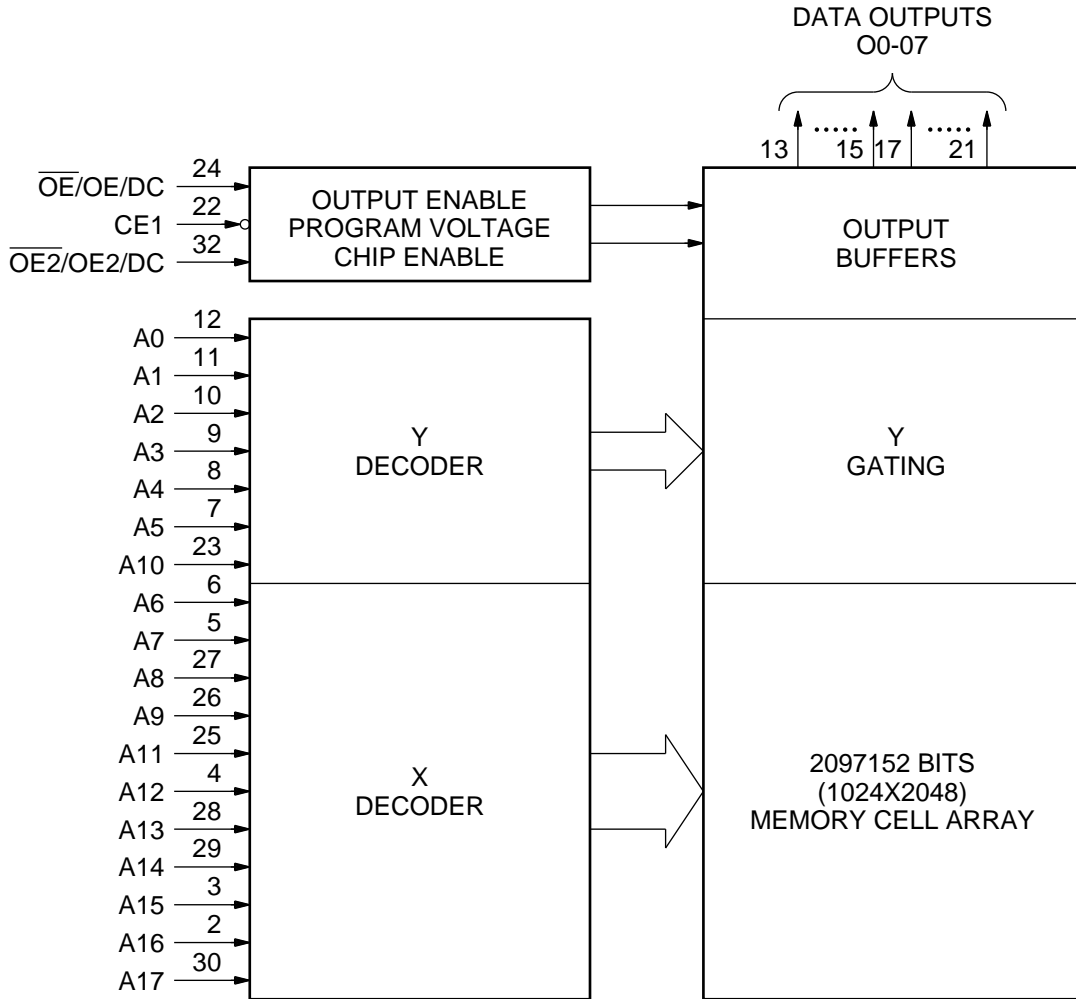

UPD27C2001GW (1/2)
IL08

C-MOS 2M (256k WX8)-BIT PROM
- TOP VIEW



A0-A17 ; ADDRESS INPUT
O0-O7 ; DATA OUTPUT
CE1, 2 ; CHIP ENABLE
OE ; OUTPUT ENABLE



| MODE \ SYMBOL | \overline{CE} | \overline{OE} | \overline{PGM} | V_{PP} | V_{DD} | O0-07 |
|-----------------|-----------------|-----------------|------------------|----------|----------|-------|
| READ | 0 | 0 | 1 | 5V | 5V | D OUT |
| OUTPUT DISABLE | 0 | 1 | X | | | Hi-Z |
| STANDBY | 1 | X | X | | | Hi-Z |
| PAGE DATA LATCH | 1 | 0 | 1 | 12.5V | 6.5V | D IN |
| PAGE PROGRAM | 1 | 1 | 0 | | | Hi-Z |
| BYTE PROGRAM | 0 | 1 | 1 | | | D IN |
| PROGRAM VERIFY | 0 | 0 | 1 | | | D OUT |
| PROGRAM INHIBIT | X | 0 | 0 | | | Hi-Z |
| | X | 1 | 1 | | | |

- 1 ; HIGH LEVEL
- 0 ; LOW LEVEL
- X ; 1 (TTL LEVEL HIGH LEVEL) or 0 (TTL LEVEL LOW LEVEL)
- Hi-Z ; HIGH IMPEDANCE