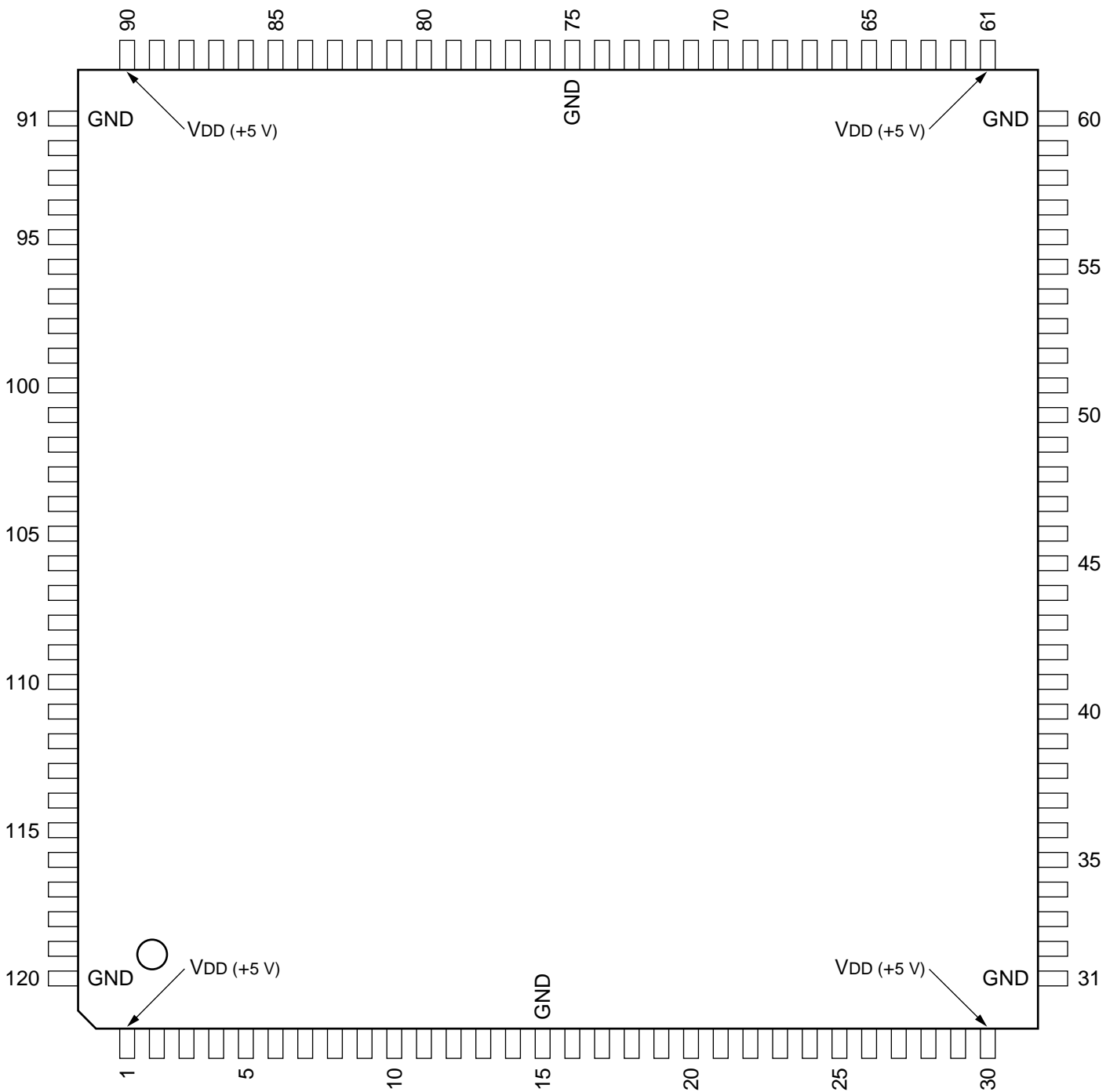


STANDARD CELL ARRAY

—TOP VIEW—



(VDD = +5 V)

| PIN NO. | I/O | SIGNAL | PIN NO. | I/O | SIGNAL | PIN NO. | I/O | SIGNAL | PIN NO. | I/O | SIGNAL |
|---------|-----|--------|---------|-----|--------|---------|-----|--------|---------|-----|--------|
| 1       | —   | VDD    | 31      | —   | GND    | 61      | —   | VDD    | 91      | —   | GND    |
| 2       | O   | DTT0   | 32      | O   | DTE    | 62      | I/O | DAA    | 92      | I   | WR     |
| 3       | O   | DTT1   | 33      | I   | DCK    | 63      | I/O | DA9    | 93      | I   | DDD0   |
| 4       | O   | DTT2   | 34      | I   | TMGP   | 64      | I/O | DA8    | 94      | I   | DDD1   |
| 5       | O   | DTT3   | 35      | I   | PRIN   | 65      | O   | CHOO   | 95      | I   | DDD2   |
| 6       | O   | DTT4   | 36      | I   | PRNS   | 66      | O   | AFOO   | 96      | I   | DDD3   |
| 7       | O   | DTT5   | 37      | I   | RESE   | 67      | O   | AAAB   | 97      | I   | DDD4   |
| 8       | O   | DTT6   | 38      | I   | LI7    | 68      | O   | ABBB   | 98      | I   | DDD5   |
| 9       | O   | DTT7   | 39      | I   | LI6    | 69      | I   | TSA    | 99      | I   | DDD6   |
| 10      | O   | DTT8   | 40      | I   | LI5    | 70      | I   | TSB    | 100     | I   | DDD7   |
| 11      | O   | DTT9   | 41      | I   | LI4    | 71      | I   | RWA    | 101     | I   | A0A    |
| 12      | O   | DTTA   | 42      | I   | LI3    | 72      | I   | RWB    | 102     | I   | A1A    |
| 13      | I   | TI07   | 43      | I   | LI2    | 73      | I   | RWC    | 103     | I   | A2A    |
| 14      | O   | TO04   | 44      | I   | LI1    | 74      | I   | LG     | 104     | I   | A3A    |
| 15      | —   | GND    | 45      | I   | LI0    | 75      | —   | GND    | 105     | I   | CS2    |
| 16      | O   | HDC    | 46      | I/O | DA7    | 76      | I/O | AD0    | 106     | I   | CS1    |
| 17      | O   | ST0B   | 47      | I/O | DA6    | 77      | I/O | AD1    | 107     | I   | CS0    |
| 18      | O   | DATA   | 48      | I/O | DA5    | 78      | I/O | AD2    | 108     | O   | TO02   |
| 19      | O   | DATB   | 49      | I/O | DA4    | 79      | I/O | AD3    | 109     | I   | TI03   |
| 20      | O   | DRV    | 50      | I/O | DA3    | 80      | I/O | AD4    | 110     | I   | TI04   |
| 21      | I   | TI08   | 51      | I/O | DA2    | 81      | I/O | AD5    | 111     | O   | TO03   |
| 22      | O   | TO05   | 52      | I/O | DA1    | 82      | I/O | AD6    | 112     | I   | TI05   |
| 23      | O   | TO01   | 53      | I/O | DA0    | 83      | I/O | AD7    | 113     | I   | TI06   |
| 24      | I   | TI01   | 54      | I/O | DAF    | 84      | I/O | AD8    | 114     | I   | TSNR   |
| 25      | I   | TI02   | 55      | I/O | DAE    | 85      | I/O | AD9    | 115     | I   | TWEB   |
| 26      | O   | TO06   | 56      | I/O | DAD    | 86      | I/O | ADA    | 116     | I   | TTOE   |
| 27      | I   | TI10   | 57      | I/O | DAC    | 87      | O   | OPTW   | 117     | I   | TTCS   |
| 28      | I   | TI11   | 58      | I/O | DAB    | 88      | O   | OPTO   | 118     | I   | CLOK   |
| 29      | I   | TI09   | 59      | I   | IOEN   | 89      | I   | OLD    | 119     | O   | OSO    |
| 30      | —   | VDD    | 60      | —   | GND    | 90      | —   | VDD    | 120     | —   | GND    |