

UPG20N60

Insulated Gate Bipolar Transistor

600V, SMPS N-CHANNEL IGBT

DESCRIPTION

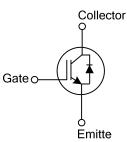
The UTC **UPG20N60** is a N-channel IGBT. it uses UTC's advanced technology to provide customers with high input impedance, high switching speed and low conduction loss, etc.

The UTC **UPG20N60** is suitable for high voltage switching, high frequency switch mode power supplies.

FEATURES

- * $V_{CE(SAT)} \le 2.8V$ @ I_C=20A, V_{GE}=15V
- * 600V Switching SOA Capability
- * High switching speed
- * High input impedance
- * Low conduction loss

SYMBOL



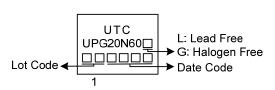
ORDERING INFORMATION

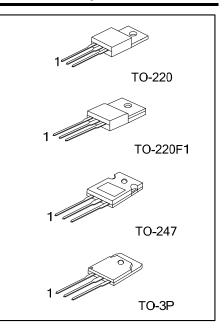
Ordering Number		Pin Assignment			Deaking	
Halogen Free	Гаскауе	1	2	3	Packing	
UPG20N60G-TA3-T	TO-220	G	С	Е	Tube	
UPG20N60G-TF1-T	TO-220F1	G	С	Е	Tube	
UPG20N60G-T3P-T	TO-3P	G	С	Е	Tube	
UPG20N60G-T47-T	TO-247	G	С	Е	Tube	
	Halogen Free UPG20N60G-TA3-T UPG20N60G-TF1-T UPG20N60G-T3P-T	Halogen FreePackageUPG20N60G-TA3-TTO-220UPG20N60G-TF1-TTO-220F1UPG20N60G-T3P-TTO-3P	Halogen FreePackageUPG20N60G-TA3-TTO-220UPG20N60G-TF1-TTO-220F1UPG20N60G-T3P-TTO-3PUPG20N60G-T3P-TTO-3P	Halogen FreePackageUPG20N60G-TA3-TTO-220GUPG20N60G-TF1-TTO-220F1GUPG20N60G-T3P-TTO-3PGUPG20N60G-T3P-TTO-3PG	Halogen Free Package 1 2 3 UPG20N60G-TA3-T TO-220 G C E UPG20N60G-TF1-T TO-220F1 G C E UPG20N60G-T3P-T TO-3P G C E	

Note: Pin Assignment: G: Gate C: Collector E: Emitter

UPG20N60G-TA3-T (1)Packing Type (2)Package Type (3)Green Package	(1) T: Tube (2) TA3: TO-220, TF1: TO-220F1, T47: TO-247 (3) G: Halogen Free and Lead Free, L: Lead Free
(3)Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free

MARKING





■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage		V _{CES}	600	V
Gate to Emitter Voltage Continuous		V _{GES}	±20	V
Continuous Collector Current	T _C =25°C	- I _C	40	А
	T _C =100°C		20	А
Collector Current Pulsed (Note 2)		I _{CM}	80	А
Peak Diode Recovery dv/dt (Note 3)	dv/dt	6.4	V/ns
Power Dissipation	TO-220	P _D	125	W
	TO-220F1		41.6	W
	TO-3P		375	W
	TO-247		300	W
Junction Temperature		TJ	-55 ~ +150	°C
Storage Temperature Range		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L=10mH, PK_{IL}=3.5A, V_{CC}=50V, R_G=25Ω, Starting T_J=25°C
- 4. $I_F \leq 8A$, di/dt $\leq 200A/\mu s$, $V_{CC} \leq BV_{CES}$, Starting $T_J=25^{\circ}C$

THERMAL CHARACTERISTICS

	PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Case	TO-220	θ _{JC}	1	°C/W	
	TO-220F1		3	°C/W	
	TO-3P		0.33	°C/W	
	TO-247		0.38	°C/W	

ELECTRICAL CHARACTERISTICS (T_=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT	
Collector-Emitter Breakdown Voltage	BV _{CES}	I _C =250μA, V _{GE} =0V		600			V	
Collector-Emitter Leakage Current	I _{CES}	V _{CE} =600V, V _{GE} =0V				10	μA	
Collector-Emitter Saturation Voltage	V _{CE(SAT)}		TJ=25°C		2.3	2.8	V	
		I _C =20A, V _{GE} =15V	Т _J =150°С		2.64		V	
Gate to Emitter Threshold Voltage	V _{GE(TH)}	I _C =250μΑ, V _{CE} = V _{GE}		4.5		6.5	V	
Gate to Emitter Leakage Current	I _{GES}	V _{CE} =0V, V _{GE} =±20V				±100	nA	
Input Capacitance	CIES	V _{CE} =25V, V _{GE} =0V, f=1MHz			1778		pF	
Output Capacitance	C _{OES}				246		pF	
Reverse Transfer Capacitance	C _{RES}]			44		pF	
Total Gate Charge	Q_G	I _C =20A, V _{CE} =50V, V _{GE} =15V			79		nC	
Gate-Emitter Charge	Q _{GE}				12.7		nC	
Gate-Collector Charge	Q _{GC}				27.4		nC	
Current Turn-On Delay Time	t _{D(ON)}	I _C =20A, V _{CE} =50V, V _{GE} =15V, R _G =10Ω			40		ns	
Current Rise Time	t _R				27.75		ns	
Current Turn-Off Delay Time	t _{D(OFF)}				160		ns	
Current Fall Time	t _F				42		ns	
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Forward Voltage Drop	V _{FM}	I _F =20A			1.5		V	
Reverse Recovery Time	t _{rr}	-I _F =20A, dl/dt=100A/μS, V _R =400V			170		ns	
Reverse Recovery Charge	Q _{rr}				0.82		μC	
Neter Dules Test Dules width < 50.0		•						

Note: Pulse Test: Pulse width \leq 50µs.



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TEST CIRCUIT AND WAVEFORMS

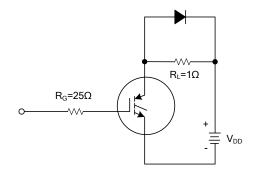


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

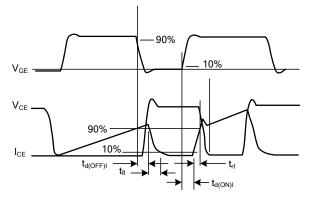
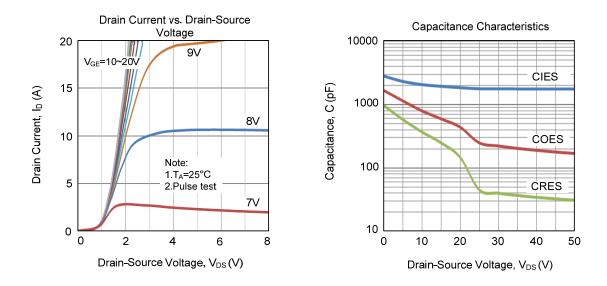


Fig 2. SWITCHING TEST WAVEFORMS



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TYPICAL CHARACTERISTICS



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