# **DATA SHEET**



# GaAs INTEGRATED CIRCUIT $\mu$ PG2151TK

# L, S-BAND SPDT SWITCH

#### **DESCRIPTION**

The  $\mu$ PG2151TK is a GaAs MMIC for L, S-band SPDT (<u>S</u>ingle <u>P</u>ole <u>D</u>ouble <u>T</u>hrow) switch which was developed for mobile phone and another L, S-band application.

This device can operate 2 control switching by control voltage 1.8 to 5.3 V. This device can operate frequency from 0.5 to 3.0 GHz, having the low insertion loss and high isolation.

This device is housed in a 6-pin lead-less minimold (1511) package. And this package is able to high-density surface mounting.

#### **FEATURES**

Switch control voltage : V<sub>cont (H)</sub> = 1.8 to 5.3 V (2.6 V TYP.)

:  $V_{cont(L)} = -0.2 \text{ to } +0.2 \text{ V (0 V TYP.)}$ 

 $\text{Low insertion loss} \qquad \qquad \text{: Lins1} = 0.30 \text{ dB TYP.} \ @ \ f = 0.5 \text{ to } 1.0 \text{ GHz}, \ V_{\text{cont (H)}} = 2.6 \text{ V}, \ V_{\text{cont (L)}} = 0 \text{ V}$ 

: LINS2 = 0.35 dB TYP. @ f = 1.0 to 2.0 GHz,  $V_{cont (H)}$  = 2.6 V,  $V_{cont (L)}$  = 0 V : LINS3 = 0.50 dB TYP. @ f = 2.0 to 3.0 GHz,  $V_{cont (H)}$  = 2.6 V,  $V_{cont (L)}$  = 0 V

 $\bullet \quad \text{High isolation} \qquad \qquad : \text{ISL1} = 25 \text{ dB TYP.} \ @ \ f = 0.5 \text{ to } 1.0 \text{ GHz}, \ V_{\text{cont (H)}} = 2.6 \text{ V}, \ V_{\text{cont (L)}} = 0 \text{ V}$ 

: ISL2 = 18 dB TYP. @ f = 1.0 to 2.0 GHz, Vcont (H) = 2.6 V, Vcont (L) = 0 V

: ISL3 = 13 dB TYP. @ f = 2.0 to 3.0 GHz,  $V_{cont(H)}$  = 2.6 V,  $V_{cont(L)}$  = 0 V

Handling power : Pin (0.1 dB) = +21.0 dBm TYP. @ f = 2.0 GHz, Vcont (H) = 2.6 V, Vcont (L) = 0 V
 High-density surface mounting : 6-pin lead-less minimold package (1.5 × 1.1 × 0.55 mm)

### **APPLICATIONS**

- · L, S-band digital cellular or cordless telephone
- W-LAN, WLL and Bluetooth<sup>™</sup> etc.

### **ORDERING INFORMATION**

Part Number	Order Number	Package	Marking	Supplying Form
μPG2151TK-E2	μPG2151TK-E2-A	6-pin lead-less minimold (1511) (Pb-Free) Note	G4N	<ul> <li>Embossed tape 8 mm wide</li> <li>Pin 1, 6 face the perforation side of the tape</li> <li>Qty 5 kpcs/reel</li> </ul>

**Note** With regards to terminal solder (the solder contains lead) plated products (conventionally plated), contact your nearby sales office.

**Remark** To order evaluation samples, contact your nearby sales office.

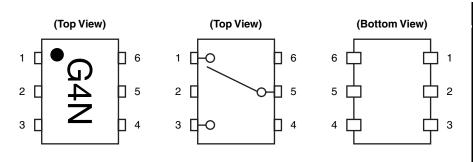
Part number for sample order:  $\mu$ PG2151TK

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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### PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM



Pin No.	Pin Name
1	OUTPUT1
2	GND
3	OUTPUT2
4	V <sub>cont</sub> 2
5	INPUT
6	V <sub>cont</sub> 1

### TRUTH TABLE

V <sub>cont</sub> 1	V <sub>cont</sub> 2	INPUT-OUTPUT1	INPUT-OUTPUT2
Low	High	OFF	ON
High	Low	ON	OFF

# ABSOLUTE MAXIMUM RATINGS (Ta = +25°C, unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Switch Control Voltage	Vcont	+6.0 Note	V
Input Power	Pin	+26	dBm
Operating Ambient Temperature	TA	-45 to +85	°C
Storage Temperature	Tstg	-55 to +150	°C

Note  $|V_{cont}1 - V_{cont}2| \le 6.0 \text{ V}$ 

# RECOMMENDED OPERATING RANGE (TA = +25°C, unless otherwise specified)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Switch Control Voltage (H)	V <sub>cont (H)</sub>	1.8	2.6	5.3	V
Switch Control Voltage (L)	V <sub>cont (L)</sub>	-0.2	0	+0.2	V



### **ELECTRICAL CHARACTERISTICS**

(TA = +25°C, Vcont (H) = 2.6 V, Vcont (L) = 0 V, DC cut capacitors = 56 pF, unless otherwise specified)

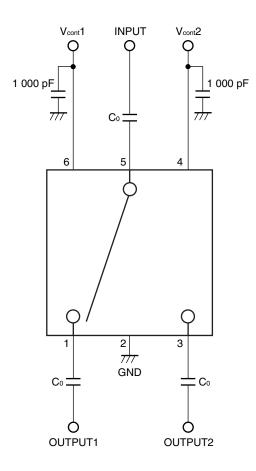
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss 1	Lins1	f = 0.5 to 1.0 GHz	-	0.30	0.42	dB
Insertion Loss 2	Lins2	f = 1.0 to 2.0 GHz	-	0.35	0.47	
Insertion Loss 3	Lins3	f = 2.0 to 3.0 GHz	-	0.50	0.65	
Isolation 1	ISL1	f = 0.5 to 1.0 GHz	22	25	-	dB
Isolation 2	ISL2	f = 1.0 to 2.0 GHz	15	18	_	
Isolation 3	ISL3	f = 2.0 to 3.0 GHz	10	13	-	
Input Return Loss	RLin	f = 0.5 to 3.0 GHz	-	20	-	dB
Output Return Loss	RLout	f = 0.5 to 3.0 GHz	-	20	-	dB
0.1 dB Loss Compression Input Power Note	Pin (0.1 dB)	f = 2.0 GHz	+18.0	+21.0	-	dBm
Switch Control Current	Icont		=	0.2	20	μΑ
Switch Control Speed	tsw	50% CTL to 90/10% RF	-	1.0	_	μs

**Note** Pin (0.1 dB) is measured the input power level when the insertion loss increases more 0.1 dB than that of linear range.

Caution This device is used it is necessary to use DC cut capacitors.

The value of DC cut capacitors should be chosen to accommodate the frequency of operation, bandwidth, switching speed and the condition with actual board of your system. The range of recommended DC cut capacitor value is less than 100 pF.

### **EVALUATION CIRCUIT**

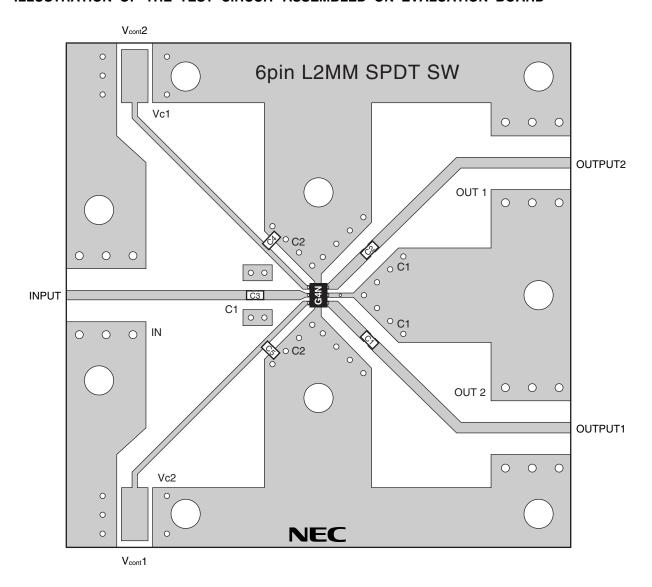


Remark Co: 56 pF

The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.



### ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD

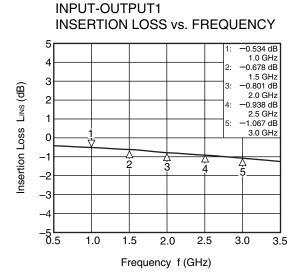


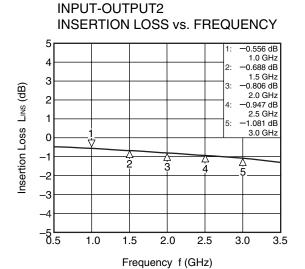
### USING THE NEC EVALUATION BOARD

Symbol	Values		
C1, C2, C3	56 pF		
C4, C5	1 000 pF		



TYPICAL CHARACTERISTICS ( $T_A = +25^{\circ}C$ ,  $V_{cont}(H) = 2.6 V$ ,  $V_{cont}(L) = 0 V$ , DC cut capacitors = 56 pF, using test fixture, unless otherwise specified)



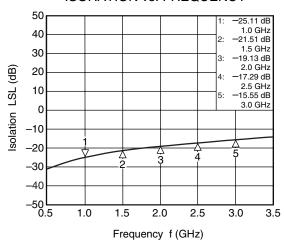


Remark The graphs indicate nominal characteristics.

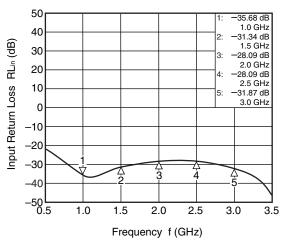
Caution These characteristics values include the losses of NEC evaluation board.



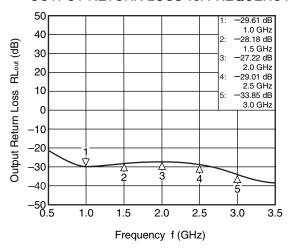
# INPUT-OUTPUT1 ISORATION vs. FREQUENCY



# INPUT-OUTPUT1 INPUT RETURN LOSS vs. FREQUENCY

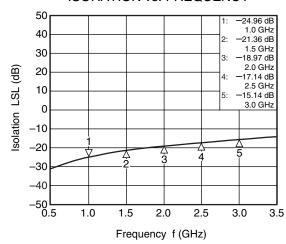


# INPUT-OUTPUT1 OUTPUT RETURN LOSS vs. FREQUENCY

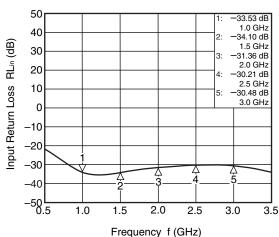


### Remark The graphs indicate nominal characteristics.

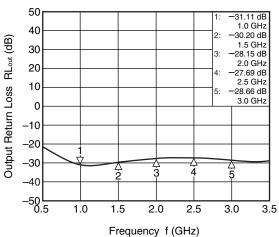
# INPUT-OUTPUT2 ISORATION vs. FREQUENCY



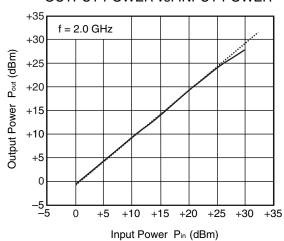
# INPUT-OUTPUT2 INPUT RETURN LOSS vs. FREQUENCY



# INPUT-OUTPUT2 OUTPUT RETURN LOSS vs. FREQUENCY



OUTPUT POWER vs. INPUT POWER

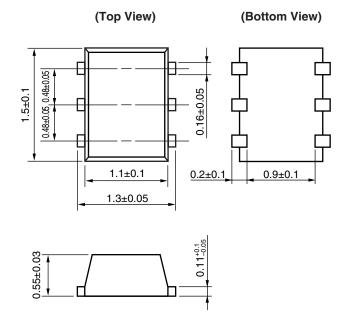


**Remark** The graph indicates nominal characteristics.



# PACKAGE DIMENSIONS

# 6-PIN LEAD-LESS MINIMOLD (1511) (UNIT: mm)





### RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions		Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below	IR260
Wave Soldering	Peak temperature (molten solder temperature) Time at peak temperature Preheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (terminal temperature) Soldering time (per side of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).



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#### Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
  - Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

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