

μ PG2419T6R

GaAs Integrated Circuit SPDT Switch for 4.0 GHz to 5.0 GHz

R09DS0006EJ0100 Rev.1.00 Sep 07, 2010

FEATURES

• Integrated DC blocking capacitors for ANT and TX ports.

• Smaller and Thin Package : 6-pin plastic TSSON (T6R) package $(1.0 \times 1.0 \times 0.37 \text{ mm})$

APPLICATIONS

This SPDT switch is developed for and used for conjunction with the CXD3267AGG and CXD3268AGW that are
designed for the TransferJet[™] specifications.

These ICs are developed and released by Sony Corporation.

ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
μPG2419T6R-E2	μPG2419T6R-E2-A	6-pin plastic	GA	Embossed tape 8 mm wide
		TSSON (T6R)		• Pin 1, 6 face the perforation side of the tape
		(Pb-Free)		Qty 5 kpcs/reel

Remark To order evaluation samples, please contact your nearby sales office.

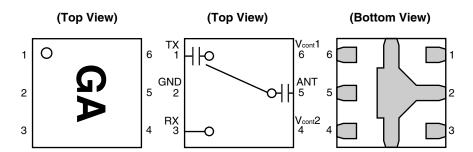
Part number for sample order: µPG2419T6R

CAUTION

Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions must be employed at all times.



PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM



Pin No.	Pin Name
1	TX
2	GND
3	RX
4	V _{cont} 2
5	ANT
6	V _{cont} 1

SW TRUTH TABLE

ON Path	V _{cont} 1	V _{cont} 2
ANT-TX	High	Low
ANT-RX	Low	High

ABSOLUTE MAXIMUM RATINGS (T_A = +25°C, unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Switch Control Voltage	V _{cont}	+6.0 Note	V
Input Power	Pin	+26.0	dBm
Power Dissipation	P_D	150	mW
Operating Ambient Temperature	T _A	-45 to +105	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Note: $|V_{cont}1 - V_{cont}2| \le 6.0 \text{ V}$

RECOMMENDED OPERATING RANGE (TA = +25°C, unless otherwise specified)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Operating Frequency	f	2.4	_	6.0	GHz
Switch Control Voltage (H)	V _{cont (H)}	2.65	3.0	3.6	V
Switch Control Voltage (L)	V _{cont (L)}	-0.2	0	0.2	V

ELECTRICAL CHARACTERISTICS

(T_A = +25°C, V_{cont (H)} = 3.0 V, V_{cont (L)} = 0 V, Z_O = 50 Ω , RX port DC blocking capacitors = 8 pF, unless otherwise specified)

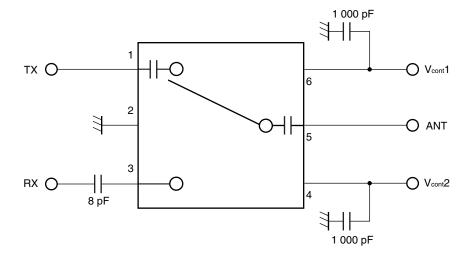
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
ANT-RX Insertion Loss 1	L _{ins} 1	f = 4.0 to 5.0 GHz	_	0.45	0.80	dB
ANT-TX Insertion Loss 2	L _{ins} 2	f = 4.0 to 5.0 GHz	-	0.55	0.80	dB
ANT-RX Insertion Loss 3	L _{ins} 3	f = 2.4 to 6.0 GHz	_	0.50	_	dB
ANT-TX Insertion Loss 4	L _{ins} 4	f = 2.4 to 6.0 GHz	_	0.80	_	dB
ANT-RX Isolation 1 (ANT-TX: ON)	ISL1	f = 4.0 to 5.0 GHz	12	17	_	dB
ANT-TX Isolation 2 (ANT-RX: ON)	ISL2	f = 4.0 to 5.0 GHz	21	26	_	dB
ANT-RX Isolation 3 (ANT-TX: ON)	ISL3	f = 2.4 to 6.0 GHz	_	15	_	dB
ANT-TX Isolation 4 (ANT-RX: ON)	ISL4	f = 2.4 to 6.0 GHz	_	25	_	dB
Return Loss 1 (ANT)	RL _{in} 1	f = 4.0 to 5.0 GHz	12	20	_	dB
Return Loss 2 (TX/RX)	RL _{in} 2	f = 4.0 to 5.0 GHz	12	20	_	dB
Return Loss 3 (ANT-RX: ON)	RL _{in} 3	f = 2.4 to 6.0 GHz	_	18	_	dB
Return Loss 4 (ANT-TX: ON)	RL _{in} 4	f = 2.4 to 6.0 GHz	_	11	_	dB
0.1 dB Loss Compression	P _{in (0.1 dB)}	f = 4.0 to 5.0 GHz	21	24	-	dBm
Input Power Note						
Switch Control Current	I _{cont}	No RF input	_	0.1	1.0	μΑ
Switch Control Speed	t _{SW}	50% CTL to 90/10% RF	_	20	100	ns

Note: $P_{in (0.1 dB)}$ is the measured input power level when the insertion loss increases 0.1 dB more than that of the linear range.

CAUTION

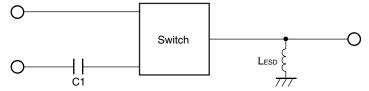
It is necessary to use DC blocking capacitor for RX port only.

EVALUATION CIRCUIT



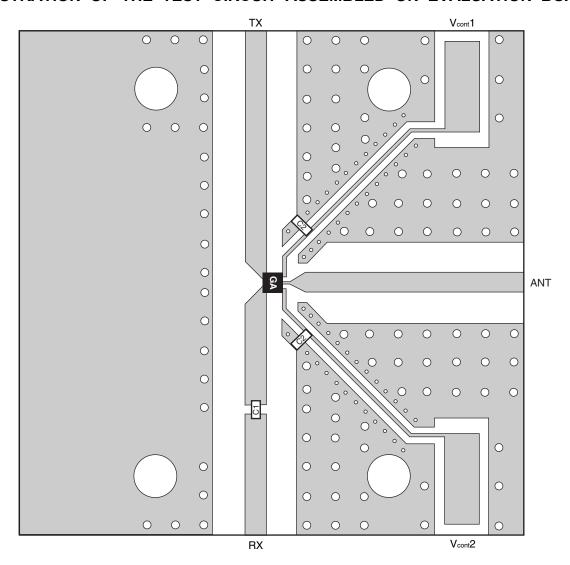
The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

APPLICATION INFORMATION



- L_{ESD} provides a means to increase the ESD protection on a specific RF port, typically the port attached to the
- The value may be tailored to provide specific electrical responses.
- The RF ground connections should be kept as short as possible and connected to directly to a good RF ground for best performance.

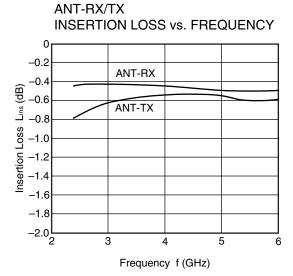
ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD

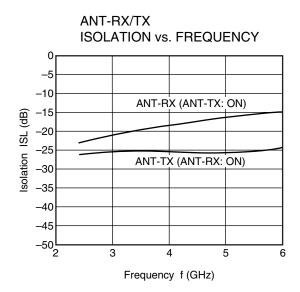


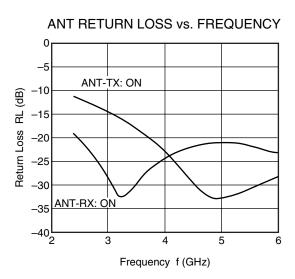
USING THE RENESAS EVALUATION BOARD

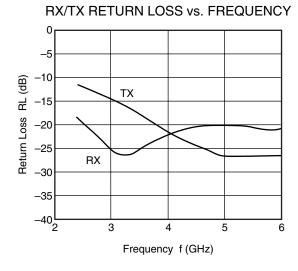
Symbol	Test Conditions	Values
C1	f = 4.0 to 5.0 GHz	8 pF
C2		1 000 pF

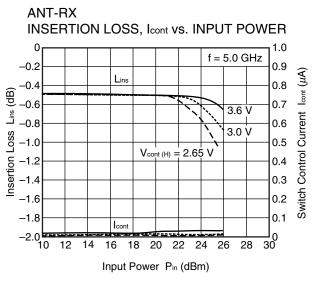
(T_A = +25°C, V_{cont (H)} = 3.0 V, V_{cont (L)} = 0 V, Z_O = 50 Ω , RX port DC blocking capacitors = 8 pF, unless otherwise specified)

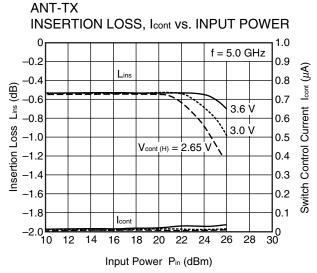






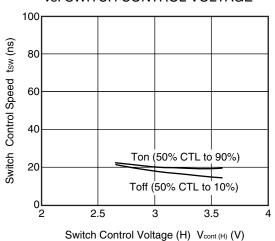






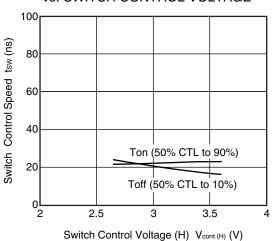
Remark The graphs indicate nominal characteristics.

ANT-TX SWITCH CONTROL SPEED vs. SWITCH CONTROL VOLTAGE



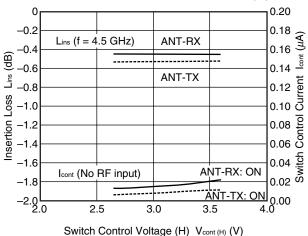
Remark The graphs indicate nominal characteristics.

ANT-RX SWITCH CONTROL SPEED vs. SWITCH CONTROL VOLTAGE

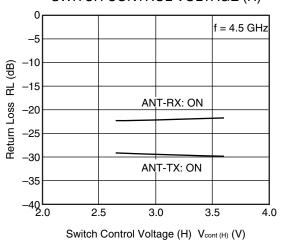


(T_A = +25°C, V_{cont (H)} = 2.65 to 3.6 V, V_{cont (L)} = 0 V, Z_O = 50 Ω , RX port DC blocking capacitors = 8 pF, unless otherwise specified)

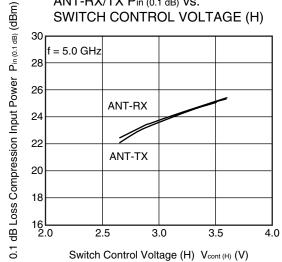




ANT RETURN LOSS vs. SWITCH CONTROL VOLTAGE (H)

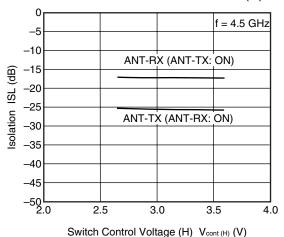


ANT-RX/TX Pin (0.1 dB) vs. SWITCH CONTROL VOLTAGE (H)

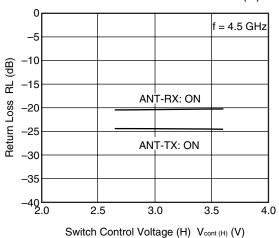


Remark The graphs indicate nominal characteristics.

ANT-RX/TX ISOLATION vs. SWITCH CONTROL VOLTAGE (H)

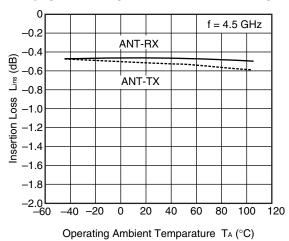


RX/TX RETURN LOSS vs. SWITCH CONTROL VOLTAGE (H)

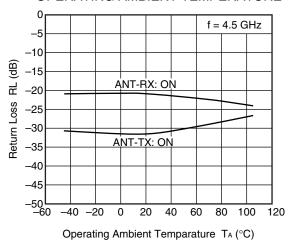


 $(T_A = -45^{\circ}C \text{ to } +105^{\circ}C, V_{cont (H)} = 3.0 \text{ V}, V_{cont (L)} = 0 \text{ V}, Z_O = 50 \Omega, RX port DC blocking}$ capacitors = 8 pF, unless otherwise specified)

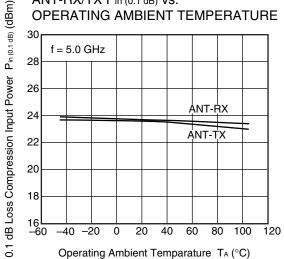
ANT-RX/TX INSERTION LOSS vs. OPERATING AMBIENT TEMPERATURE



ANT RETURN LOSS vs. **OPERATING AMBIENT TEMPERATURE**

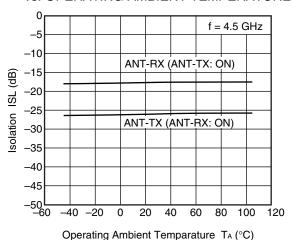


ANT-RX/TX Pin (0.1 dB) VS. **OPERATING AMBIENT TEMPERATURE**

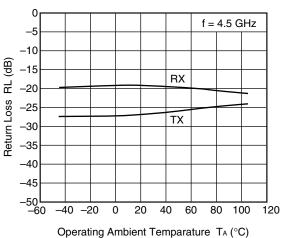


Remark The graphs indicate nominal characteristics.

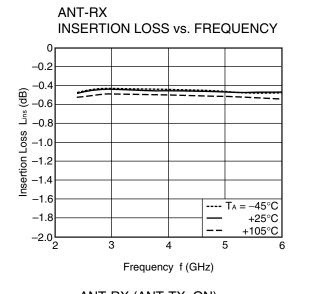
ANT-RX/TX ISOLATION vs. OPERATING AMBIENT TEMPERATURE

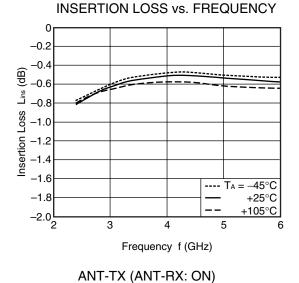


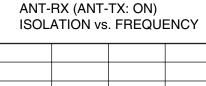
RX/TX RETURN LOSS vs. **OPERATING AMBIENT TEMPERATURE**

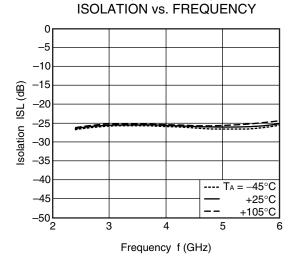


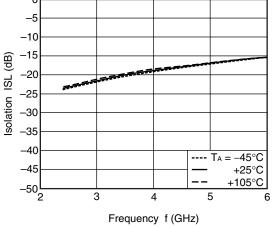
 $(V_{cont\,(H)}$ = 3.0 V, $V_{cont\,(L)}$ = 0 V, Z_O = 50 $\Omega,$ RX port DC blocking capacitors = 8 pF, unless otherwise specified)



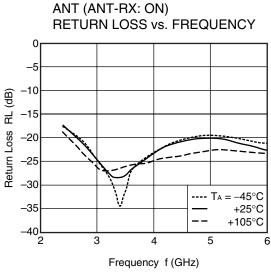


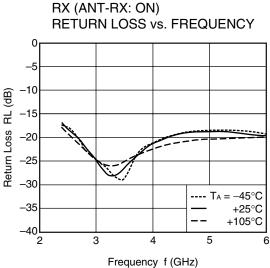




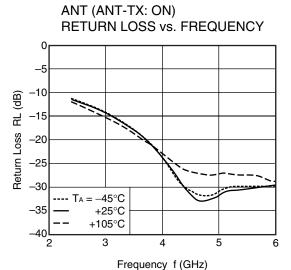


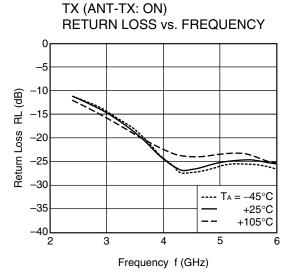
Remark The graphs indicate nominal characteristics.





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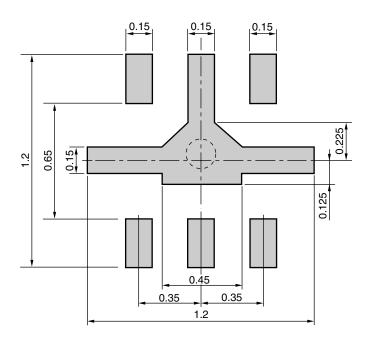


Remark The graphs indicate nominal characteristics.

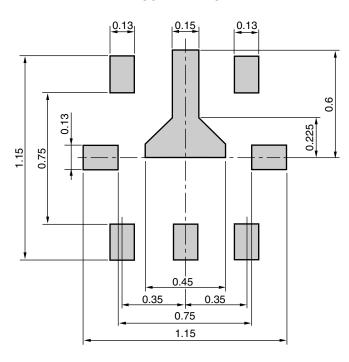
MOUNTING PAD AND SOLDER MASK LAYOUT DIMENSIONS

6-PIN PLASTIC TSSON (T6R) (UNIT: mm)

MOUNTING PAD



SOLDER MASK



Solder thickness: 0.08 mm

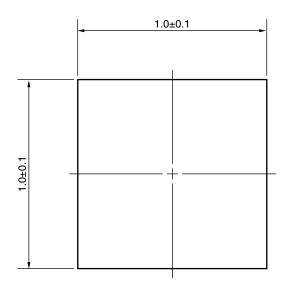
Remark The mounting pad and solder mask layouts in this document are for reference only. When designing PCB, please consider workability of mounting, solder joint reliability, prevention of solder bridge and so on, in order to optimize the design.

Sep 07, 2010

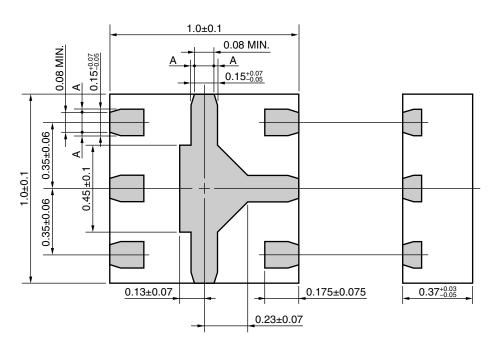
PACKAGE DIMENSIONS

6-PIN PLASTIC TSSON (T6R) (UNIT: mm)

(Top View)



(Bottom View)



Remark A>0

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions		Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature)	: 260°C or below	IR260
	Time at peak temperature	: 10 seconds or less	
	Time at temperature of 220°C or higher	: 60 seconds or less	
	Preheating time at 120 to 180°C	: 120±30 seconds	
	Maximum number of reflow processes	: 3 times	
	Maximum chlorine content of rosin flux (% mass)	: 0.2%(Wt.) or below	
Partial Heating	Peak temperature (terminal temperature)	: 350°C or below	HS350
	Soldering time (per side of device)	: 3 seconds or less	
	Maximum chlorine content of rosin flux (% mass)	: 0.2%(Wt.) or below	

CAUTION

Do not use different soldering methods together (except for partial heating).

Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
 - Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

TAC VISION I NISTON Y	Revision	History
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μ PG2419T6R Data Sheet

			Description	
Rev.	Date	Page	Summary	
1.00	Sep 07, 2010	_	First edition issued	

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Renesas Electronics America Inc. 2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A. Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited 1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K
Tel: +44-1628-585-100, Fax: +44-1628-585-900

Renesas Electronics Europe GmbH Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-6503-0, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: +852-2886-9318, Fax: +852 2886-9022/9044

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Renesas Electronics Singapore Pte. Ltd.

1 harbourFront Avenue, #06-10, keppel Bay Tower, Singapore 098632
Tel: +65-627-80-3000, Fax: +65-6278-8001
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Unit 1906, Block B, Menara Amorp, Amorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

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