UNISONIC TECHNOLOGIES CO., LTD

UPG25N120

Insulated Gate Bipolar Transistor

1200V, SMPS N-CHANNEL **IGBT**

DESCRIPTION

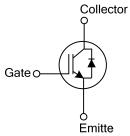
The UTC UPG25N120 is a N-channel IGBT. it uses UTC's advanced technology to provide customers with high input impedance, high switching speed and low conduction loss, etc.

The UTC UPG25N120 is suitable for high voltage switching, high frequency switch mode power supplies.

FEATURES

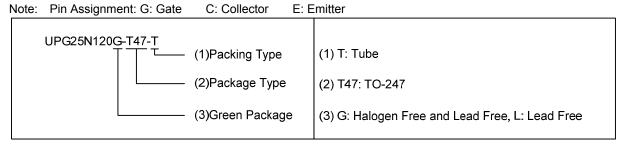
- * $V_{CE(SAT)} \le 2.8 V @ I_C=25A, V_{GE}=15V$
- * 1200V Switching SOA Capability
- * High switching speed
- * High input impedance
- * Low conduction loss

SYMBOL

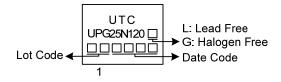


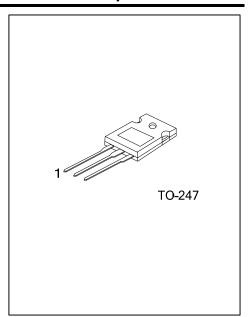
ORDERING INFORMATION

Ordering Number		Dookaga	Pin	Assignn	Dooking		
Lead Free	Halogen Free	Package	1	2	3	Packing	
UPG25N120L-T47-T	UPG25N120G-T47-T	TO-247	G	С	Е	Tube	



MARKING





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■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT	
Collector-Emitter Voltage	V _{CES}	1200	V	
Gate to Emitter Voltage Continuous	V_{GES}	±20	V	
Continuous Collector Current T _C =25°C	Ic	50	Α	
Continuous Collector Current $T_c=100^{\circ}C$		25	Α	
Collector Current Pulsed (Note 2)	I _{CM}	100	Α	
Single Pulse Avalanche Energy (Note 3)	E _{AS}	88	mJ	
Peak Diode Recovery dv/dt (Note 4)	dv/dt	4.2	V/ns	
Power Dissipation	P_{D}	350	W	
Junction Temperature	TJ	-55 ~ + 150	°C	
Storage Temperature Range	T _{STG}	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L=10mH, PK_{IL}=4.2A, V_{CC} =50V, R_G =25 Ω , Starting T_J =25 $^{\circ}C$
- 4. $I_F \le 25A$, di/dt $\le 200A/\mu s$, $V_{CC} \le BV_{CES}$, Starting $T_J = 25$ °C

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Case	θ_{JC}	0.35	°C/W

■ **ELECTRICAL CHARACTERISTICS** (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT	
Collector-Emitter Breakdown Voltage	BV_CES	I _C =250μA, V _{GE} =0V		1200			V	
Collector-Emitter Leakage Current	I _{CES}	V _{CE} =1200V, V _{GE} =0V				200	μΑ	
Callegtor Emitter Seturation Voltage	V _{CE(SAT)}	1 -254 \/ -15\/	T _J =25°C		2.3	2.8	V	
Collector-Emitter Saturation Voltage		I _C =25A, V _{GE} =15V	T _J =150°C		2.65		V	
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	I_C =250 μ A, V_{CE} = V_{GE}		5.5		7.5	V	
Gate to Emitter Leakage Current	I_{GES}	V _{CE} =0V, V _{GE} =15V				±400	nA	
Input Capacitance	C_{IES}				1557		pF	
Output Capacitance	Capacitance C_{OES} V_{CE} =25V, V_{GE} =0V, f=1MHz			168		pF		
Reverse Transfer Capacitance	C _{RES}				53.5		pF	
Total Gate Charge	Q_G	I _C =25A, V _{CE} =50V, V _{GE} =15V			89.3		nC	
Gate-Emitter Charge	Q_GE				17		nC	
Gate-Collector Charge	Q_GC				43.5		nC	
Current Turn-On Delay Time	$t_{D(ON)}$	I _C =25, V _{CE} =50V, V _{GE} =15V, R _G =10Ω			52		ns	
Current Rise Time	t_R				122		ns	
Current Turn-Off Delay Time	t _{D(OFF)}				105		ns	
Current Fall Time	t_{F}]			58.2		ns	
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Forward Voltage Drop V _{FM}		I _F =25A			2.1		V	
Reverse Recovery Time	t _{rr}	I _F =25A, dl/dt=100A/μS			210		ns	
Reverse Recovery Charge	Q_{rr}				0.54		μC	

Note: Pulse Test: Pulse width $\leq\!50\mu s.$

■ TEST CIRCUIT AND WAVEFORMS

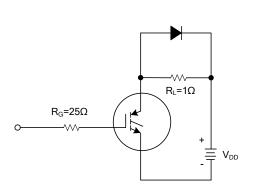


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

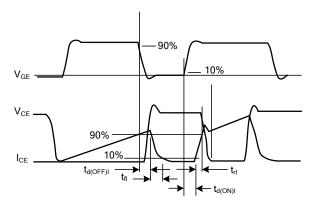
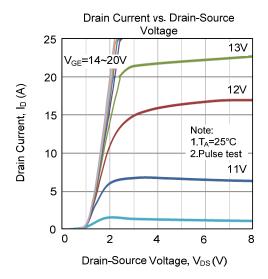
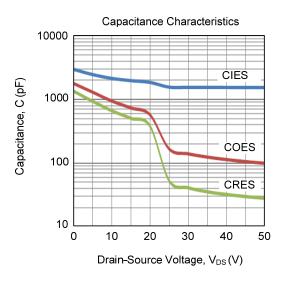


Fig 2. SWITCHING TEST WAVEFORMS

■ TYPICAL CHARACTERISTICS





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