UNISONIC TECHNOLOGIES CO., LTD

UPG25N60

Insulated Gate Bipolar Transistor

600V, SMPS N-CHANNEL IGBT

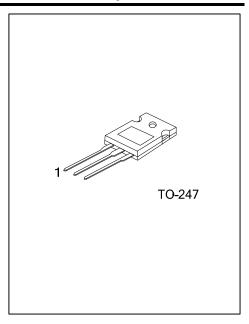
DESCRIPTION

The UTC **UPG25N60** is a N-channel IGBT. it uses UTC's advanced technology to provide customers with high input impedance, high switching speed and low conduction loss, etc.

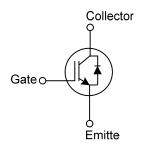
The UTC **UPG25N60** is suitable for high voltage switching, high frequency switch mode power supplies.

■ FEATURES

- * $V_{CE(SAT)} \le 2.5V @ I_C = 25A, V_{GE} = 15V$
- * 600V Switching SOA Capability
- * High switching speed
- * High input impedance
- * Low conduction loss

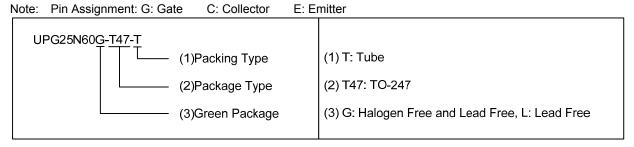


■ SYMBOL

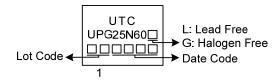


■ ORDERING INFORMATION

Ordering Number		Dooksaya	Pin .	Assignn	Dealing		
Lead Free	Halogen Free	Package	1	2	3	Packing	
UPG25N60L-T47-T	UPG25N60G-T47-T	TO-247	G	С	Е	Tube	



MARKING



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■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage		V_{CES}	600	V
Gate to Emitter Voltage Continuous		V_{GES}	±20	V
Cantinuous Callagter Current	T _C =25°C	Ic	50	Α
Continuous Collector Current	T _C =100°C		25	Α
Collector Current Pulsed (Note 2)		I _{CM}	100	Α
Peak Diode Recovery dv/dt (Note 3)		dv/dt	6.1	V/ns
Power Dissipation		P_{D}	250	W
Junction Temperature		TJ	-55 ~ + 150	°C
Storage Temperature Range		T _{STG}	-55 ~ + 150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L=10mH, PK_{IL} =3.0A, V_{CC} =50V, R_G =25 Ω , Starting T_J =25 $^{\circ}C$
- 3. $I_F \le 25A$, di/dt $\le 200A/\mu s$, $V_{CC} \le BV_{CES}$, Starting $T_J = 25^{\circ}C$

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Case	θ_{JC}	0.5	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Collector-Emitter Breakdown Voltage	BV _{CES}	I _C =250μA, V _{GE} =0V		600			V
Collector-Emitter Leakage Current	I _{CES}	V _{CE} =600V, V _{GE} =0V				10	μΑ
Gate to Emitter Leakage Current	I _{GES}	V _{CE} =0V, V _{GE} =±60V				±400	nA
ON CHARACTERISTICS							
Collector-Emitter Saturation Voltage	W	I _C =25A, V _{GE} =15V	T _J =25°C		2.0	2.5	V
	$V_{CE(SAT)}$		T _J =150°C		2.4		V
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	$I_C=250\mu A, V_{CE}=V_{GE}$				6.5	V
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{IES}	V _{CE} =25V, V _{GE} =0V, f=1MHz			2726		pF
Output Capacitance	C _{OES}				343		pF
Reverse Transfer Capacitance	C _{RES}				67.5		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge	Q_G	I _C =25A, V _{CE} =50V, V _{GE} =15V			126.5		nC
Gate-Emitter Charge	Q_GE				18.3		nC
Gate-Collector Charge	Q_{GC}				42.8		nC
Current Turn-On Delay Time	t _{D(ON)}	I_{C} =25A, V_{CE} =50V, V_{GE} =15V, R_{G} =10 Ω			50.5		ns
Current Rise Time	t _R				37.8		ns
Current Turn-Off Delay Time	t _{D(OFF)}				225		ns
Current Fall Time	t _F				59		ns
DRAIN-SOURCE DIODE CHARACTER	ISTICS						
Forward Voltage Drop	V_{FM}	I _F =25A			1.2		V
Reverse Recovery Time	t _{rr}	-I _F =25A, dI/dt=100A/μS, V _{CC} =400V			822		ns
Reverse Recovery Charge	Qrr				0.65		nC

Note: Pulse Test: Pulse width ≤ 50 µs.

■ TEST CIRCUIT AND WAVEFORMS

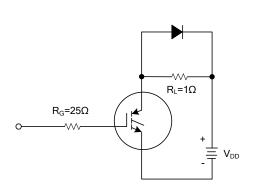


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

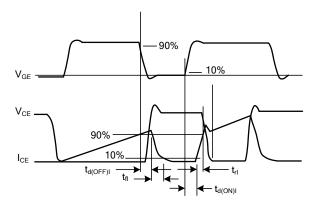
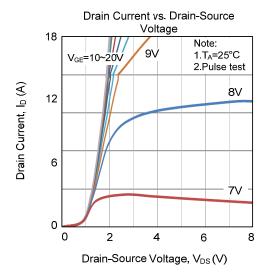
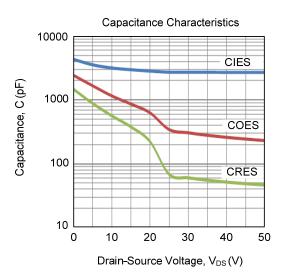


Fig 2. SWITCHING TEST WAVEFORMS

■ TYPICAL CHARACTERISTICS





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