

**UPSSL150**

Preliminary

LINEAR INTEGRATED CIRCUIT**NON ISOLATED BUCK
CONSTANT CURRENT LED
DRIVER IC****■ DESCRIPTION**

UPSSL150 is a high precision LED BUCK constant current driver chip. The chip operates in the inductor current critical continuous mode, which is suitable for the non isolated buck LED constant current power supply with 85Vac~265Vac full range input voltage.

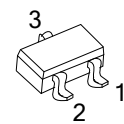
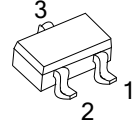
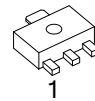
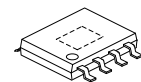
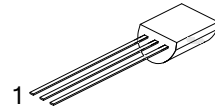
UPSSL150 chip integrated 500V power switch by patent demagnetization detection technology and high voltage power supply technology, without detection and power supply for auxiliary winding, the peripheral devices are more simple, saving the cost and volume of the system.

UPSSL150 chip with high precision current sampling circuit, while the use of patented constant current control technology to achieve high accuracy of the LED constant current output and excellent line voltage regulator. The chip operates in the critical mode of inductor current and the output current is not changed with the increase of the inductance and the working voltage of LED.

UPSSL150 has a variety of protection features, including LED short circuit protection, under voltage protection, over temperature adjustment function and so on.

■ FEATURES

- * The integrated 500V power mos.
- * The integrated high voltage power supply function.
- * The inductor current critical continuous mode.
- * Without auxiliary winding detection and power supply.
- * The wide voltage input voltage.
- * $\pm 5\%$ LED output current accuracy.
- * The LED short circuit and open circuit protection.
- * The chip power supply under voltage protection.
- * The regulating function of overheating

**SOT-23-3**
(JEDEC TO-236)**SOT-23**
(JEDEC SC-59)**SOT-89****HSOP-8****TO-92**

■ ORDERING INFORMATION

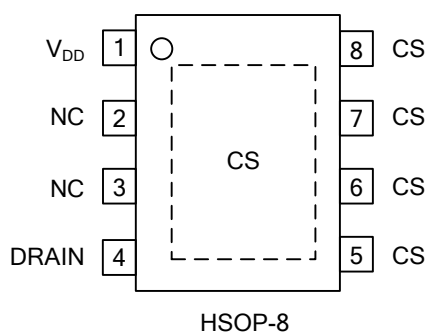
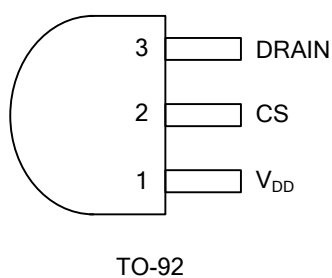
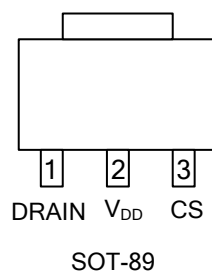
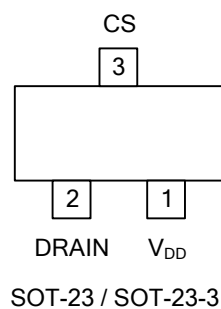
Ordering Number		Package	Packing
Lead Free	Halogen Free		
-	UPSSL150AG-AB3-R	SOT-89	Tape Reel
-	UPSSL150AG-AE2-R	SOT-23-3	Tape Reel
-	UPSSL150AG-AE3-R	SOT-23	Tape Reel
-	UPSSL150AG-SH2-R	HSOP-8	Tape Reel
UPSSL150AL-T92-B	UPSSL150AG-T92-B	TO-92	Tape Box
UPSSL150AL-T92-K	UPSSL150AG-T92-K	TO-92	Bulk
-	UPSSL150BG-AB3-R	SOT-89	Tape Reel
-	UPSSL150BG-AE2-R	SOT-23-3	Tape Reel
-	UPSSL150BG-AE3-R	SOT-23	Tape Reel
-	UPSSL150BG-SH2-R	HSOP-8	Tape Reel
UPSSL150BL-T92-B	UPSSL150BG-T92-B	TO-92	Tape Box
UPSSL150BL-T92-K	UPSSL150BG-T92-K	TO-92	Bulk
-	UPSSL150CG-AB3-R	SOT-89	Tape Reel
-	UPSSL150CG-AE2-R	SOT-23-3	Tape Reel
-	UPSSL150CG-AE3-R	SOT-23	Tape Reel
-	UPSSL150CG-SH2-R	HSOP-8	Tape Reel
UPSSL150CL-T92-C	UPSSL150CG-T92-C	TO-92	Tape Box
UPSSL150CL-T92-K	UPSSL150CG-T92-K	TO-92	Bulk

UPSSL150AG-AE2-R (1) Packing Type (2) Package Type (3) Green Package (4) I_{LED}, RON	(1) R: Tape Reel, B: Tape Box, K: Bulk (2) AB3: SOT-89, AE2: SOT-23-3, AE3: SOT-23, SH2: HSOP-8, T92: TO-92 (3) G: Halogen Free and Lead Free, L: Lead Free (4) refer to ELECTRICAL CHARACTERISTICS
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■ MARKING

SOT-23-3 / SOT-23 I_{LED}, RON	SOT-89 I_{LED}, RON
HSOP-8 I_{LED}, RON	TO-92 I_{LED}, RON

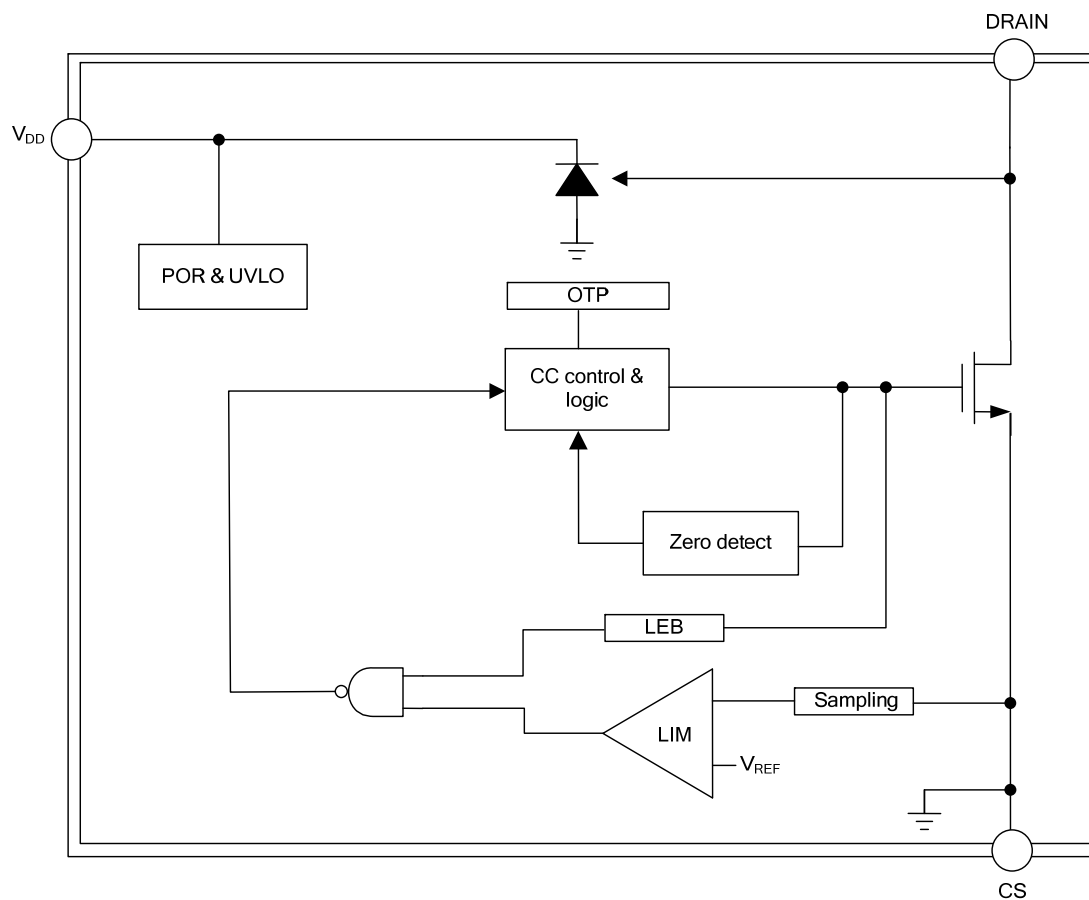
PIN CONFIGURATION



PIN DESCRIPTION

PIN NO.				PIN NAME	DESCRIPTION
SOT-23 SOT-23-3	SOT-89	HSOP-8	TO-92		
1	2	1	1	V _{DD}	Power Supply
2	1	4	3	DRAIN	The Internal High-Voltage Drain
3	3	5, 6, 7, 8	2	CS	Float Ground and Current Sampling Pin
-	-	2, 3	-	NC	

■ BLOCK DIAGRAM



■ **ABSOLUTE MAXIMUM RATING** ($T_A=25^{\circ}\text{C}$, Unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Input Voltage		V_{IN}	-0.3 ~ 550	V
Supply Voltage		V_{DD}	-0.3 ~ 8.5	V
Power Dissipation	SOT-23	P_D	300	mW
	SOT-23-3			
	SOT-89			mW
	HSOP-8			mW
			625	mW
Junction Temperature		T_J	-45 ~ +125	$^{\circ}\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^{\circ}\text{C}$

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ **RECOMMENDED OPERATION CONDITIONS**

PARAMETER		SYMBOL	RATINGS	UNIT
Input Voltage		V_{IN}	140 ~ 375	V
Supply Voltage		V_{DD}	7 ~ 8	V
Reference Load Current		I_{LOAD}	0 ~ 200	mA
Ambient Temperature		T_A	0 ~ +70	$^{\circ}\text{C}$

■ **THERMAL RESISTANCES CHARACTERISTICS**

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	SOT-23	θ_{JA}	416	$^{\circ}\text{C/W}$
	SOT-23-3			
	SOT-89			$^{\circ}\text{C/W}$
	HSOP-8			$^{\circ}\text{C/W}$
			200	$^{\circ}\text{C/W}$

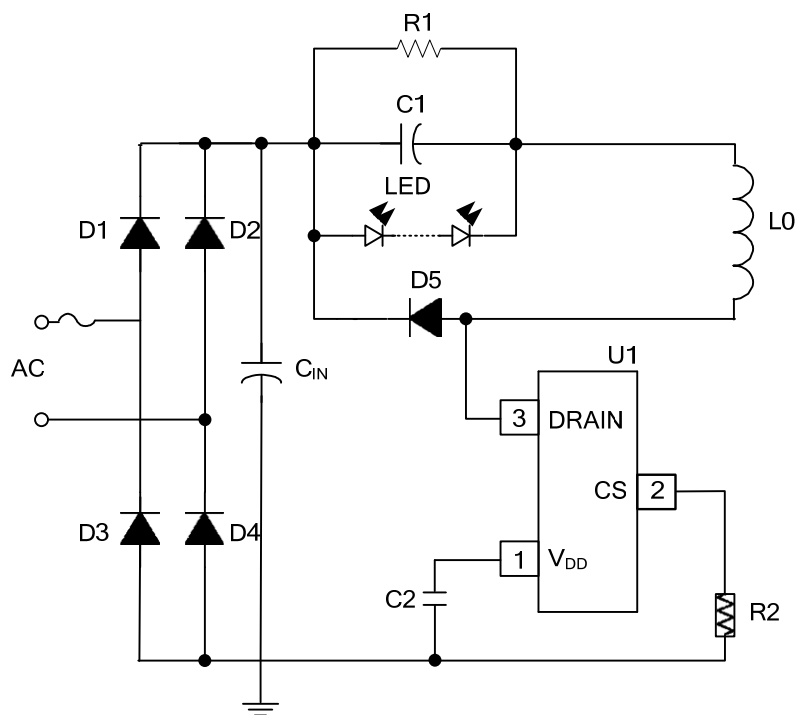
■ **ELECTRICAL CHARACTERISTICS** ($T_A=25^{\circ}\text{C}$, Unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
POWER SUPPLY SECTION						
Input Voltage	V _{IN}		85	220	265	V
Chip Operating Voltage	V _{DD}	DRAIN=100V	7	7.3	7.8	V
Chip Starting Voltage	V _{DD ON}	V _{DD} rise	6	6.2	6.5	V
Chip Starting Current	I _{ST}	V _{DD} =6.2V	0.5	1	2	mA
Chip Operating Current	I _{CC}		120	180	300	uA
Power Efficiency	η	I _{LED} =100mA	0.88			
Power Factor	P _F	I _{LED} =100mA	0.4	0.5	0.7	
CC SECTION						
The Precision of Constant Current	CC	I _{LED} =0mA~200mA		±5		%
Standby Power Consumption					0.3	W
PROTECTION SECTION						
Short Circuit Protection Frequency	F _{ST}	V _{IN} =220AC RCS=3Ω	2	3	4	KHz
Current Detection Threshold	V _{CS_TH}		580	600	620	mV
Edge Blanking Time	T _{LEB}			500		ns
Turn Off Delay Time	T _{OFF_DELAY}			200		ns
TIME CONTROL SECTION						
Minimum Turn Off Time	T _{OFF_MIN}			2.5		us
Maximum Turn off Time	T _{OFF_MAX}			300		us
Maximum Turn-on Time	T _{ON_MAX}			40		us
POWER MOS SECTION						
LDMOS Withstand Voltage	B _V		500	550	600	V
Conduction Resistance	RON	I _{DS} =0.1A	UPSSL150A	30		Ω
			UPSSL150B	18		Ω
			UPSSL150C	12		Ω
TEMPER SECTION						
Over Temper Adjust				140		°C
Over Temper Protect				160		°C
Over Temper Hysteresis				20		°C

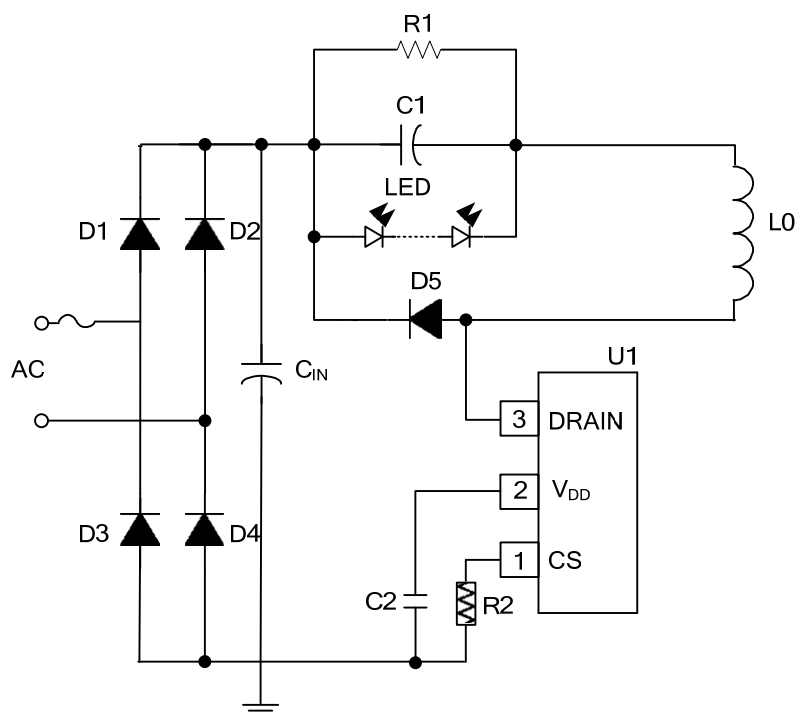
Notes: 1. The parameters are not 100% tested in production.

2. The minimum, maximum range of standard specification by the test to ensure, typical values by design, test or analysis to ensure.

■ TYPICAL APPLICATION CIRCUIT

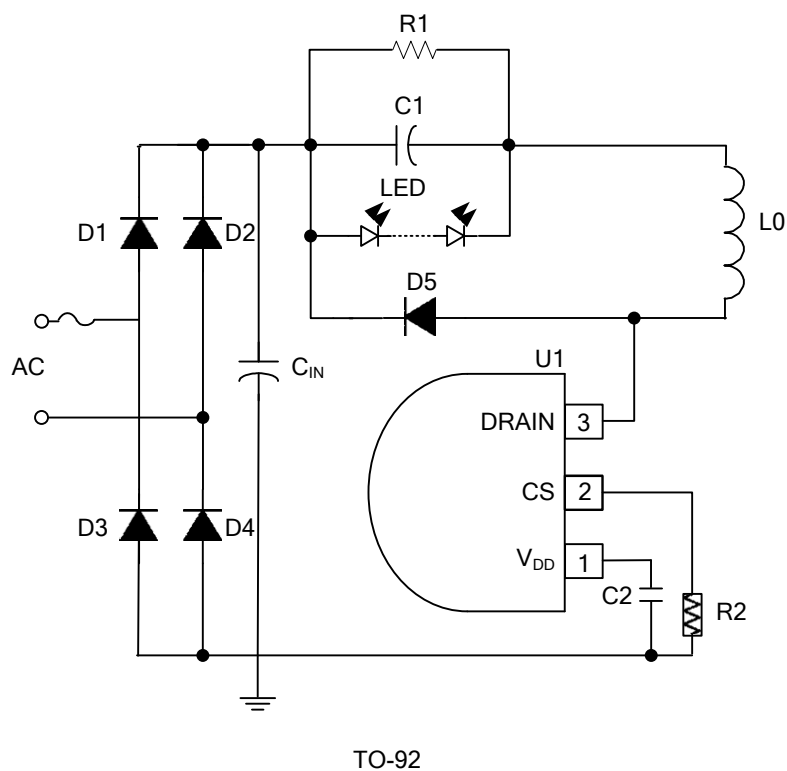
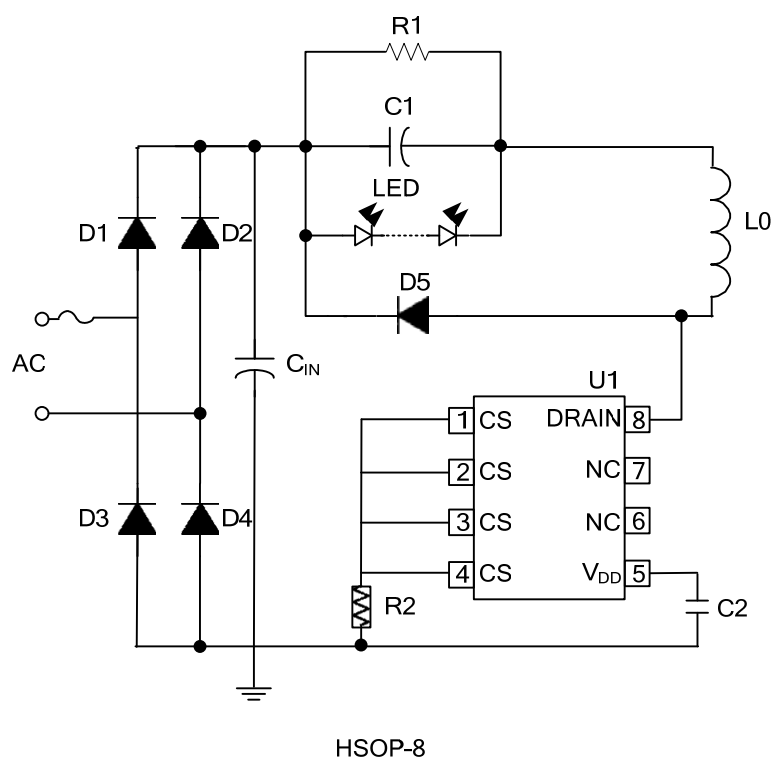


SOT-23-3 / SOT-23



SOT-89

■ TYPICAL APPLICATION CIRCUIT (Cont.)



■ **TYPICAL APPLICATION CIRCUIT (Cont.)****BOM**

Reference	Component
R1	750K, 1206
R2	3Ω, 1206
C1	2.2nF / 1KV, 1206
C2	2.2nF / 50V, 0805
C _{IN}	6.8μF, 400V
D1 ~ D4	1N4007
D5	ES1J, SMA
L0	4.7mH
U1	UPSSL150

■ APPLICATION INFORMATION

UPSSL150 is a constant current driver IC for LED lighting, which is applied to the non isolated LED driver power supply.

The 500V power switch is integrated in the chip, which is based on the constant current architecture and control method, and it can achieve excellent constant current characteristics with minimal external components.

And without auxiliary winding power supply and testing, the system cost is very low.

Start

After the system is powered up, the input voltage is charged on the V_{DD} pin through the jfet, and when the V_{DD} voltage reaches the threshold value, the chip's internal reference circuit begins to work. When the chip is working normally, the required operating current is still through the supply of internal consumption.

Constant current control

The chip is detected the peak current, and the RCS resistance is connected to the input terminal of the peak comparator, which is compared with the threshold voltage 0.6V. When the RCS voltage reaches the threshold, the LED current is adjusted, and realized the constant current control.

The formula for calculating the inductor peak current is $I_{PK}=600/RCS$ (mA).

Among them, RCS is a current sampling resistor.

The output of the comparator CS also includes a 500ns leading edge blanking time.

The formula for calculating the LED average current is $I_{LED}=I_{PK}/2$.

Among them, I_{PK} is the inductor peak current.

Protection function

UPSSL150 built in a variety of protection functions, including LED short-circuit protection, LED open circuit protection, sampling circuit short-circuit protection, temperature regulation and protection, etc.

When LED short circuit, the system works in the 3 KHz low frequency, so the power consumption is very low.

Over temperature adjustment function

UPSSL150 has the function of over temperature adjustment, the output current is gradually reduced when the driving power is over, so that the output power and temperature rise is controlled, the power supply is maintained at the set value,

In order to improve the reliability of the system, the chip is internally set to adjust the temperature of the 140°C.

PCB design

In the design of PCB, you need to follow the following guidelines:

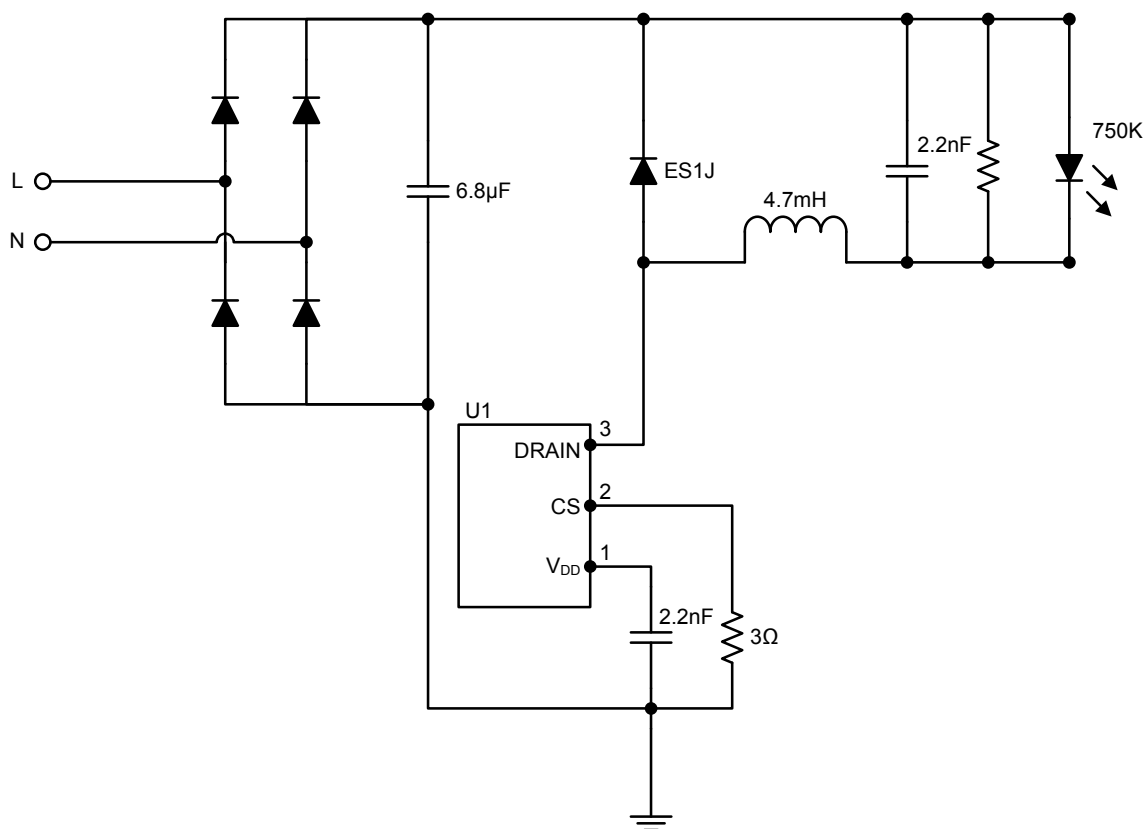
V_{DD} bypass capacitor is required to close the V_{DD} pin of the chip.

RCS resistor requires close to the CS pin of the chip

Between RCS and V_{DD} bypass capacitor resistor connected to the copper foil as short as possible

CS pin increases in the area of copper clad to improve chip cooling.

■ TYPICAL CHARACTERISTICS



■ TEST CIRCUIT

Table 1. Efficiency and power factor test

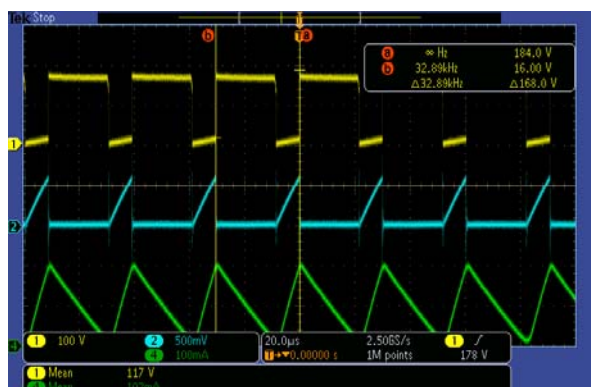
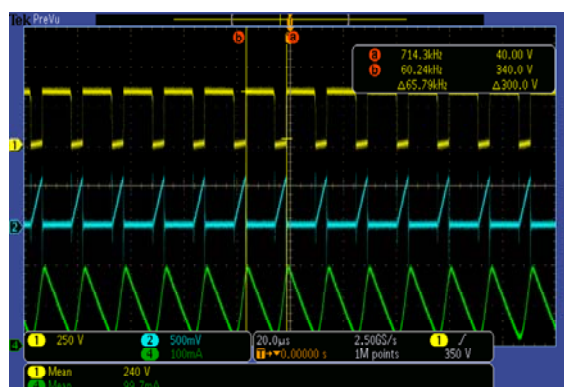
V_i	pF	V_o (V)	I_o (A)	P_i (W)	η
85V	---	61	0.0962	6.6	88.91%
115V	0.552	61	0.1031	7.04	89.33%
132V	---	60.4	0.102	6.92	89.03%
180V	---	117	0.1026	12.9	93.06%
230V	0.496	116	0.1008	12.65	92.43%
264V	---	116	0.1006	12.64	92.32%

Table 2. Line Regulation & Load Regulation test

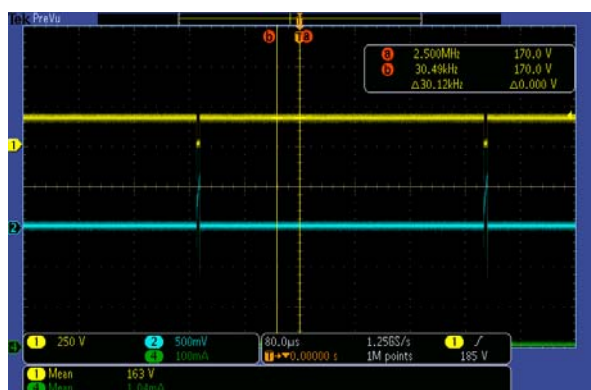
V_{IN} \ VLED	60V	80V	120V	Load Regulation%
180VAC	0.1042	0.104	0.1042	$\pm 0.1\%$
230VAC	0.1043	0.1038	0.1021	$\pm 1.1\%$
264VAC	0.1045	0.1038	0.1017	$\pm 1.4\%$
Line Regulation%	$\pm 0.15\%$	$\pm 0.1\%$	$\pm 1.25\%$	

■ TEST CIRCUIT (Cont.)

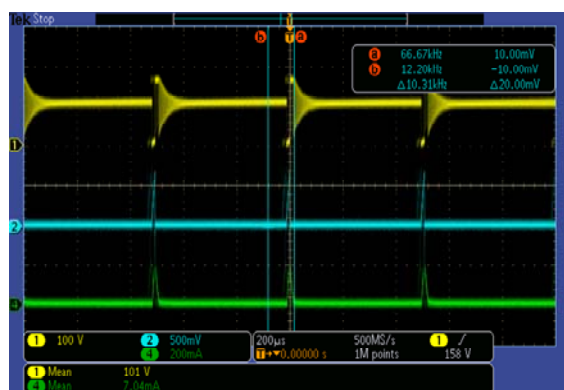
Crucial waveforms

 $V_i=115V$, $f_{sw}=33KHZ$  $V_i=230V$, $f_{sw}=66KHZ$

Short circuit protection test



Over-temperature protection test



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