

**US205**

Preliminary

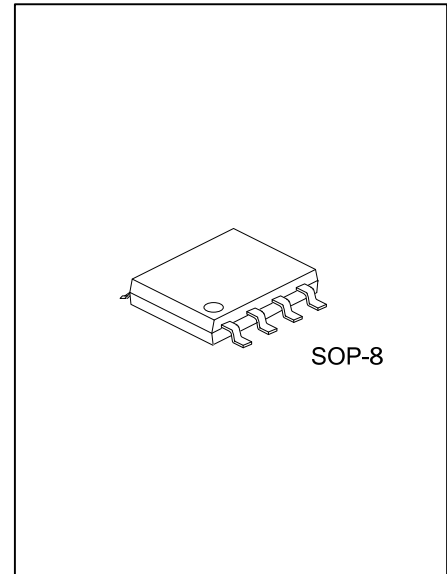
CMOS IC**100mΩ, 2A SLEW RATE
CONTROLLED LOAD SWITCH****■ DESCRIPTION**

The UTC **US205** is a cost-effective, low-voltage, low switch-on resistance (typ. 100mΩ) high-side Power Switch IC. It has a 20μA low quiescent supply current that meets the requirements of battery-powered portable equipment.

The UTC **US205** incorporates protection circuits, such as an over-current protection circuit, a short fold back circuit, a thermal shutdown circuit and an under-voltage lockout circuit for overall protection. Besides, a slew rate controlled function is embedded for turn-on rising time control.

The UTC **US205** can be used in applications, such as Cellular Phones, Digital still Camera, Hot swap Supplies, Notebook Computers, Personal Communication Devices, Personal Digital Assistants and so on.

The UTC **US205** is available in SOP-8 package.

**■ FEATURES**

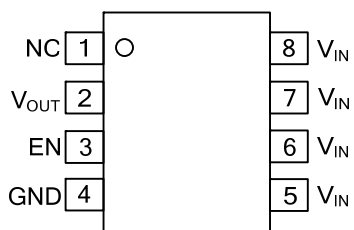
- * High-Side NMOSFET: 100mΩ (TYP)
- * Input Voltage Varies From 2.7V to 5.5V
- * Low Supply Current: 20μA
- * Over Current Protection
- * Short Circuit Protection
- * Under Voltage Lockout
- * Thermal Protection with Foldback
- * Slew Rate Limited Turn-On Time 3ms (5V)
- * Reverse Blocking Current
- * RoHS Compliant

■ ORDERING INFORMATION

Ordering Number		Package	Packing
Lead Free	Halogen Free		
US205XL-S08-R	US205XG-S08-R	SOP-8	Tape Reel

US205XL-S08-R (1) Packing Type (2) Package Type (3) Lead Free (4) Logic Level Enable Pin	(1) R: Tape Reel (2) S08: SOP-8 (3) G: Halogen Free, L: Lead Free (4) H: Active High, L: Active Low
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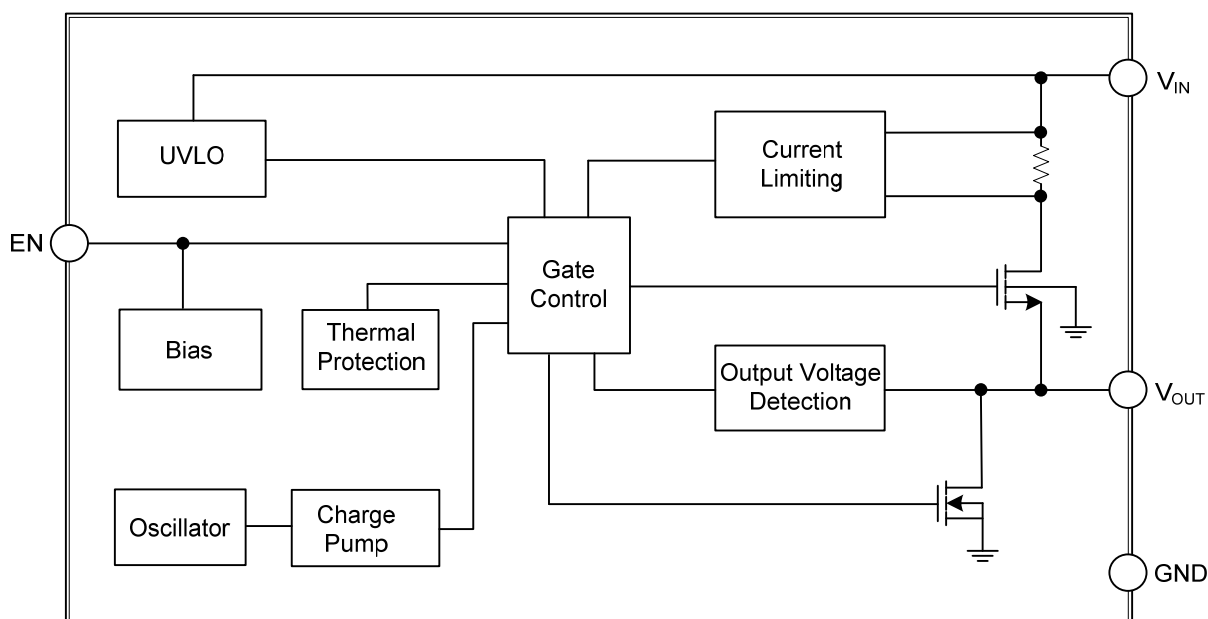
PIN CONFIGURATION



PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	NC	No Internal Connection.
2	V _{OUT}	Power-Switch Output.
3	EN	Chip Enable (Active High).
4	GND	Ground. The exposed pad must be soldered to a large PCB and connected to GND for maximum power dissipation.
5, 6, 7, 8	V _{IN}	Power Input Voltage.

BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATING (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V _{IN}	6	V
Enable Input Voltage	EN	-0.3~6	V
Power Dissipation, @T _A =25°C	P _D	0.3	W
Junction Temperature	T _J	150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.
Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ RECOMMENDED OPERATING CONDITIONS (T_A=25°C, unless otherwise specified) (Note)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V _{IN}	2.7~5.5	V
Enable Input Voltage	EN	0~5.5	V
Junction Temperature	T _J	-40~100	°C
Ambient Temperature	T _A	-40~85	°C

Note: The device is not guaranteed to function outside its operating conditions.

■ ELECTRICAL CHARACTERISTICS

(V_{IN}=5V, C_{IN}=1uF, C_{OUT}=0.1uF, T_A=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Operation Voltage		V _{IN}		2.7		5	V
Under Voltage Lookout		V _{UVLO}	V _{IN} Falling	1.3	1.7	2.1	V
Under Voltage Lookout Hysteresis		ΔV _{UVLO}			50		mV
Quiescent Current		I _Q	EN=High		20	70	μA
Off Supply Current		I _{SHDN}	EN=Low, V _{OUT} =Open			1	μA
Off Switch Current		I _{LEAKAGE}	EN=Low, V _{OUT} =0			1	μA
On-Resistance		R _{DS(ON)}	V _{IN} =3.3V, I _{OUT} =1.3A		100	120	mΩ
Current Limiting		I _{LIM}	V _{IN} =3.3V, V _{OUT} =2.3V	1.5	2	2.5	A
Short Circuit Current		I _{SC_FB}	V _{OUT} =0V, Measured Prior to Thermal Shutdown	0.4	0.8	1.5	A
Thermal shutdown Threshold		T _{SD}			130		°C
Thermal shutdown Hysteresis		ΔT _{SD}			20		°C
EN Threshold	Low	V _{IL}	V _{IN} =2.7V ~ 5.5V			0.8	V
	High	V _{IH}	V _{IN} =2.7V ~ 5.5V	2			V
Enable Input Leakage		I _{EN}	V _{EN} =5.5V			1	μA
Output Turn-On Delay Time		T _{D ON}	V _{IN} =5V, R _{LOAD} =10Ω		60	100	μs
Output Turn-On Rise Time		T _{ON}	V _{IN} =5V, R _{LOAD} =10Ω	1	3		ms
Output Turn-Off Fall Time		T _{D OFF}	V _{IN} =5V, R _{LOAD} =10Ω		4	10	μs
Output Pull-Down Resistance During OFF		R _{DISCHARGE}	EN=Low		150		Ω

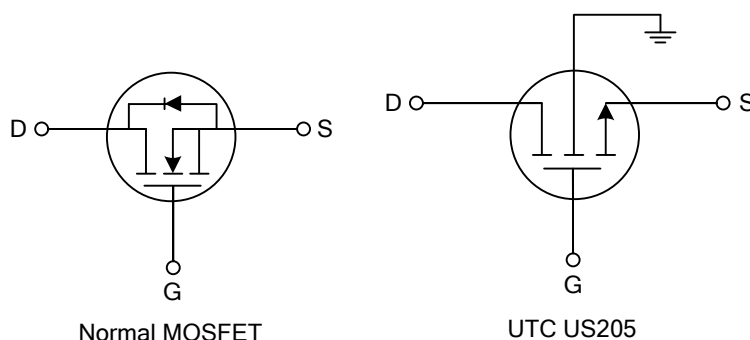
■ APPLICATION INFORMATION

The UTC **US205** is an enable input single N-MOSFET high-side power switches, optimized for self-powered and bus-powered Universal Serial Bus (USB) applications. A charge pump circuitry is contained to drive the internal MOSFET switch, the switch on resistance is 100mΩ, meets USB voltage droop requirements.

Input and Output

V_{IN} (Input) is both the power supply connection for the internal circuitry driving the switch and the input (Drain connection) of the power MOSFET switch. V_{OUT} (Output) is the Source connection of the MOSFET and supplies power to the load. In a typical circuit, current flows from V_{IN} to V_{OUT} toward the load. Since the switch is bidirectional when enabled, if V_{OUT} is greater than V_{IN} , current will flow through the switch from V_{OUT} to V_{IN} .

There is no parasitic body diode between drain and source of the MOSFET, prevents reverse current flow if V_{OUT} is externally forced to a higher voltage than V_{IN} when the chip is disabled ($V_{EN} < 0.8V$).



Chip Enable Input

If the EN pin is in a logic low condition the UTC **US205** switch will be disabled. During this case, MOSFET and the internal circuitry will be turned off, and the supply current is reduced to 0.1uA typical. When the EN is floating, unpredictable operation may be caused. EN goes negative with respect to GND should not be allowed. The EN pin can be directly tied to V_{IN} .

Soft Start for Hot Plug-In Applications

The UTC **US205** “soft-start” feature eliminates the upstream voltage droop caused by the large inrush current during hot-plug events, and effectively isolates the power source from extremely large capacitive load, meets the USB voltage droop requirements.

Under-Voltage Lockout

The UTC **US205** internal Under-voltage lockout (UVLO) circuit prevents the MOSFET switch from turning on until the input voltage exceeds approximately 1.75V. The internal MOSFET switch will be turned off if input voltage drops below approximately 1.7V. Under-voltage detection functions only when the chip is enabled.

Current Limiting and Short-Circuit Protection

The UTC **US205** protects the power supply and the load from damage by the current limit circuitry, but can deliver load current up to the current limit threshold (typically 2A). If an enabled switch encounters a heavy load or short circuit case, a large transient current may flow until the current limit circuitry responds. Once this current limit threshold level is exceeded, the device enters constant current mode until the fault is removed or thermal shutdown occurs.

Supply Filter/Bypass Capacitor

To prevent the input voltage drooping during hot-plug events, a 1uF low-ESR ceramic capacitor from V_{IN} to GND is strongly recommended. But higher capacitor values will further reduce the voltage droop on the input. Furthermore, without the bypass capacitor, an output short may cause sufficient ringing on the input (from source lead inductance) to destroy the internal circuitry. The input voltage transient must not exceed the absolute maximum supply voltage 6V even for a short duration.

■ APPLICATION INFORMATION(Cont.)

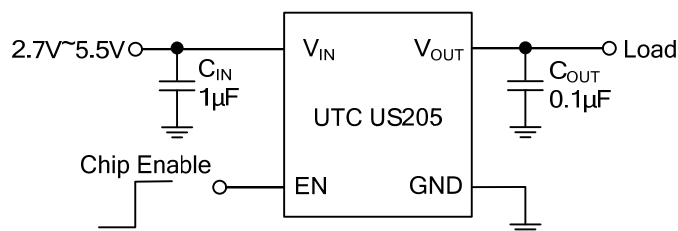
Output Filter Capacitor

A low-ESR 150 μ F aluminum electrolytic or tantalum between V_{OUT} and GND is strongly recommended to meet the 330mV maximum droop requirement in the hub V_{BUS} (Per USB 2.0, output ports must have a minimum 120 μ F of low-ESR bulk capacitance per hub). The bypass capacitor itself should have a low dissipation factor to allow decoupling at higher frequencies.

Thermal Shutdown

Thermal shutdown is used to turn off the output MOSFET when the if the temperature is higher than 130°C, 20°C of hysteresis prevents the switch turn on until the die temperature drops to 110°C.

■ TYPICAL APPLICATION CIRCUIT



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