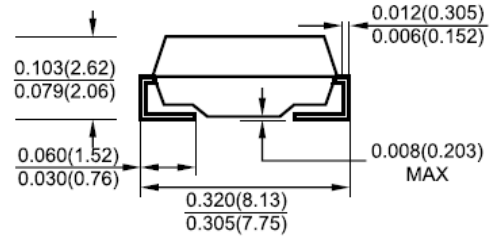
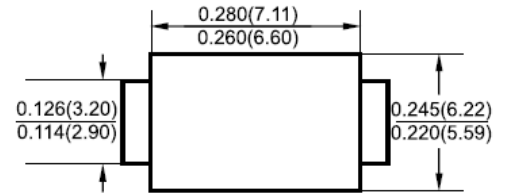


FEATURES

- Voltage Range 50 to 1000 Volts
- Glass passivated chip junction
- Very Fast Recovery
- Low forward voltage drop
- High surge current capability
- High temperature soldering:
260°C/10 seconds at terminals

MECHANICAL DATA

- Molded plastic body (UL 94 V-0 Rated)
- Solder plated terminals
- Polarity: Indicated by cathode band
- Packaging: 16mm tape EIA STD RS-481
- Weight: 0.21 gram



SMC (DO-214AB)

MAXIMUM RATINGS & ELECTRICAL CHARACTERISTICS

Rating at 25°C ambient temperature unless otherwise specified.
Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%

Parameter	Symbol	US3A	US3B	US3D	US3G	US3J	US3K	US3M	Unit	
Maximum Repetitive Peak Reverse Voltage	VRRM	50	100	200	400	600	800	1000	V	
Maximum RMS Voltage	VRMS	35	70	140	280	420	560	700	V	
Maximum DC Blocking Voltage	VDC	50	100	200	400	600	800	1000	V	
Maximum Average Forward Rectified Current @ TL = 110°C	IF (AV)	3.0								μA
Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC Method)	IFSM	100								μA
Maximum Instantaneous Forward Voltage @1.0A	VF	1.0				1.7			V	
Maximum DC Reverse Current @ TA = 25°C At rated DC blocking voltage @ TA = 125°C	IR	10.0				500			μA	
Reverse Recovery Time (Note 1.)	trr	50				75			Ns	
Junction Thermal Resistance (Note 2.)	ROJA ROJL	25							°C / W	
Operating Temperature Range	TJ	-55 to +150							°C	
Storage Temperature Range	TSTG	-55 to +150							°C	

NOTE: 1. Reverse Recovery Test conditions: IF = 0.5A, IR = 1.0A, IRR = 0.25A.
2. Measured on P.C. Board with 0.2x0.2”(5.0x5.0mm) Copper Pad Areas.

■ **RATING & CHARACTERISTIC CURVES**

FIG. 1 - MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

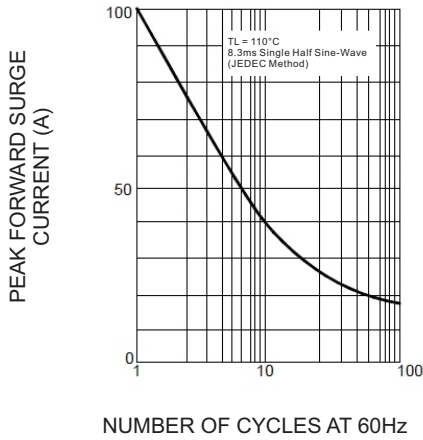


FIG. 2 - MAXIMUM CURRENT DERATING CURVE

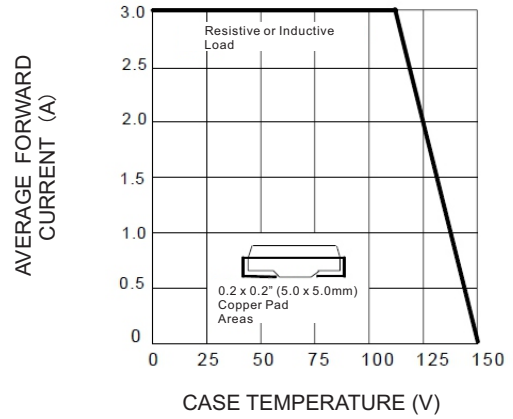


FIG. 3 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

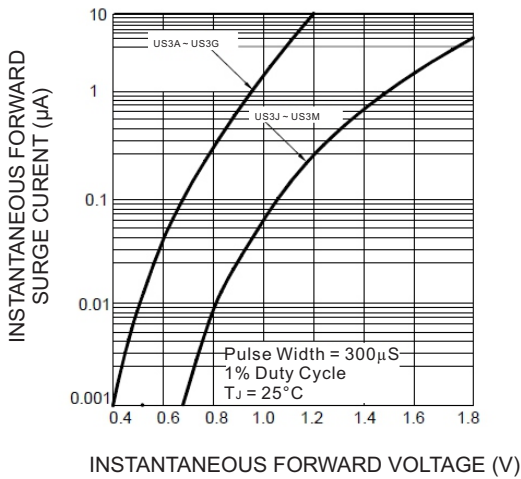


FIG. 4 - TYPICAL REVERSE CHARACTERISTICS

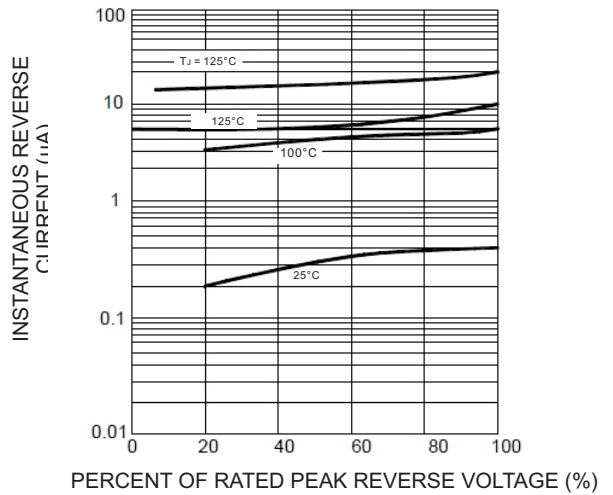


FIG. 5 - TYPICAL JUNCTION CAPACITANCE

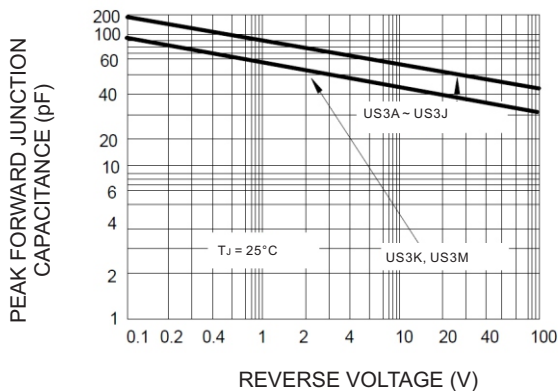


FIG. 6 - TYPICAL TRANSIENT THERMAL IMPEDANCE

