USG70N11 Advance POWER MOSFET

70A, 115V N-CHANNEL ENHANCEMENT MODE TRENCH POWER MOSFET

DESCRIPTION

The UTC **USG70N11** is a N-channel Power MOSFET, it uses UTC's advanced technology to provide the customers with low $R_{\text{DS(ON)}}$ characteristic by high cell density trench technology.

The UTC **USG70N11** is suitable for high efficiency synchronous rectification in SMPS, UPS, hard switched and high frequency circuits.

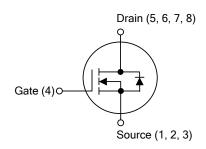
APPLICATION

- * Synchronous Rectification in SMPS
- * Hard Switching and High Speed Circuit
- * DC/DCin Telecoms and Inductrial

■ FEATURES

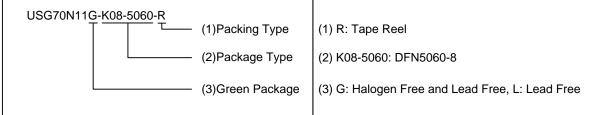
- * $R_{DS(ON)} \le 12.5 \text{ m}\Omega$ @ $V_{GS}=10\text{V}$, $I_{D}=15\text{A}$ $R_{DS(ON)} \le 19 \text{ m}\Omega$ @ $V_{GS}=4.5\text{V}$, $I_{D}=8.0\text{A}$
- * Optimized for high speed switching, Logic level
- * Enhanced Body diode dv/dt capability
- * Enhanced Avalanche Ruggednessy

■ SYMBOL



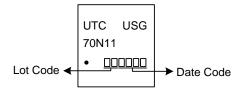
■ ORDERING INFORMATION

Ordering N	Dookogo	Pin Assignment							Dooking			
Lead Free	Halogen Free	Package	1	2	3	4	5	6	7	8	Packing	
USG70N11L-K08-5060-R	USG70N11G-K08-5060-R	DFN5060-8	S	ഗ	S	G	О	D	D	D	Tape Reel	
Note: Pin Assignment: G: Gate D: Drain S: Source												
USG70N11G-K08-5060-R		(4) D. Ton	D-	-1								



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■ MARKING



■ **ABSOLUTE MAXIMUM RATINGS** (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	115	V
Gate-Source Voltage		V_{GSS}	20 / -12	V
Continuous Drain Current	Continuous	Ι _D	70	Α
Pulsed Drain Current	Pulsed (Note 2)	I _{DM}	140	Α
Power Dissipation		P_{D}	20	W
Junction Temperature		TJ	+150	°C
Storage Temperature Range		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient (Note)	θ_{JA}	35	°C/W
Junction to Case	θ_{JC}	6.25 (Note)	°C/W

Note: Device mounted on FR-4 substrate P_C board, 2oz copper, with 1inch square copper plate.

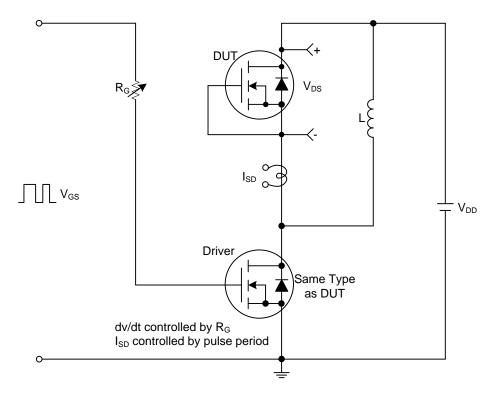
■ **ELECTRICAL CHARACTERISTICS** (T_J =25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT		
OFF CHARACTERISTICS									
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μA, V _{GS} =0V	115			V		
Drain-Source Leakage Current		I _{DSS}	V _{DS} =100V, V _{GS} =0V			1	μΑ		
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} =+20V, V _{DS} =0V			+100	nA		
	Reverse		V _{GS} =-12V, V _{DS} =0V			-100	nA		
ON CHARACTERISTICS									
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1.2		2.5	V		
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =15A		10	12.5	mΩ		
			V _{GS} =4.5V, I _D =8.0A		15	19	mΩ		
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS									
Maximum Body-Diode Continuous Current		Is				70	Α		
Maximum Body-Diode Pulsed Current		I _{SM}				140	Α		
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I _F =70A, V _{GS} =0V			1.4	V		

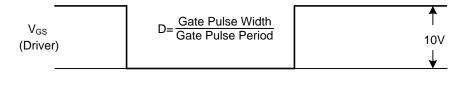
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤2%.

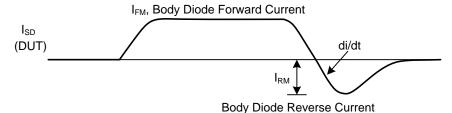
^{2.} Essentially independent of operating temperature.

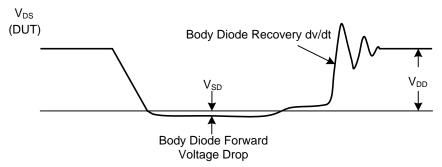
■ TEST CIRCUITS AND WAVEFORMS



Peak Diode Recovery dv/dt Test Circuit



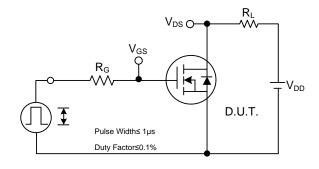


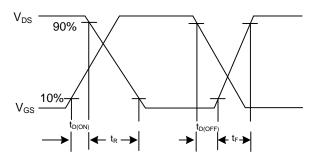


Peak Diode Recovery dv/dt Test Circuit and Waveforms

Peak Diode Recovery dv/dt Waveforms

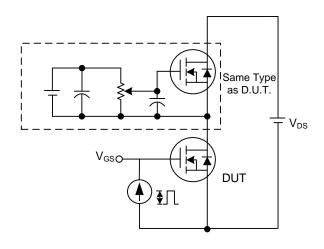
■ TEST CIRCUITS AND WAVEFORMS

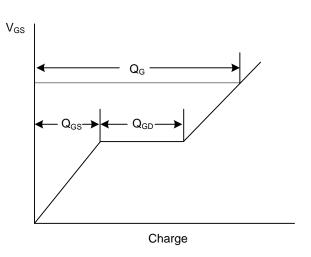




Switching Test Circuit

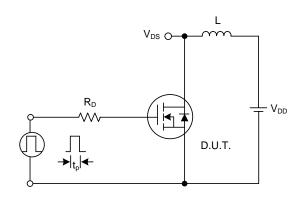
Switching Waveforms

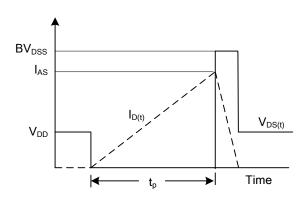




Gate Charge Test Circuit

Gate Charge Waveform





Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

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