



## UT2319

Power MOSFET

### -40V, -4.4A P-CHANNEL POWER MOSFET

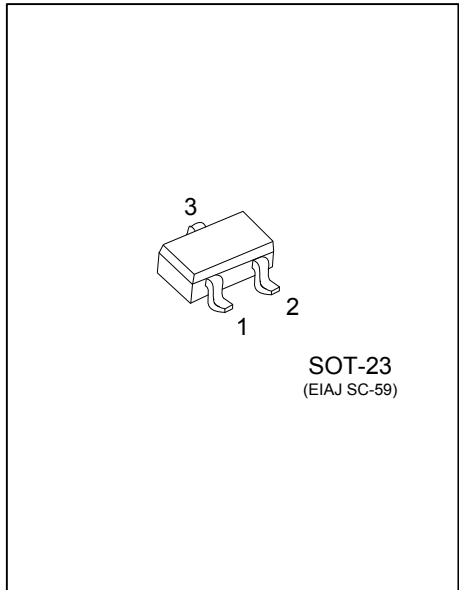
#### DESCRIPTION

The UTC **UT2319** is a P-channel power MOSFET using UTC's advanced technology to provide the customers with high switching speed and a minimum on-state resistance, and it can also withstand high energy in the avalanche.

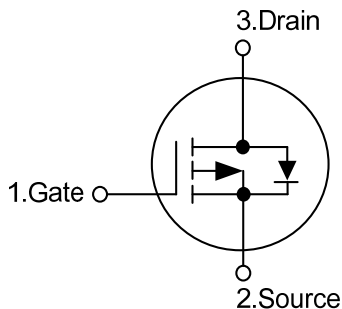
This UTC **UT2319** is suitable for motor drivers, high-side switch and 12V board net, etc.

#### FEATURES

- \*  $R_{DS(ON)} \leq 75\text{ m}\Omega$  @  $V_{GS}=-10\text{V}$ ,  $I_D=-2.7\text{A}$
- \*  $R_{DS(ON)} \leq 100\text{ m}\Omega$  @  $V_{GS}=-10\text{V}$ ,  $I_D=-2.4\text{A}$
- \* High Switching Speed



#### SYMBOL



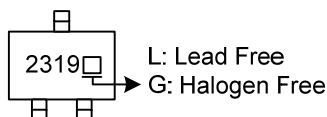
#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UT2319L-AE3-R	UT2319G-AE3-R	SOT-23	G	S	D	Tape Reel

Note: Pin Assignment: G: Gate S: Source D: Drain

<p>UT2319G-AE3-R</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Green Package</p>	<p>(1) R: Tape Reel</p> <p>(2) AE3: SOT-23</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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#### MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_C=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	-40	V
Gate-Source Voltage		$V_{GSS}$	$\pm 20$	V
Drain Current	Continuous	$I_D$	-4.4	A
	Pulsed	$I_{DM}$	-20	A
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	17	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	1.7	V/ns
Power Dissipation		$P_D$	0.6	W
Junction Temperature		$T_J$	-55 ~ +150	$^\circ\text{C}$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

- Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.  
 2. Repetitive Rating: Pulse width limited by maximum junction temperature.  
 3.  $L = 0.1\text{mH}$ ,  $I_{AS} = 18.5\text{A}$ ,  $V_{DD} = 20\text{V}$ ,  $R_G = 25\ \Omega$ , Starting  $T_J = 25^\circ\text{C}$   
 4.  $I_{SD} \leq 4.4\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient	$\theta_{JA}$	347	$^\circ\text{C}/\text{W}$
Junction to Case	$\theta_{JC}$	208	$^\circ\text{C}/\text{W}$

Note: Device mounted on FR-4 substrate  $P_C$  board, 2oz copper, with 1inch square copper plate.

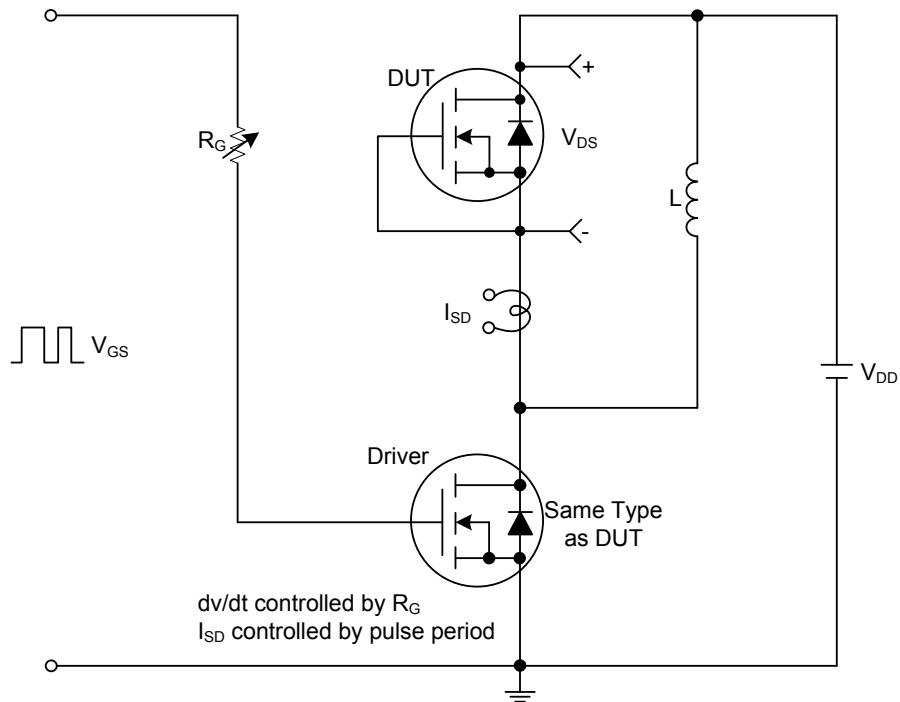
■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =-250μA, V <sub>GS</sub> =0V	-40			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =-40V, V <sub>GS</sub> =0V			-1	μA
Gate- Source Leakage Current	Forward	V <sub>GS</sub> =+20V, V <sub>DS</sub> =0V			+100	nA
	Reverse	V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V			-100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1.0		-3.0	V
Static Drain-Source On-State Resistance (Note 1)	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-2.7A			75	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2.4A			100	mΩ
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =-25V, V <sub>GS</sub> =0V, f=1MHz		600		pF
Output Capacitance	C <sub>OSS</sub>			105		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			65		pF
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge	Q <sub>G</sub>	V <sub>DS</sub> =-32V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-4.4A I <sub>G</sub> =-1mA (Note 1, 2)		15		nC
Gate to Source Charge	Q <sub>GS</sub>			3.1		nC
Gate to Drain Charge	Q <sub>GD</sub>			2.9		nC
Turn-ON Delay Time	t <sub>D(ON)</sub>	V <sub>DS</sub> =-20V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-4.4A, R <sub>G</sub> =3Ω (Note 1, 2)		6		ns
Rise Time	t <sub>R</sub>			16		ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>			22		ns
Fall-Time	t <sub>F</sub>			17		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Continuous Drain-Source Diode Forward Current	I <sub>S</sub>				-4.4	A
Maximum Pulsed Drain-Source Diode Forward Current	I <sub>SM</sub>				-20	A
Drain-Source Diode Forward Voltage (Note 1)	V <sub>SD</sub>	I <sub>S</sub> =-4.4A, V <sub>GS</sub> =0V			1.4	V
Reverse Recovery Time (Note 1)	t <sub>rr</sub>	I <sub>S</sub> =-4.4A, V <sub>GS</sub> =0V		44		ns
Reverse Recovery Charge	Q <sub>rr</sub>	di/dt=-100A/μs		28		nC

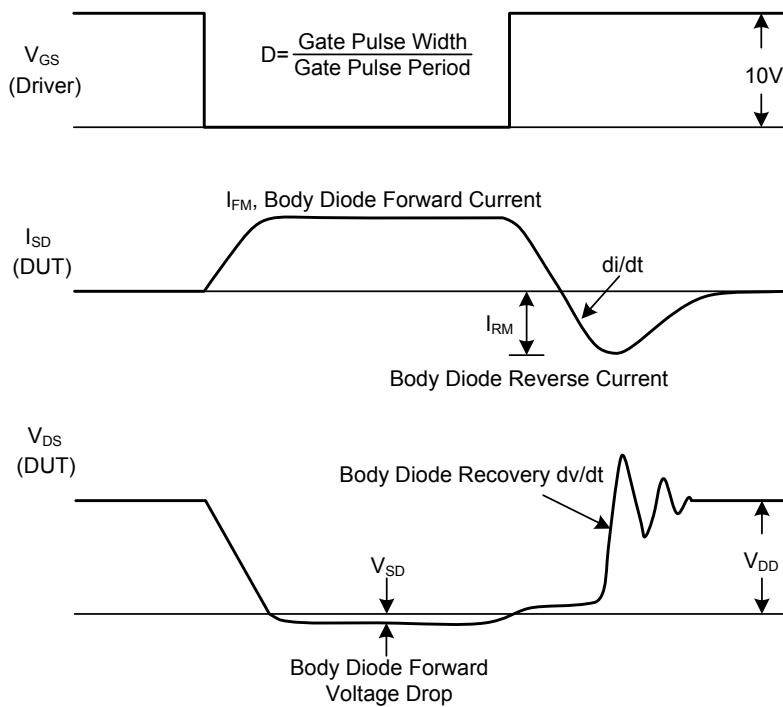
Notes: 1. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS



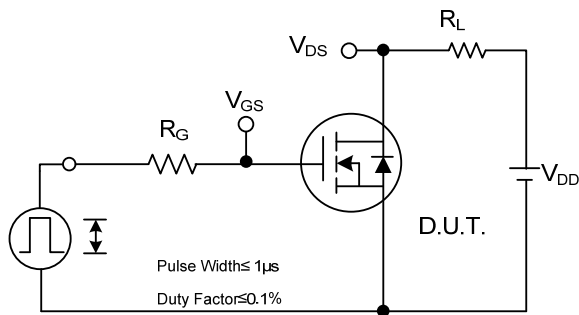
Peak Diode Recovery dv/dt Test Circuit



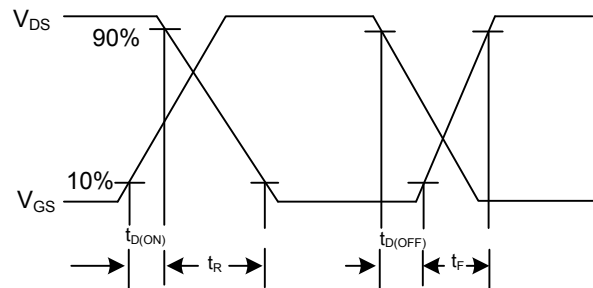
Peak Diode Recovery dv/dt Test Circuit and Waveforms

Peak Diode Recovery dv/dt Waveforms

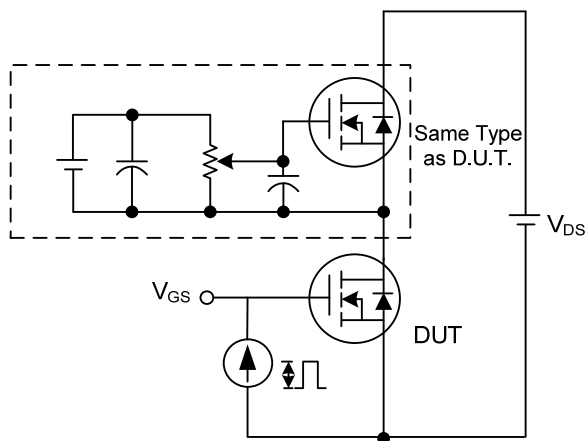
■ TEST CIRCUITS AND WAVEFORMS



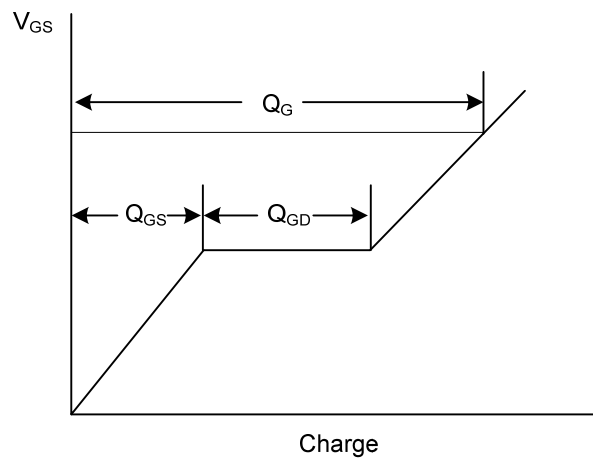
Switching Test Circuit



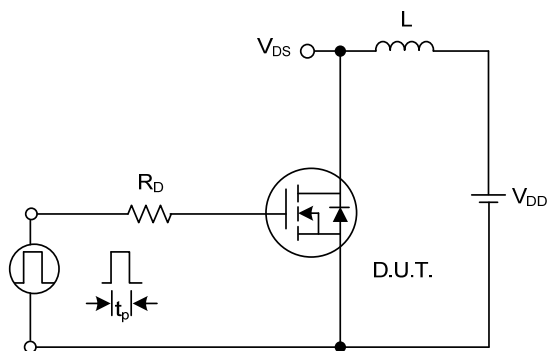
Switching Waveforms



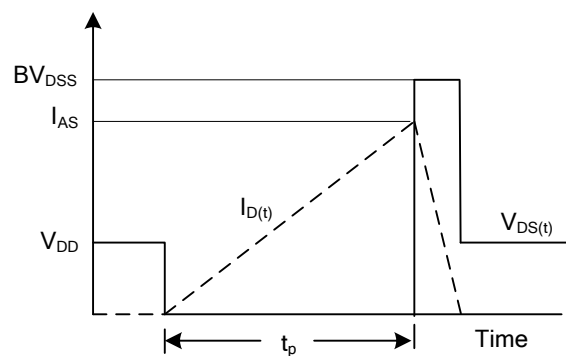
Gate Charge Test Circuit



Gate Charge Waveform

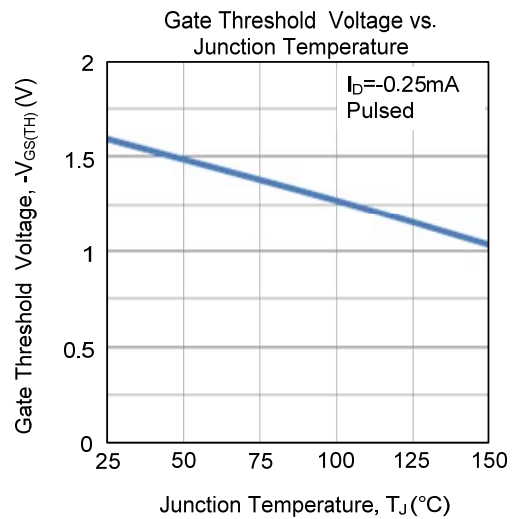
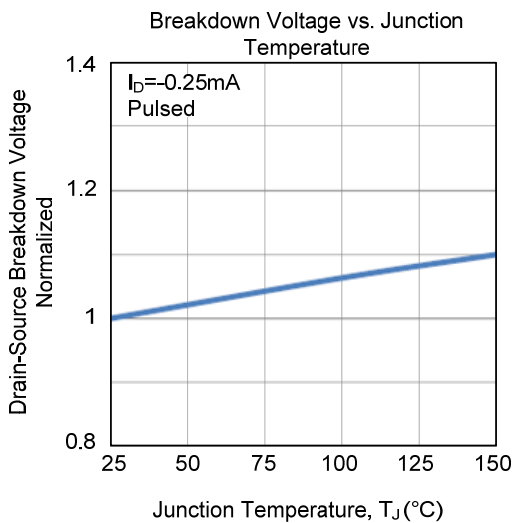
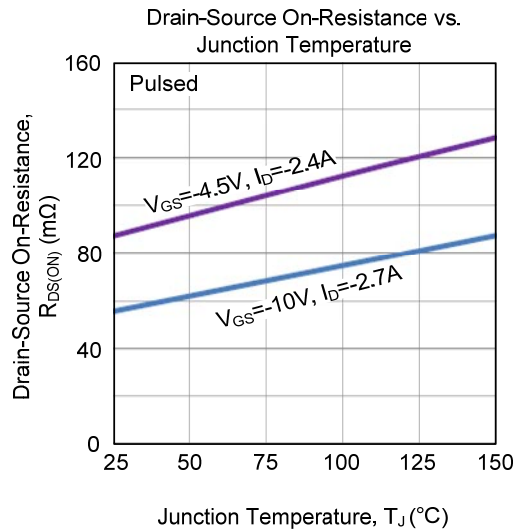
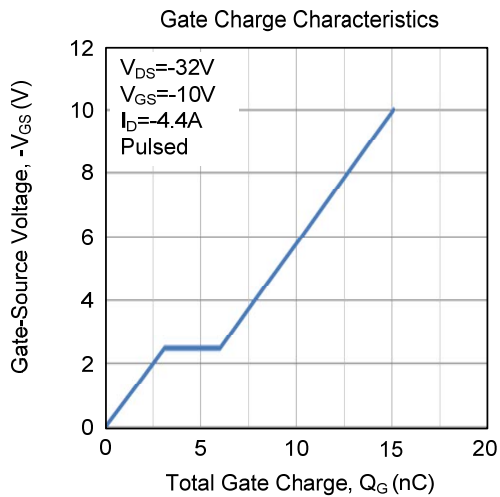
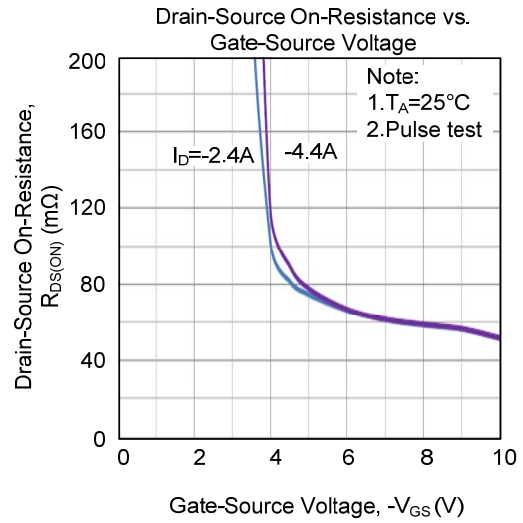
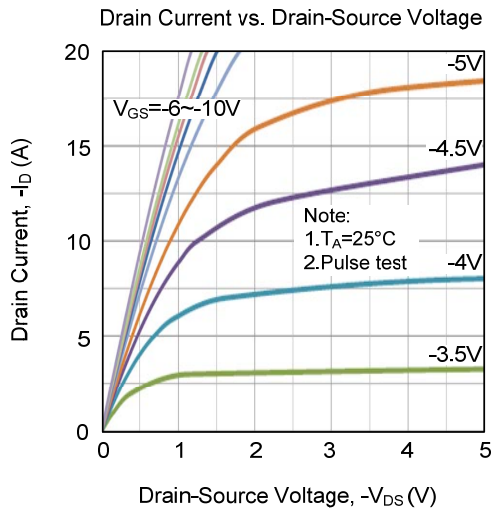


Unclamped Inductive Switching Test Circuit

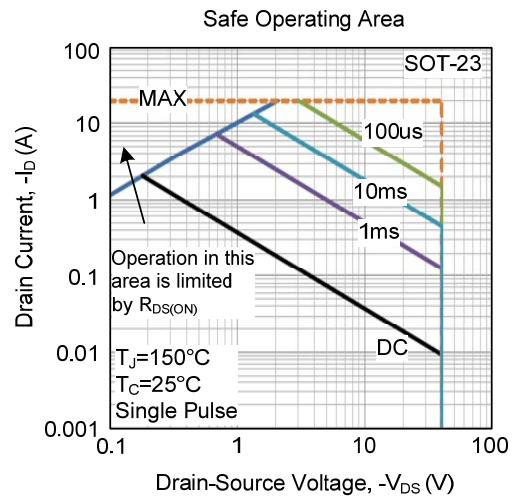
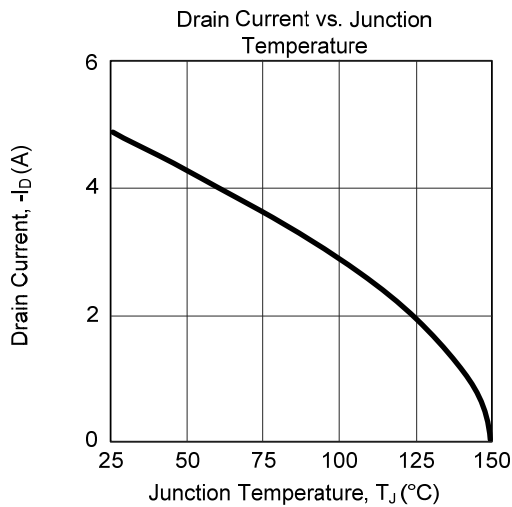
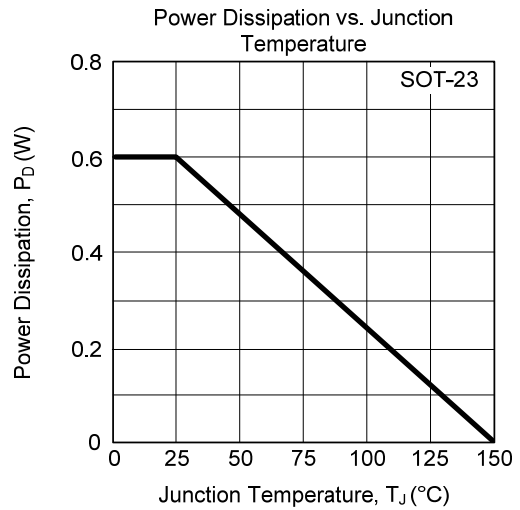
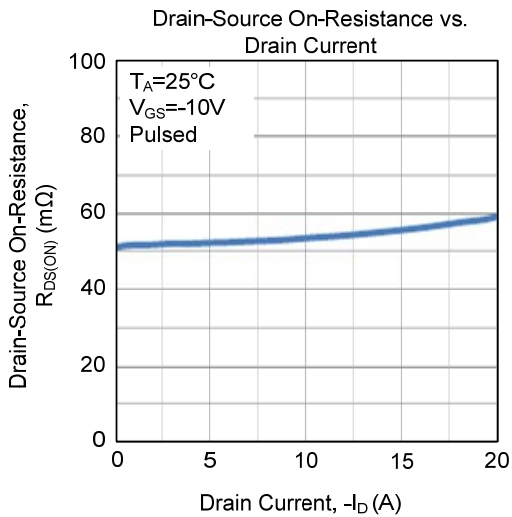
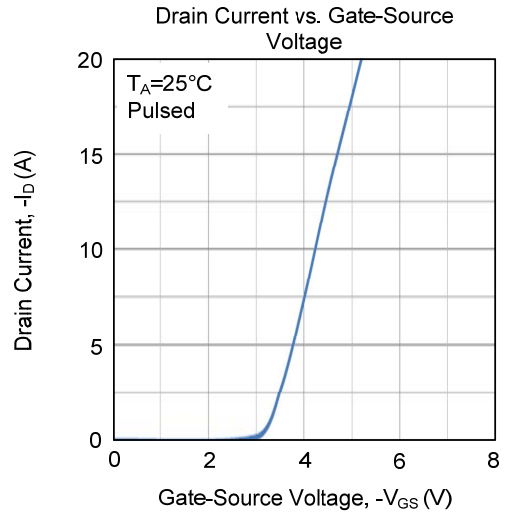
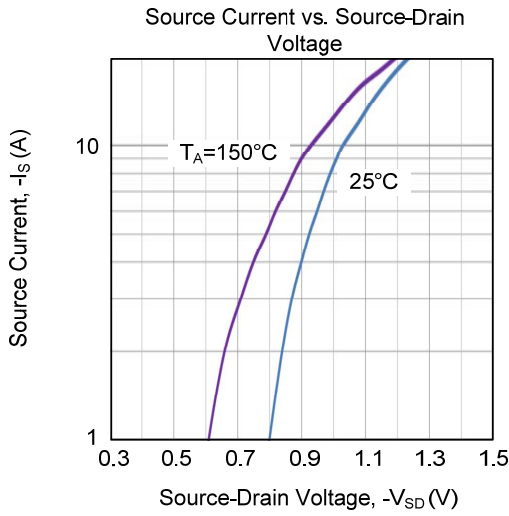


Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



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