



UT25P03

Power MOSFET

-25A, -30V P-CHANNEL POWER MOSFET

DESCRIPTION

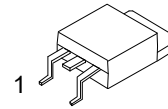
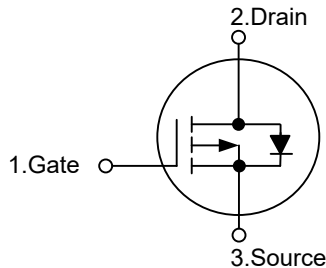
The UTC **UT25P03** The UTC **UT25P02L** is a P-channel power MOSFET using UTC's advanced technology.

The advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength.

FEATURES

- * $R_{DS(ON)} \leq 52 \text{ m}\Omega$ @ $V_{GS} = -10\text{V}$, $I_D = -10\text{A}$
 $R_{DS(ON)} \leq 90 \text{ m}\Omega$ @ $V_{GS} = -4.5\text{V}$, $I_D = -10\text{A}$
- * Low Capacitance
- * Optimized gate charge
- * Fast switching capability
- * Avalanche energy specified

SYMBOL



TO-252

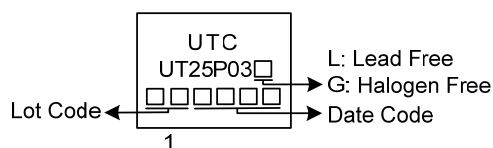
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UT25P03L-TN3-R	UT25P03G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

	(1) Packing Type
	(2) Package Type
	(3) Green Package
(1) R: Tape Reel (2) TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free	

MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_c = 25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V _{DSS}	-30	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current	I _D	-25	A
Pulsed Drain Current (Note 2)	I _{DM}	-50	A
Avalanche Energy	E _{AS}	22	mJ
Peak Diode Recovery dv/dt (Note 4)	dv/dt	0.3	V/ns
Power Dissipation	P _D	32	W
Junction Temperature	T _J	+150	°C
Storage Temperature	T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 0.1mH, I_{AS} = -21A, V_{DD} = -25V, R_G = 25 Ω, Starting T_J = 25°C

4. I_{SD} ≤ -25A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ _{JA}	50	°C/W
Junction to Case	θ _{JC}	3.9	°C/W

Note: Device mounted on FR-4 substrate P_c board, 2oz copper, with 1inch square copper plate.

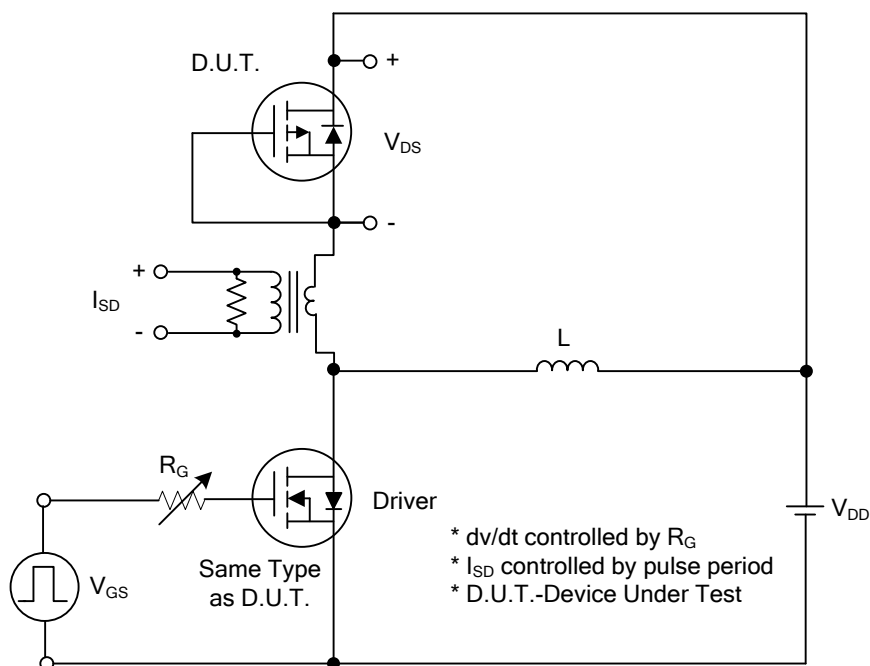
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250μA	-30			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0V			-1	μA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =-250 μA	-1.0		-3.0	V
Static Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-10A			52	mΩ
		V _{GS} =-4.5V, I _D =-10A			90	mΩ
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{ISS}	V _{DS} =-25V, V _{GS} =0V, f=1.0MHz		700		pF
Output Capacitance	C _{OSS}			94		pF
Reverse Transfer Capacitance	C _{RSS}			76		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge (Note 1)	Q _G	V _{DS} =-24V, V _{GS} =-10V, I _D =-25A (Note 1, 2)		32		nC
Gate-Source Charge	Q _{GS}			4.2		nC
Gate-Drain Charge	Q _{GD}			4.6		nC
Turn-On Delay Time (Note 1)	t _{D(ON)}	V _{DS} =-15V, V _{GS} =-10V, I _D =-25A, R _G =3Ω (Note 1, 2)		4.4		ns
Turn-On Rise Time	t _R			15		ns
Turn-Off Delay Time	t _{D(OFF)}			22		ns
Turn-Off Fall Time	t _F			19		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Continuous Drain-Source Diode Forward Current	I _S				-25	A
Maximum Pulsed Drain-Source Diode Forward Current	I _{SM}				-50	A
Drain-Source Diode Forward Voltage (Note 1)	V _{SD}	V _{GS} =0V, I _S =-10A			-1.2	V

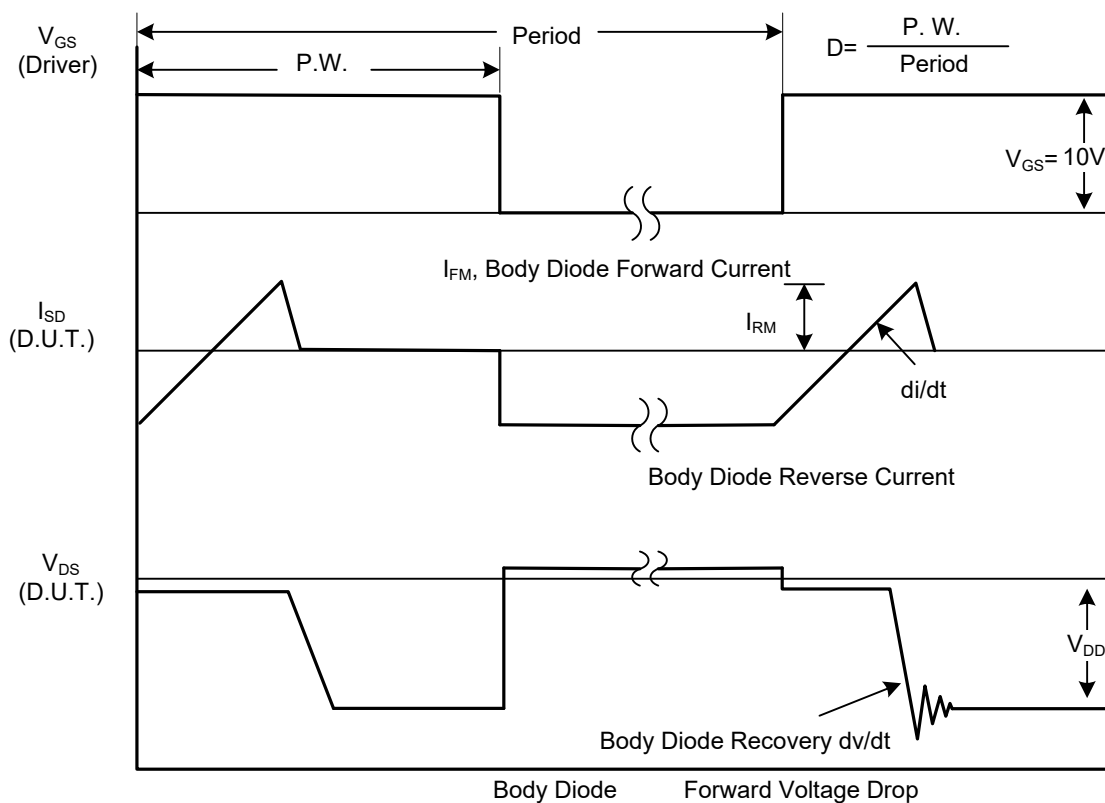
Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

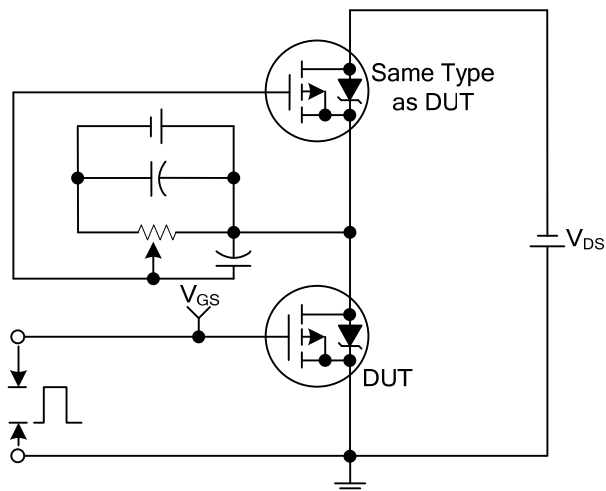


Peak Diode Recovery dv/dt Test Circuit

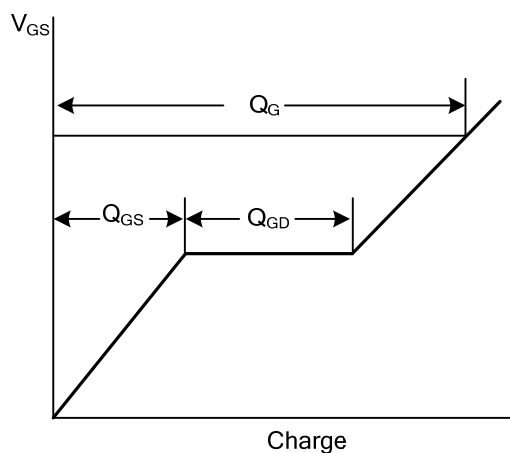


Peak Diode Recovery dv/dt Waveforms

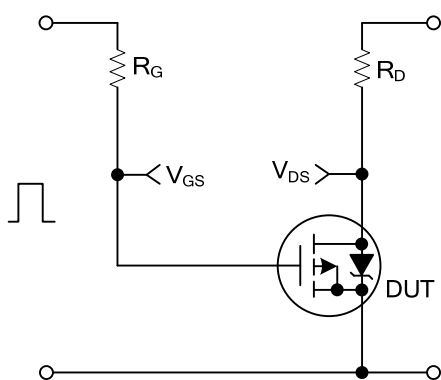
■ TEST CIRCUITS AND WAVEFORMS



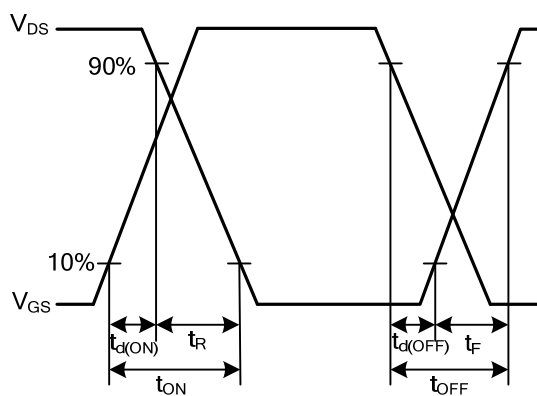
Gate Charge Test Circuit



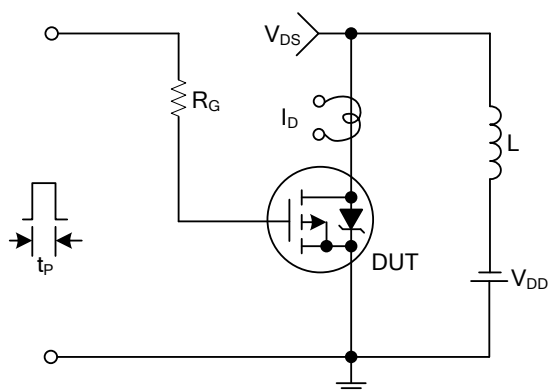
Gate Charge Waveforms



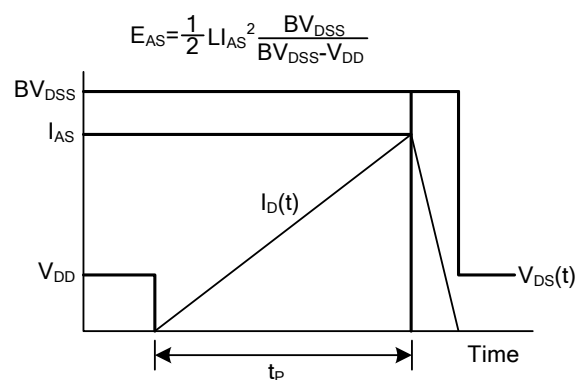
Resistive Switching Test Circuit



Resistive Switching Waveforms

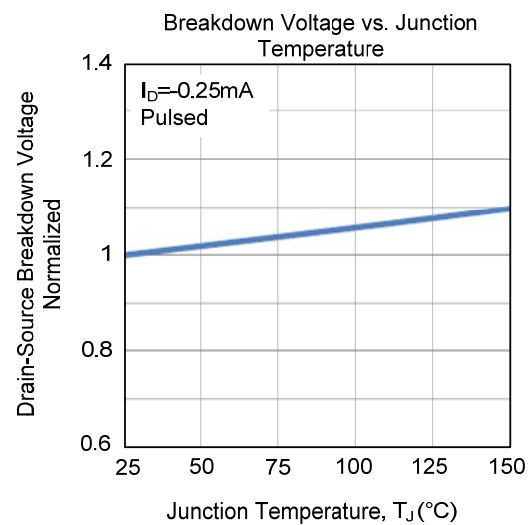
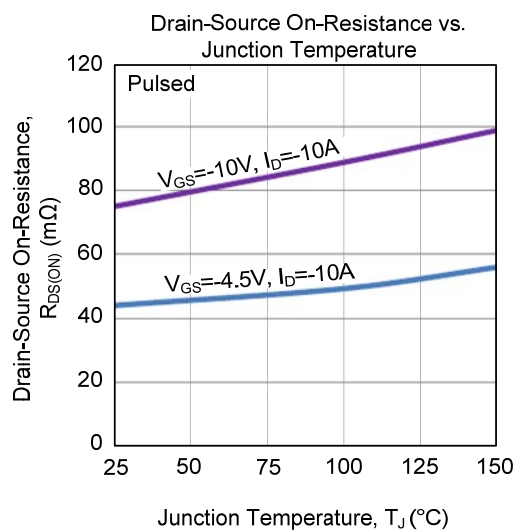
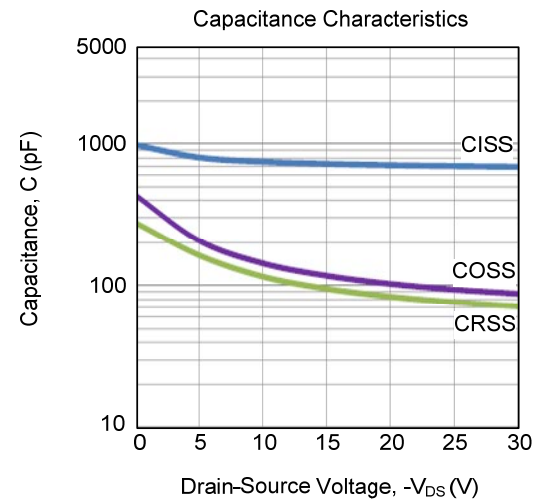
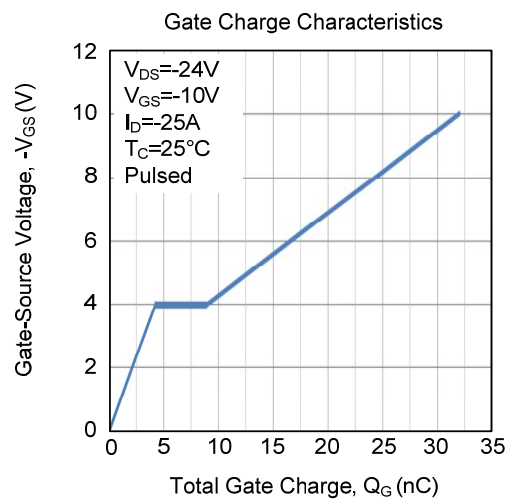
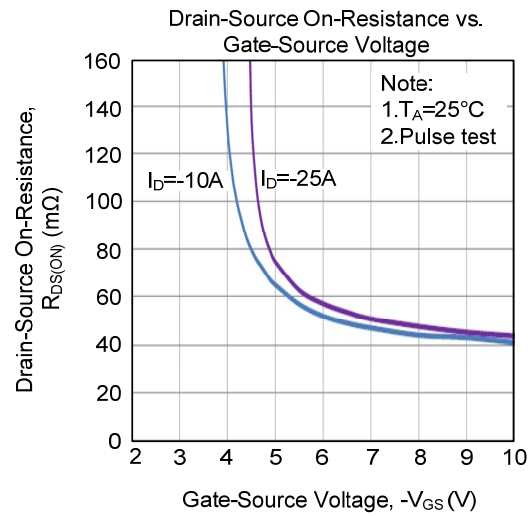
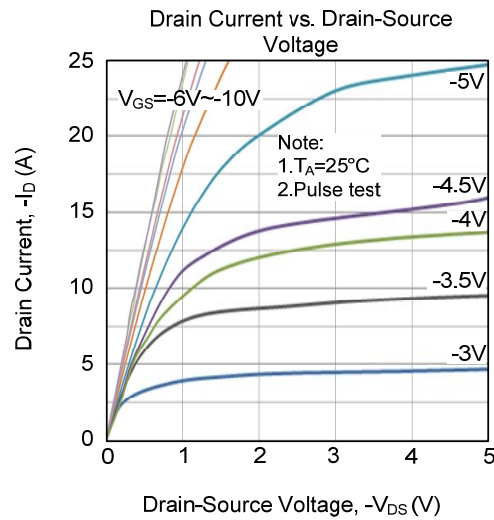


Unclamped Inductive Switching Test Circuit

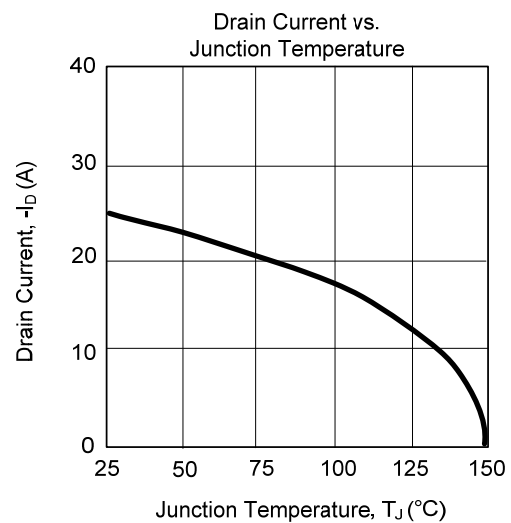
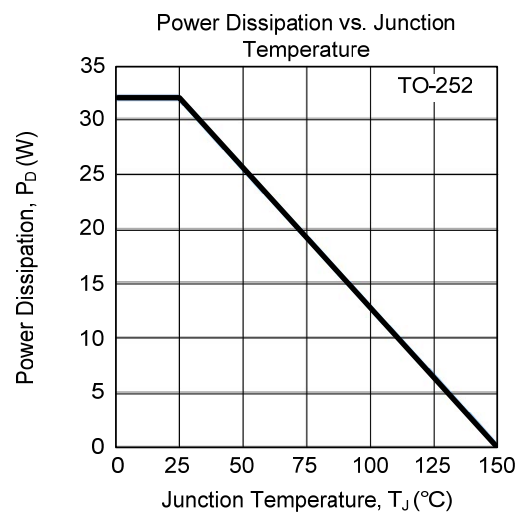
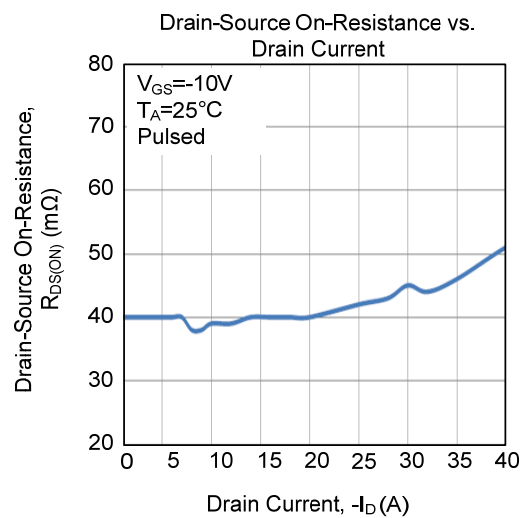
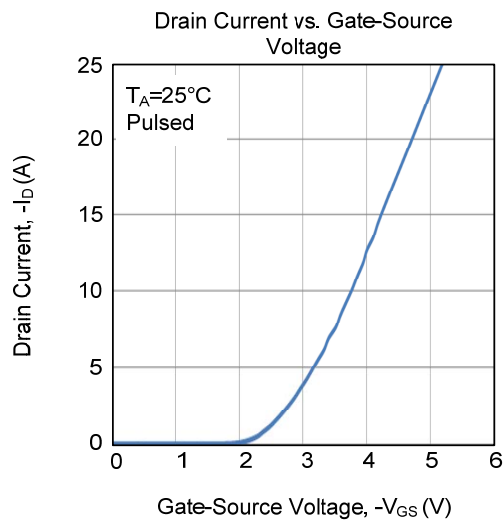
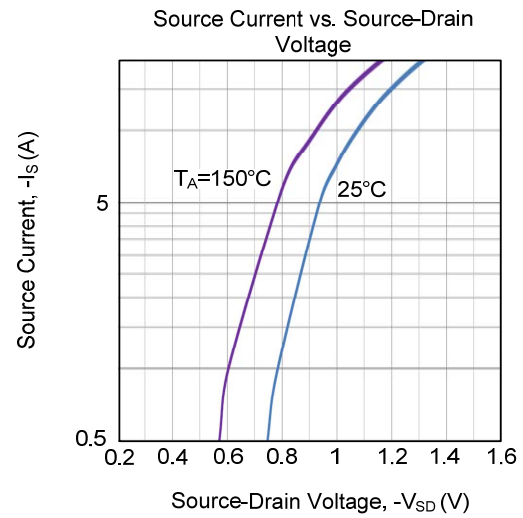
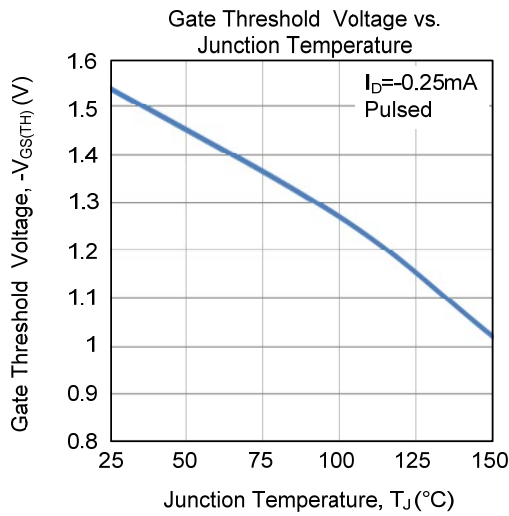


Unclamped Inductive Switching Waveforms

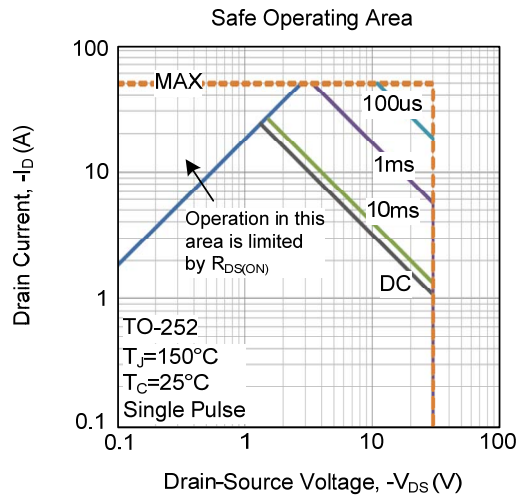
TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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