



UT306S

POWER MOSFET

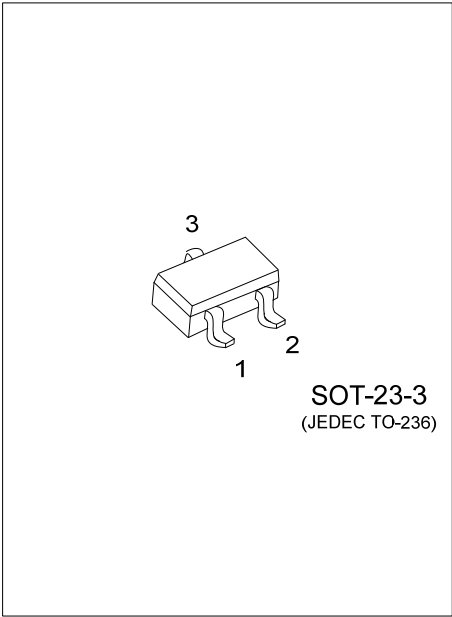
2.2A, 60V SHIELDED GATE N-CHANNEL POWER MOSFET

■ DESCRIPTION

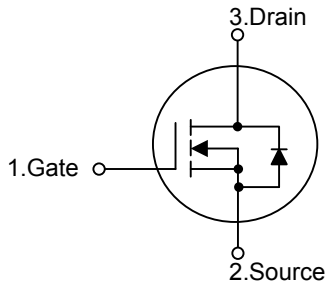
The UTC **UT306S** is N-Channel MOSFET produced using advanced Power process that incorporates Shielded Gate technology. This process has been optimized for $R_{DS(ON)}$, switching performance and ruggedness.

■ FEATURES

- * $R_{DS(ON)} \leq 235 \text{ m}\Omega$ @ $V_{GS}=10\text{V}$, $I_D=2.2\text{A}$
- $R_{DS(ON)} \leq 280 \text{ m}\Omega$ @ $V_{GS}=4.5\text{V}$, $I_D=1.3\text{A}$
- * Simple drive requirement
- * Small package outline
- * Fast Switching Speed



■ SYMBOL



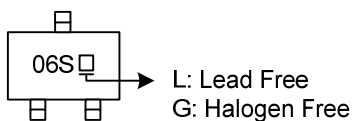
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UT306SL-AE2-R	UT306SG-AE2-R	SOT-23-3	G	S	D	Tape Reel

Note: Pin Assignment: G: Gate S: Source D: Drain

<p>UT306SG-AE2-R</p> <p>(1)Packing Type (2)Package Type (3)Green Package</p>	<p>(1) R: Tape Reel (2) AE2: SOT-23-3 (3) G: Halogen Free and Lead Free, L: Lead Free</p>
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■ MARKING



■ ABSOLUTE MAXIMUM RATING ($T_A=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	60	V
Gate-Source Voltage		V_{GSS}	± 20	V
Drain Current	Continuous	I_D	2.2	A
	Pulsed	I_{DM}	6.6	A
Avalanche Energy	Single Pulsed (Note 4)	E_{AS}	245	mJ
Peak Diode Recovery dv/dt (Note 5)		dv/dt	0.896	V/ns
Power Dissipation (Note 3)		P_D	350	mW
Junction Temperature		T_J	+150	$^{\circ}\text{C}$
Storage Temperature Range		T_{STG}	-55 ~ +150	$^{\circ}\text{C}$

- Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. Surface mounted on 1 in² copper pad of FR-4 board. 270 $^{\circ}\text{C}/\text{W}$ when mounted on minimum copper pad.
4. $L=10\text{mH}$, $I_{AS}=7.0\text{A}$, $V_{DD}=20\text{V}$, $R_G=25\ \Omega$, Starting $T_J = 25^{\circ}\text{C}$
5. $I_{SD}\leq 3.0\text{A}$, $di/dt\leq 200\text{A}/\mu\text{s}$, $V_{DD}\leq BV_{DSS}$, Starting $T_J = 25^{\circ}\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	357	$^{\circ}\text{C}/\text{W}$
Junction to Case	θ_{JC}	110	$^{\circ}\text{C}/\text{W}$

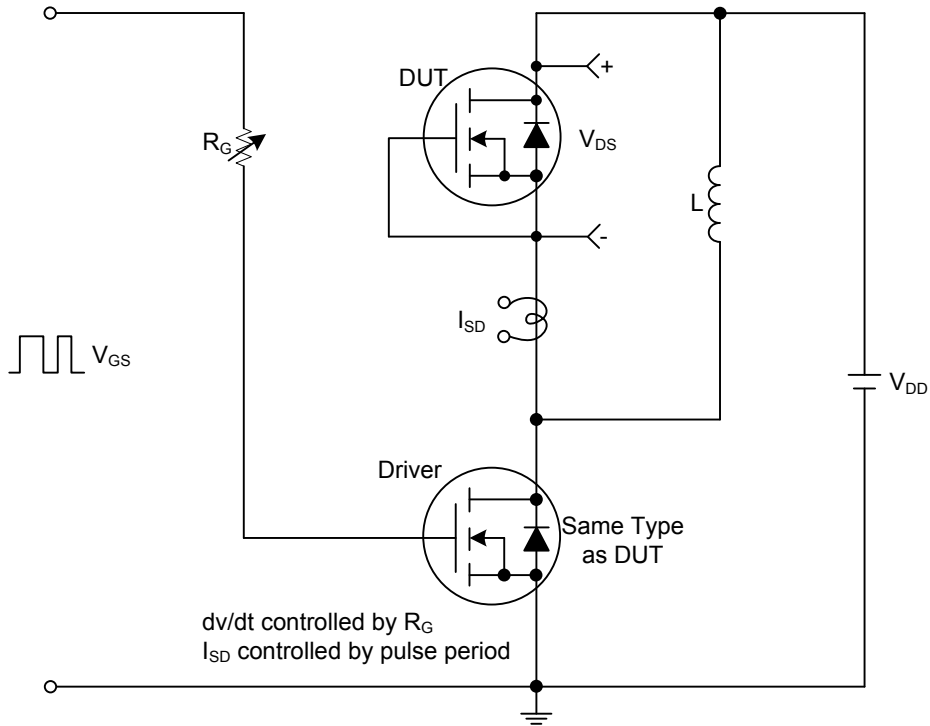
Note: Surface mounted on 1 in² copper pad of FR-4 board. 270 $^{\circ}\text{C}/\text{W}$ when mounted on minimum copper pad.

■ ELECTRICAL CHARACTERISTICS ($T_J=25^{\circ}\text{C}$, unless otherwise specified)

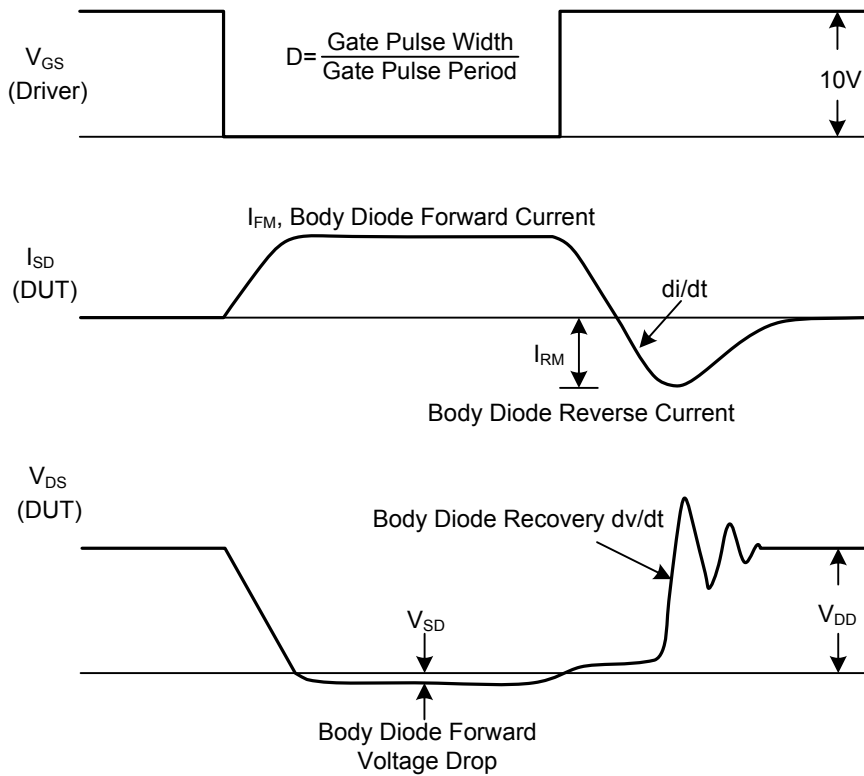
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu\text{A}$, $V_{GS}=0\text{V}$	60			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=60\text{V}$, $V_{GS}=0\text{V}$			1	μA
Gate-Source Leakage Current	Forward	I_{GSS}			+100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	1.0		2.5	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10\text{V}$, $I_D=2.2\text{A}$			235	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$, $I_D=1.3\text{A}$			280	$\text{m}\Omega$
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{GS}=0\text{V}$, $V_{DS}=30\text{V}$, $f=1.0\text{MHz}$		112		pF
Output Capacitance	C_{OSS}			21		pF
Reverse Transfer Capacitance	C_{RSS}			14		pF
SWITCHING PARAMETERS						
Total Gate Charge (Note 1)	Q_G	$V_{DS}=48\text{V}$, $V_{GS}=10\text{V}$, $I_D=2.2\text{A}$ $I_G=1\text{mA}$ (Note 1, 2)		7.1		nC
Gate to Source Charge	Q_{GS}			1.0		nC
Gate to Drain Charge	Q_{GD}			1.6		nC
Turn-on Delay Time (Note 1)	$t_{D(ON)}$	$V_{DD}=30\text{V}$, $V_{GS}=10\text{V}$, $I_D=3.0\text{A}$, $R_G=25\Omega$ (Note 1, 2)		4.0		ns
Rise Time	t_R			15		ns
Turn-off Delay Time	$t_{D(OFF)}$			50		ns
Fall-Time	t_F			25		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				2.2	A
Maximum Body-Diode Pulsed Current	I_{SM}				6.6	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$I_S=0.45\text{A}$, $V_{GS}=0\text{V}$			1.0	V
Reverse Recovery Time	t_{rr}	$I_S=1.0\text{A}$, $V_{GS}=0\text{V}$,		62.8		ns
Reverse Recovery Charge	Q_{rr}	$dI_F/dt=100\text{A}/\mu\text{s}$		0.34		μC

Notes: 1. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS



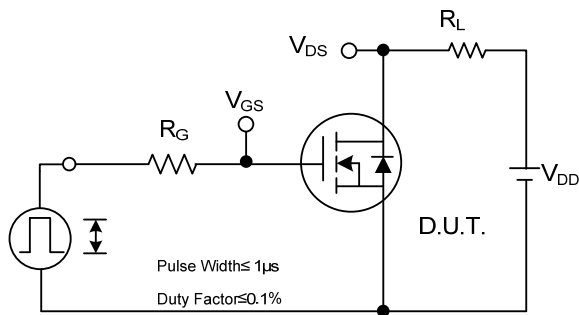
Peak Diode Recovery dv/dt Test Circuit



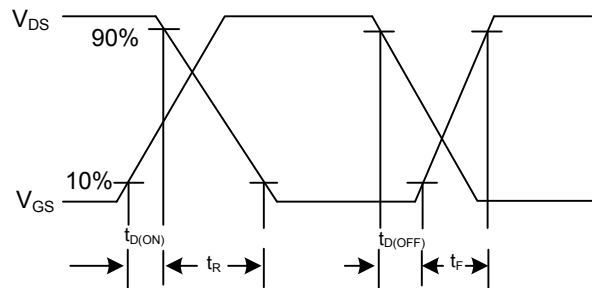
Peak Diode Recovery dv/dt Test Circuit and Waveforms

Peak Diode Recovery dv/dt Waveforms

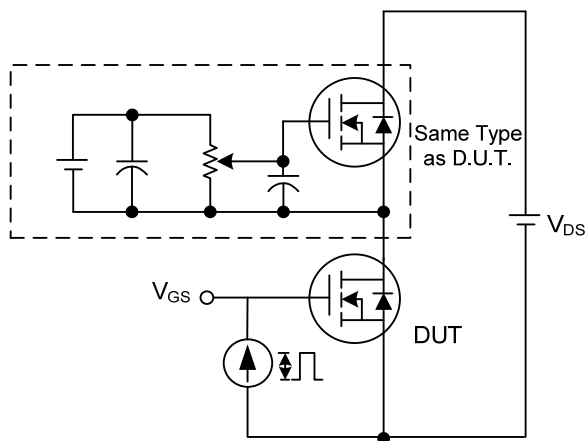
TEST CIRCUITS AND WAVEFORMS



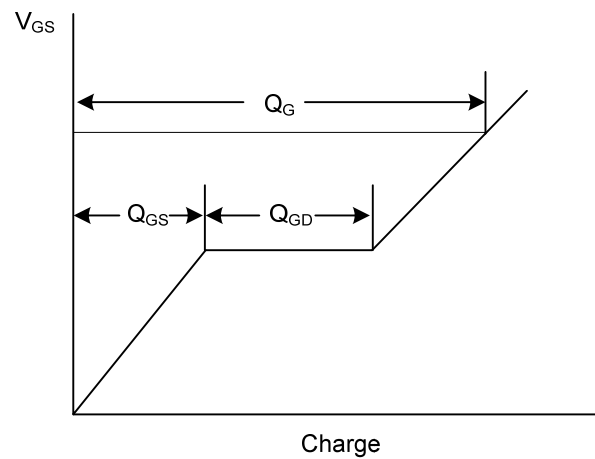
Switching Test Circuit



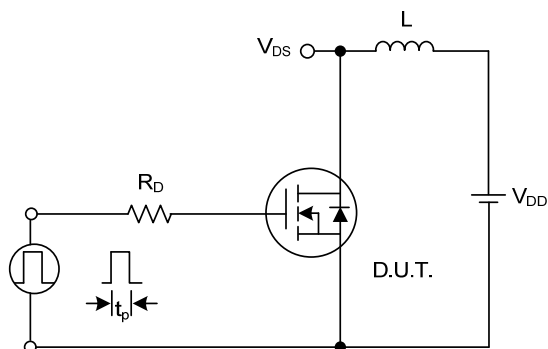
Switching Waveforms



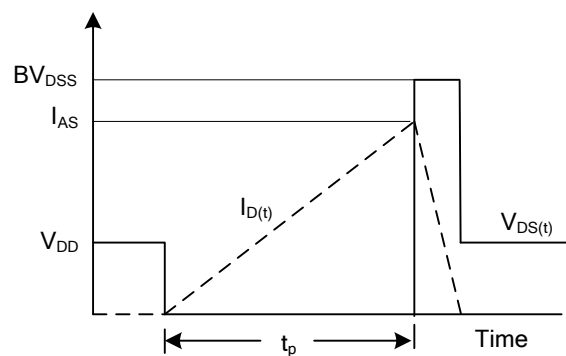
Gate Charge Test Circuit



Gate Charge Waveform

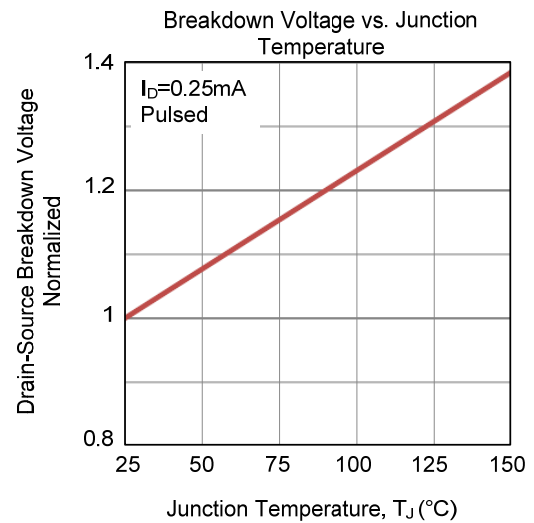
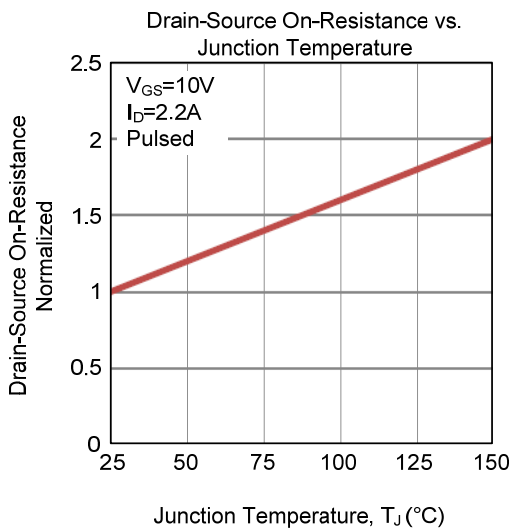
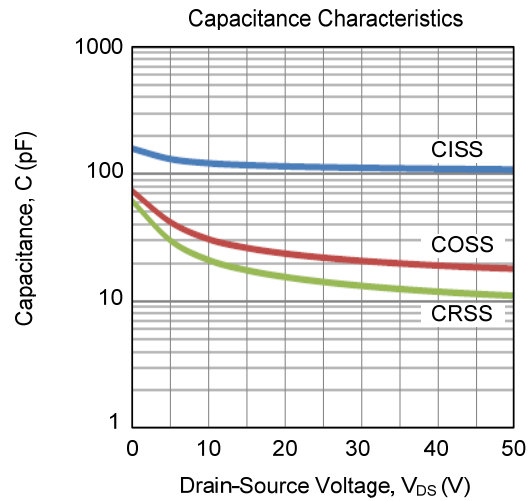
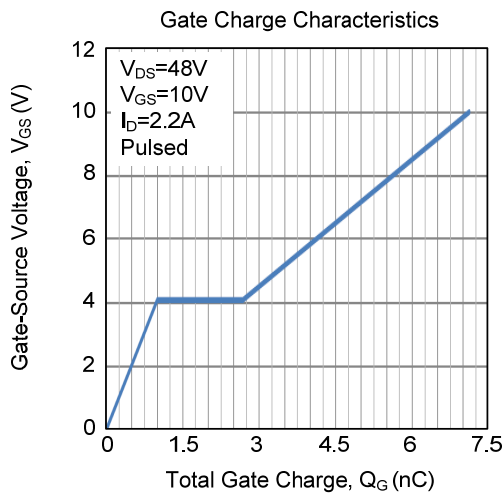
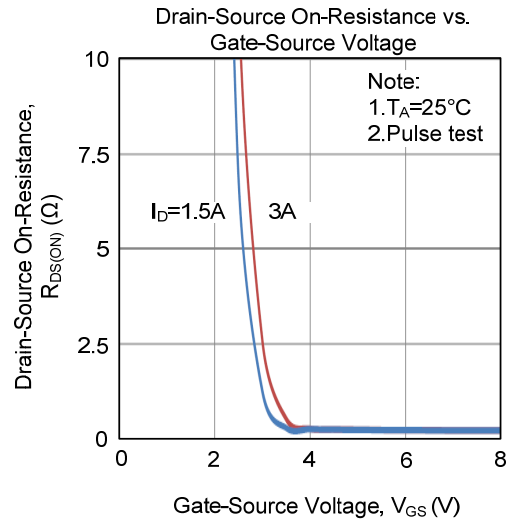
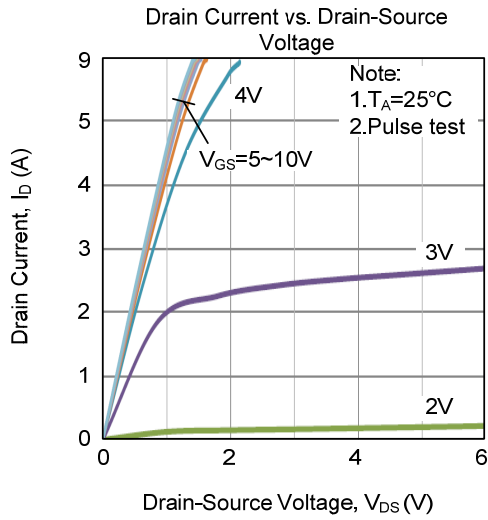


Unclamped Inductive Switching Test Circuit

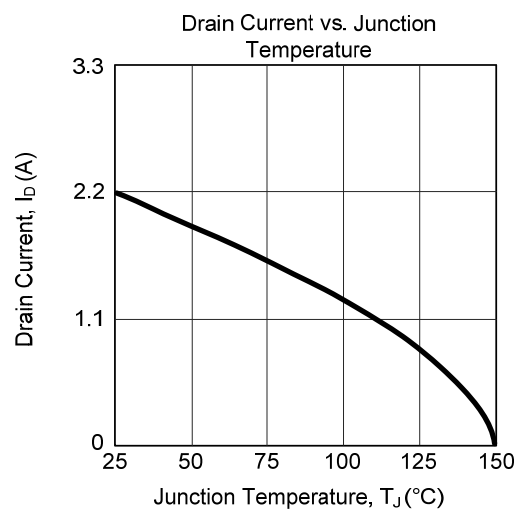
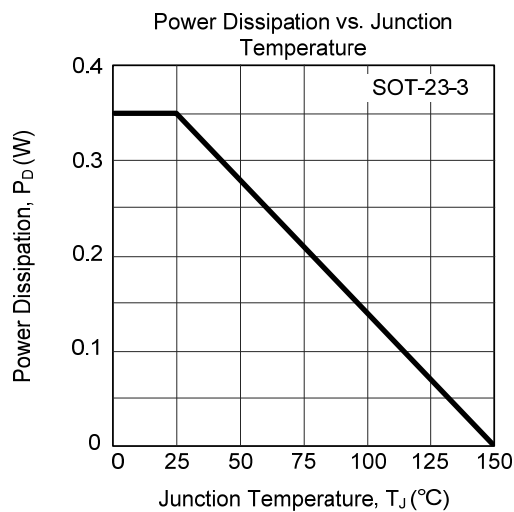
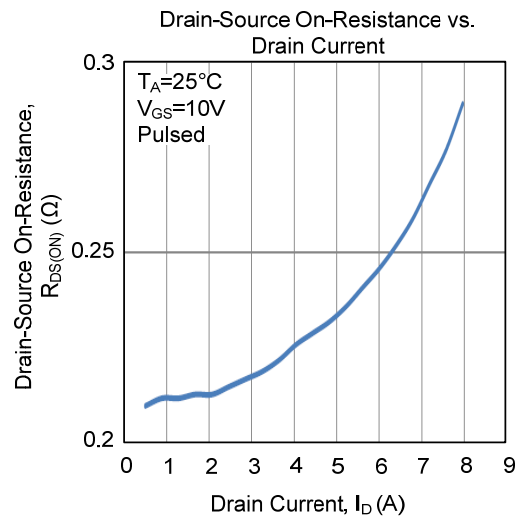
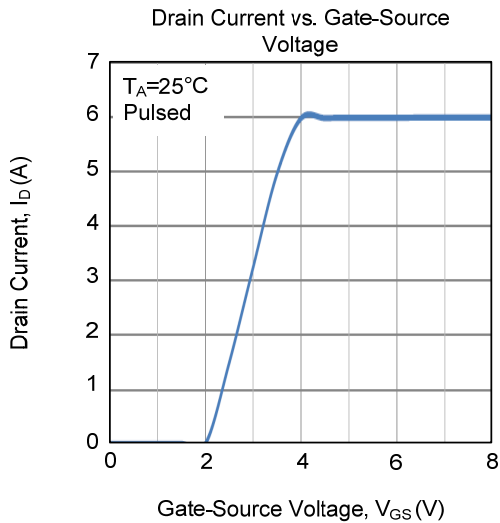
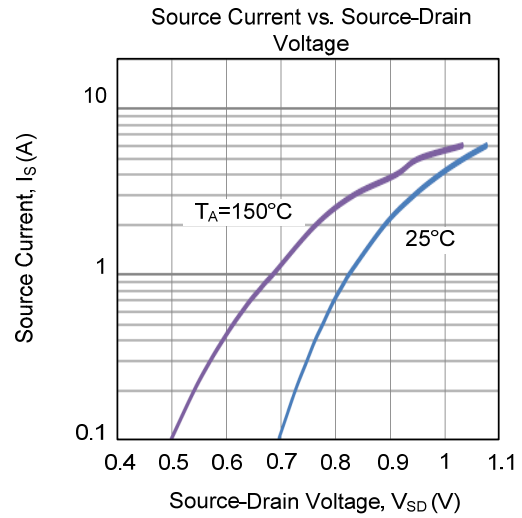
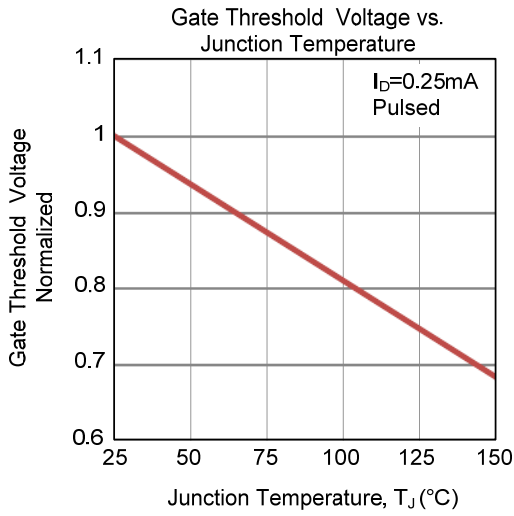


Unclamped Inductive Switching Waveforms

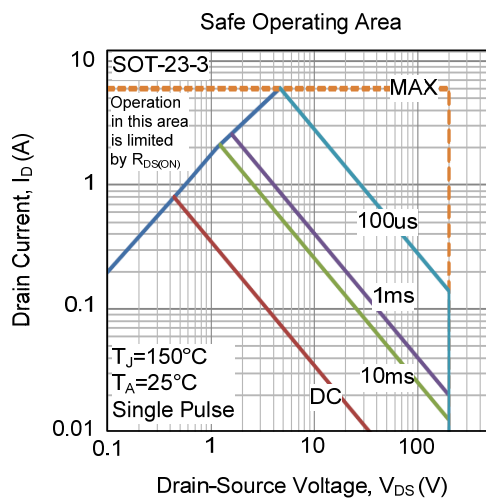
TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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