



## UT60N06

Power MOSFET

### 60A, 60V N-CHANNEL POWER MOSFET

#### DESCRIPTION

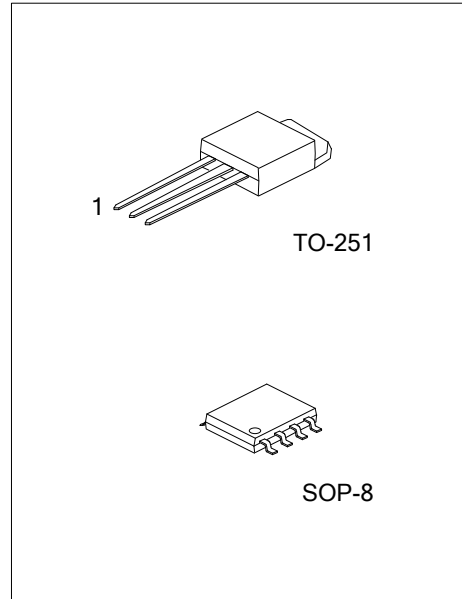
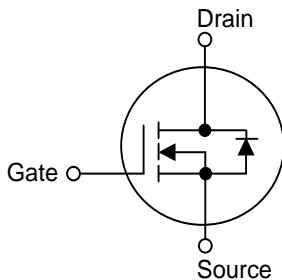
The UTC **UT60N06** is a N-channel enhancement MOSFET using UTC's advanced technology to provide the customers with perfect  $R_{DS(ON)}$  and high switching speed.

The UTC **UT60N06** is suitable for all commercial-industrial applications at power dissipation levels to approximately 50 watts, etc.

#### FEATURES

- \*  $R_{DS(ON)} \leq 11 \text{ m}\Omega$  @  $V_{GS}=10\text{V}$ ,  $I_D=30\text{A}$
- $R_{DS(ON)} \leq 14 \text{ m}\Omega$  @  $V_{GS}=4.5\text{V}$ ,  $I_D=30\text{A}$
- \* High Switching Speed

#### SYMBOL



#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UT60N06L-TM3-T	UT60N06G-TM3-T	TO-251	G	D	S	-	-	-	-	-	Tube
UT60N06L-S08-R	UT60N06G-S08-R	SOP-8	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: Source G: Gate D: Drain

<p>UT60N06G-TM3-T</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Green Package</p>	<p>(1) T: Tube, R: Tape Reel</p> <p>(2) TM3: TO-251, S08: SOP-8</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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#### MARKING

TO-251	SOP-8

## ■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	60	V
Gate-Source Voltage		$V_{GSS}$	$\pm 20$	V
Drain Current	Continuous ( $V_{GS}=10V$ )	$I_D$	60	A
	Pulsed (Note 2)	$I_{DM}$	120	A
Power Dissipation	TO-251	$P_D$	48	W
	SOP-8		5.4	W
Junction Temperature		$T_J$	+150	$^{\circ}C$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^{\circ}C$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

## ■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-251	$\theta_{JA}$	110	$^{\circ}C/W$
	SOP-8		62.5	$^{\circ}C/W$
Junction to Case	TO-251	$\theta_{JC}$	2.6 (Note)	$^{\circ}C/W$
	SOP-8		23.1 (Note)	$^{\circ}C/W$

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

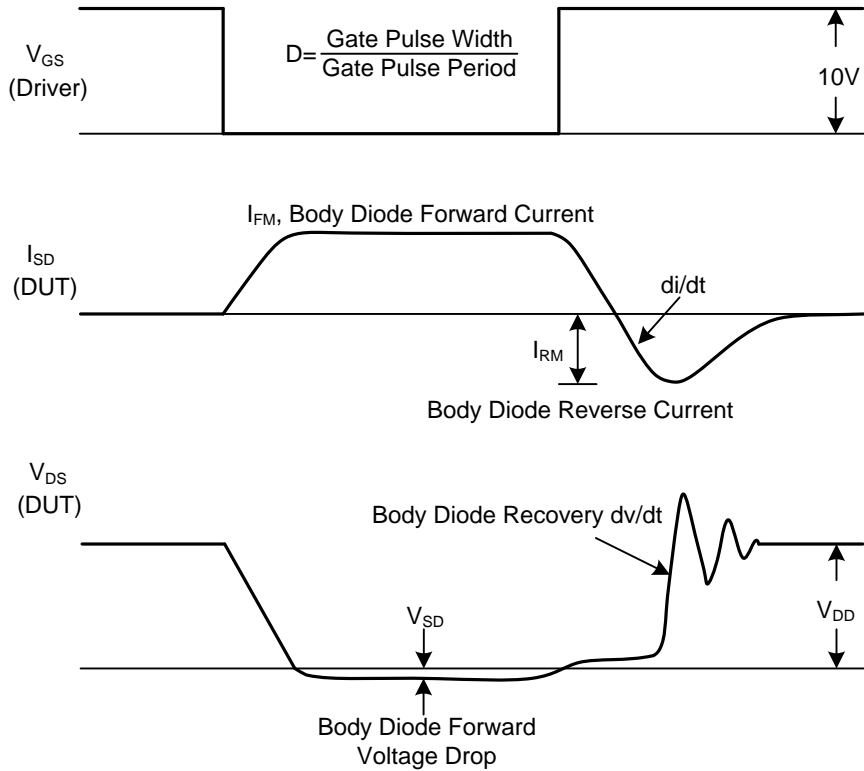
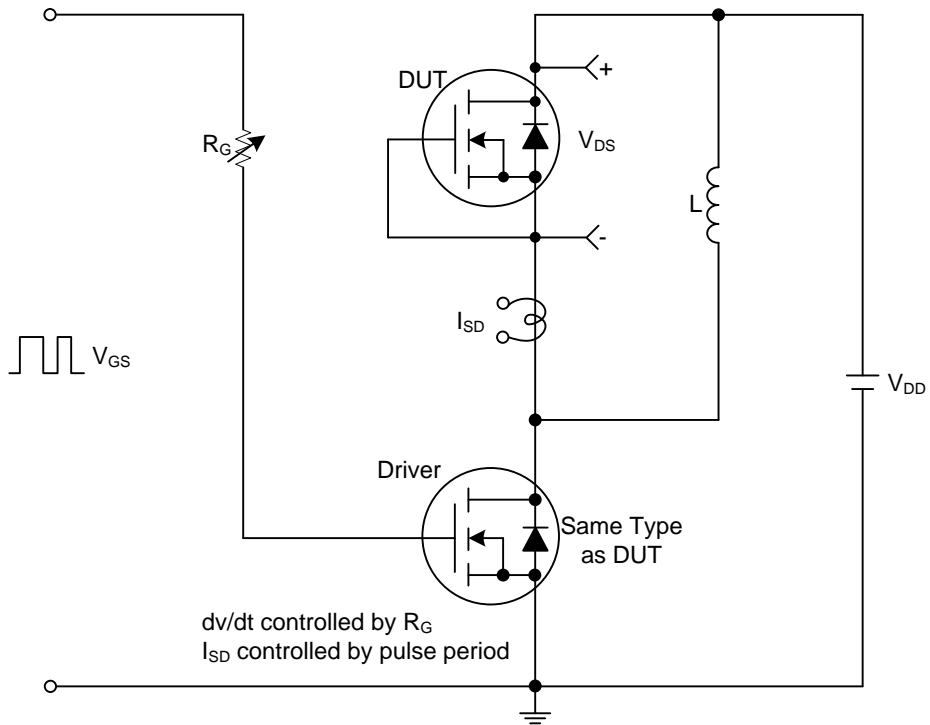
## ■ ELECTRICAL CHARACTERISTICS ( $T_J=25^{\circ}C$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	60			V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=60V, V_{GS}=0V$			1	$\mu A$
Gate- Source Leakage Current	Forward	$I_{GSS}$			+100	nA
	Reverse				-100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0		3.0	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=30A$			11	m $\Omega$
		$V_{GS}=4.5V, I_D=30A$			14	m $\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$C_{ISS}$	$V_{DS}=25V, V_{GS}=0V, f=1MHz$		3500		pF
Output Capacitance	$C_{OSS}$			410		pF
Reverse Transfer Capacitance	$C_{RSS}$			340		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge (Note 1)	$Q_G$	$V_{DS}=48V, V_{GS}=10V, I_D=60A$ $I_G=1mA$ (Note 1, 2)		98		nC
Gate-Source Charge	$Q_{GS}$			6		nC
Gate-Drain Charge	$Q_{GD}$			21		nC
Turn-On Delay Time (Note 1)	$t_{D(ON)}$	$V_{DD}=30V, V_{GS}=10V, I_D=60A,$ $R_G=3\Omega$ (Note 1, 2)		10		ns
Turn-On Rise Time	$t_R$			17		ns
Turn-Off Delay Time	$t_{D(OFF)}$			86		ns
Turn-Off Fall Time	$t_F$			48		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_S$				60	A
Maximum Pulsed Drain-Source Diode Forward Current	$I_{SM}$				120	A
Drain-Source Diode Forward Voltage	$V_{SD}$	$I_S=60A, V_{GS}=0V$			1.4	V

Notes: 1. Pulse Test : Pulse width  $\leq 600\mu s$ , Duty cycle  $\leq 2\%$ .

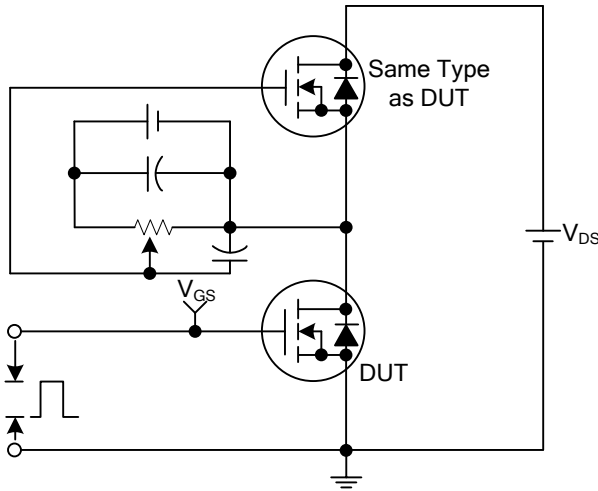
2. Essentially independent of operating ambient temperature.

## TEST CIRCUITS AND WAVEFORMS

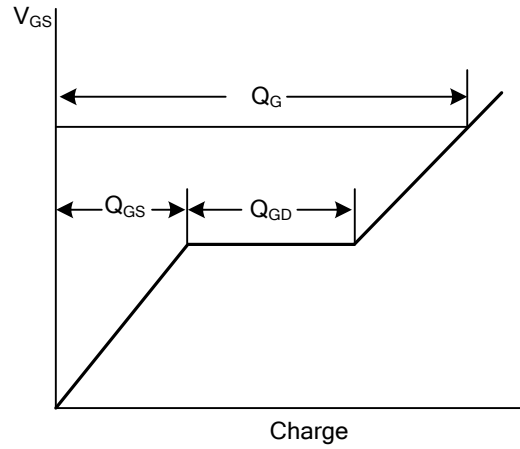


Peak Diode Recovery dv/dt Test Circuit and Waveforms

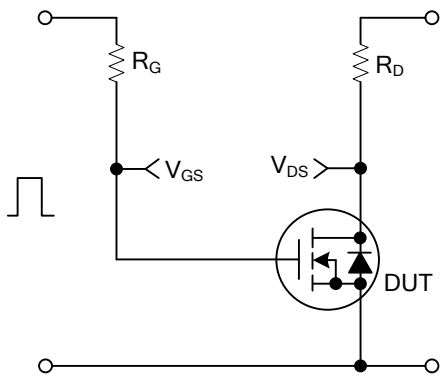
## TEST CIRCUITS AND WAVEFORMS



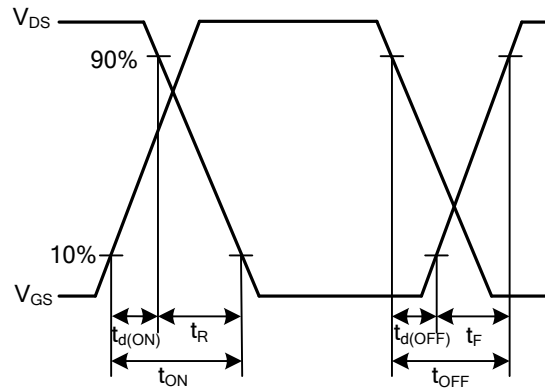
Gate Charge Test Circuit



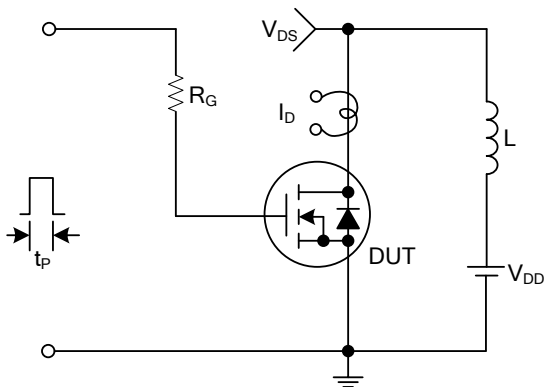
Gate Charge Waveforms



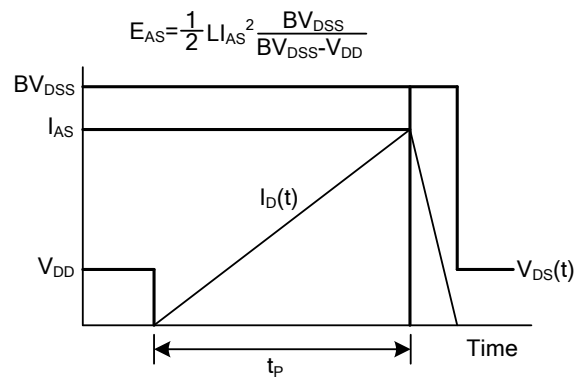
Resistive Switching Test Circuit



Resistive Switching Waveforms

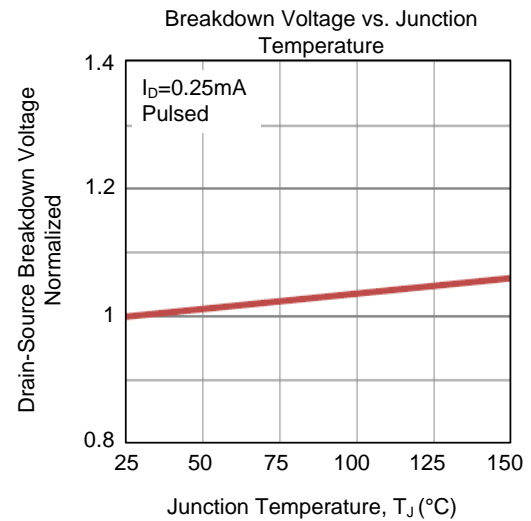
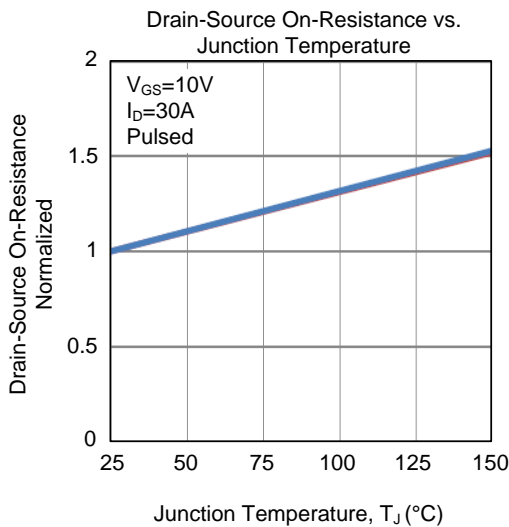
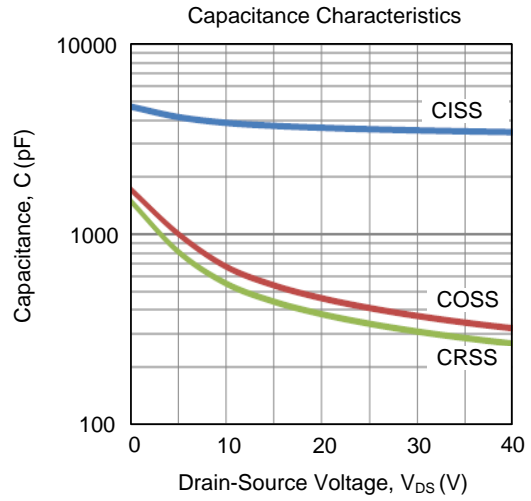
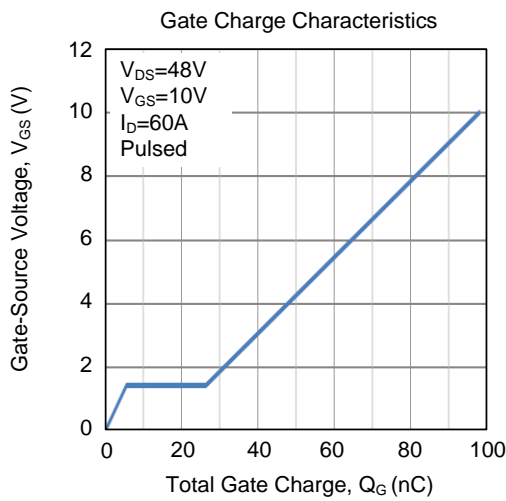
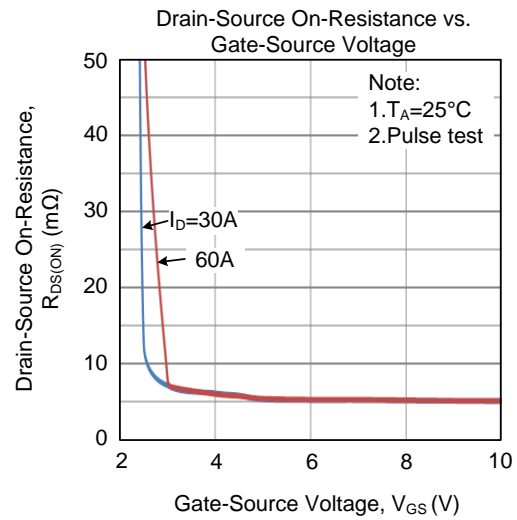
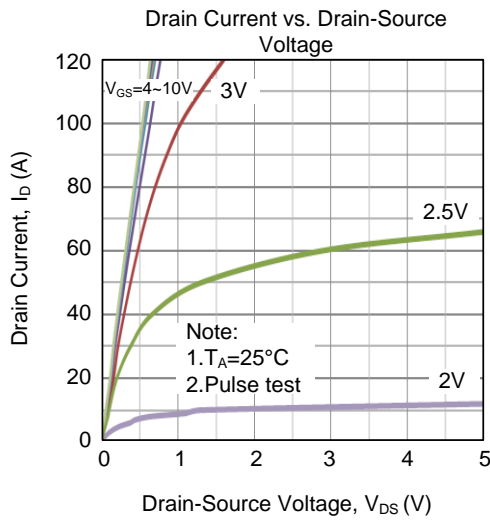


Unclamped Inductive Switching Test Circuit

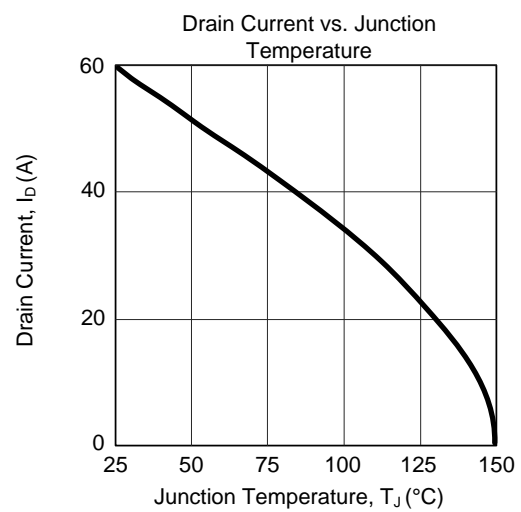
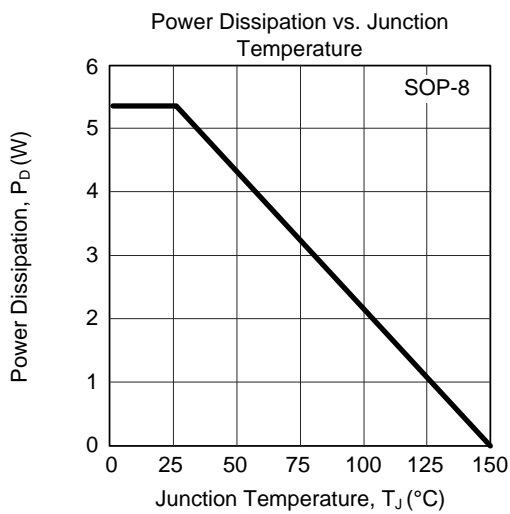
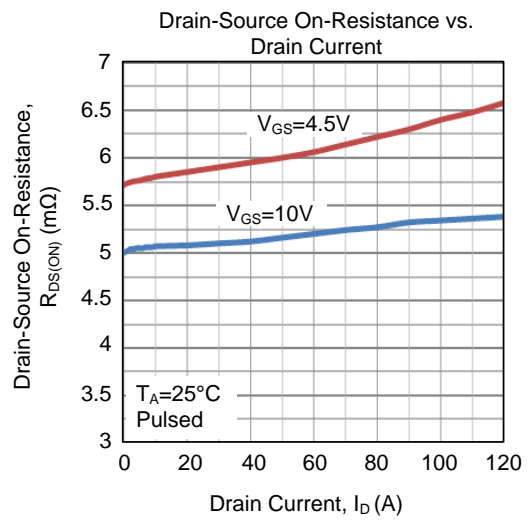
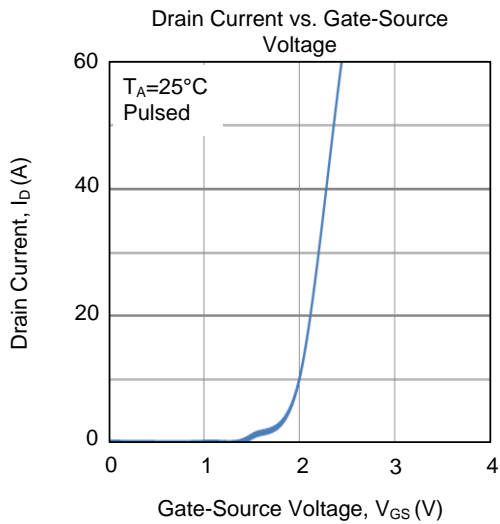
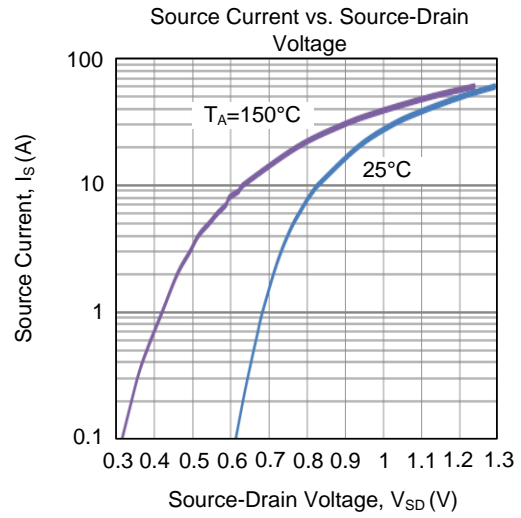
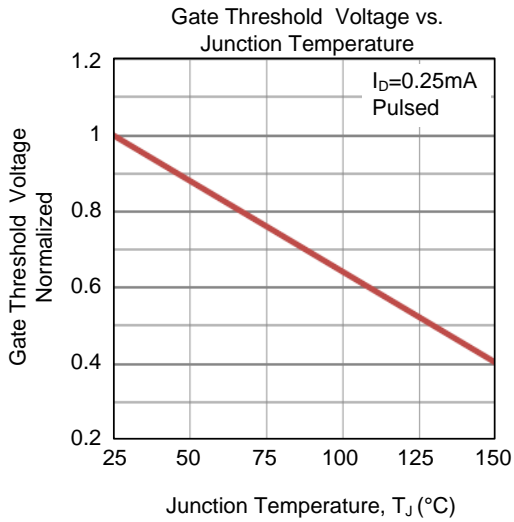


Unclamped Inductive Switching Waveforms

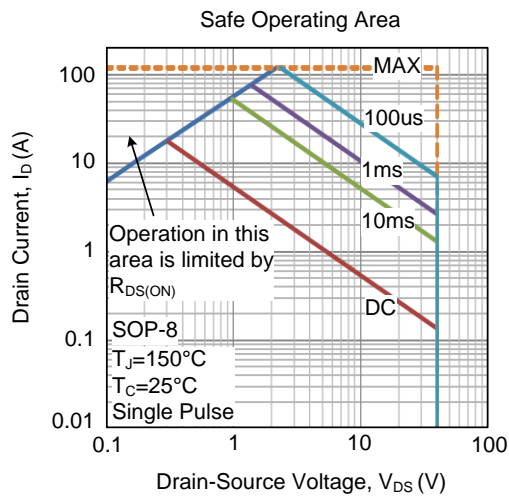
## TYPICAL CHARACTERISTICS



## ■ TYPICAL CHARACTERISTICS (Cont.)



## ■ TYPICAL CHARACTERISTICS (Cont.)



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