



UT7401

Power MOSFET

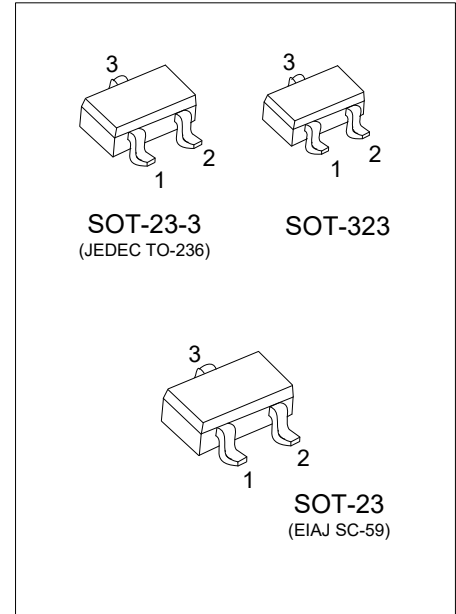
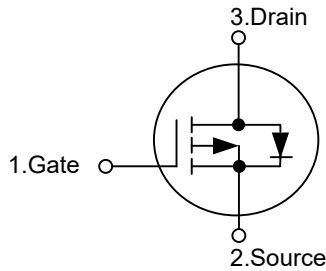
-1.2A, -30V P-CHANNEL ENHANCEMENT MODE POWER MOSFET

DESCRIPTION

The UTC **UT7401** is P-channel enhancement mode power MOSFET, designed in serried ranks. With fast switching speed, low on-resistance, favorable stabilization.

Used in commercial and industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

SYMBOL



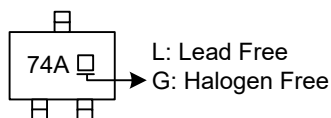
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UT7401L-AE2-R	UT7401G-AE2-R	SOT-23-3	G	S	D	Tape Reel
UT7401L-AE3-R	UT7401G-AE3-R	SOT-23	G	S	D	Tape Reel
UT7401L-AL3-R	UT7401G-AL3-R	SOT-323	G	S	D	Tape Reel

Note: Pin Assignment: S: Source G: Gate D: Drain

UT7401G-AE2-R		(1)Packing Type	(1) R: Tape Reel
		(2)Package Type	(2) AE2: SOT-23-3, AE3: SOT-23, AL3: SOT-323
		(3)Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free

MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current (Note 2)	I_D	$T_A=25^{\circ}\text{C}$	A
		$T_A=70^{\circ}\text{C}$	A
Pulsed Drain Current (Note 3)	I_{DM}	-10	A
Avalanche Energy	E_{AS}	42	mJ
Power Dissipation (Note 2)	P_D	$T_A=25^{\circ}\text{C}$	mW
		$T_A=70^{\circ}\text{C}$	mW
Junction Temperature	T_J	+150	$^{\circ}\text{C}$
Storage Temperature	T_{STG}	-55 ~ +150	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

4. $L=100\text{mH}$, $I_{AS} = -0.9\text{A}$, $V_{DD} = -25\text{V}$, $R_G = 25\ \Omega$, Starting $T_J = 25^{\circ}\text{C}$.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	357	$^{\circ}\text{C/W}$

Note: Device mounted on FR-4 substrate P_c board, 2oz copper, with 1inch square copper plate.

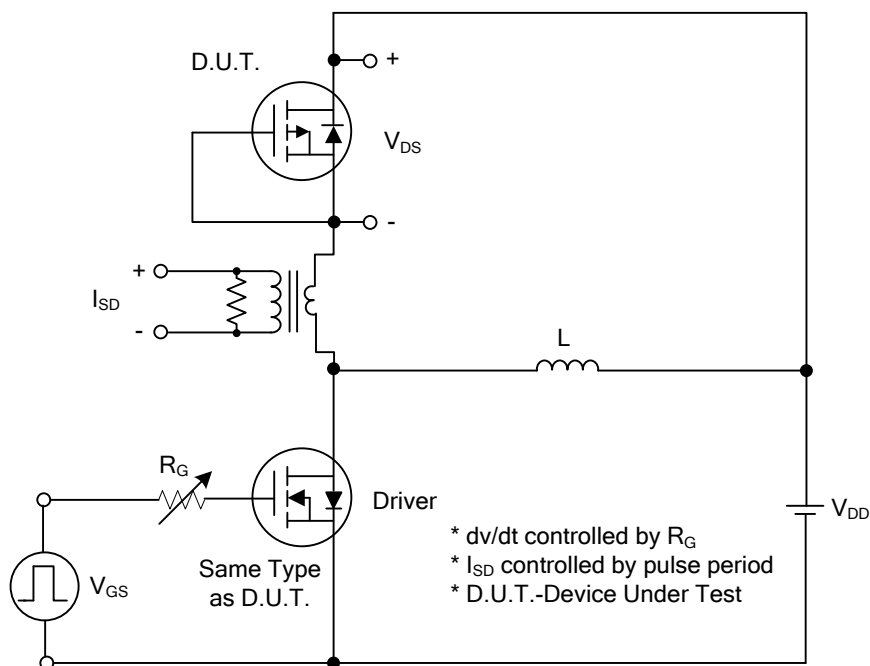
■ ELECTRICAL CHARACTERISTICS ($T_J=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-30			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =-24V, V _{GS} =0V			-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±12V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =-250uA	-0.6	-0.8	-1.4	V
Drain-Source On-State Resistance (Note 1)	R _{DS(ON)}	V _{GS} =-10V, I _D =-1.2A		122	150	mΩ
		V _{GS} =-4.5V, I _D =-1.2A		147	200	mΩ
		V _{GS} =-2.5V, I _D =-1.0A		207	280	mΩ
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{ISS}	V _{GS} =0V, V _{DS} =-15V, f=1MHz		260		pF
Output Capacitance	C _{OSS}			40		pF
Reverse Transfer Capacitance	C _{RSS}			27		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge (Note 1)	Q _G	V _{DS} =-15V, V _{GS} =-4.5V, I _D =-1.0A		2.4		nC
Gate-Source Charge	Q _{GS}			0.52		nC
Gate-Drain Charge	Q _{GD}			0.45		nC
Turn-ON Delay Time (Note 1)	t _{D(ON)}	V _{DS} =-15V, V _{GS} =-10V, R _G =3Ω, I _D =-1.0A		2		ns
Turn-ON Rise Time	t _R			16		ns
Turn-OFF Delay Time	t _{D(OFF)}			14		ns
Turn-OFF Fall Time	t _F			19		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Continuous Drain-Source Diode Forward Current	I _S				-1.2	A
Drain-Source Diode Forward Voltage(Note2)	V _{SD}	V _{GS} =0V, I _S =-1.0A		-0.85	-1.2	V

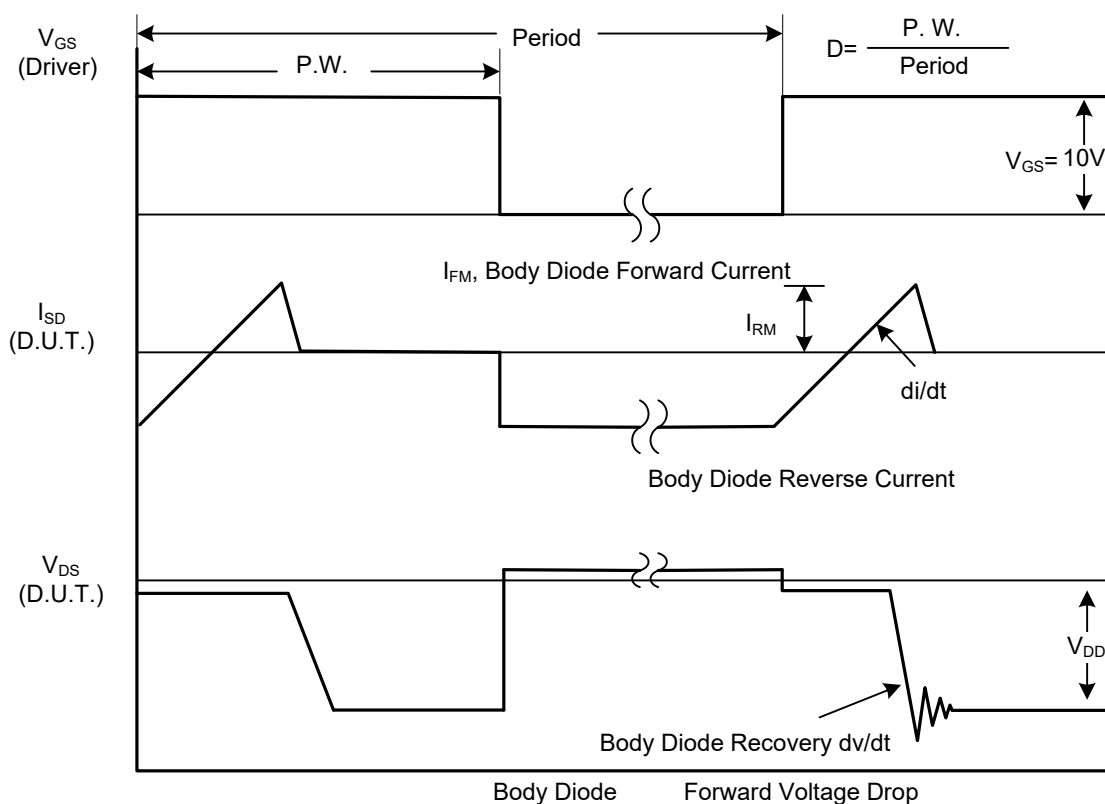
Notes: 1. Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

2. Surface mounted on 1 in² copper pad of FR4 board.

■ TEST CIRCUITS AND WAVEFORMS

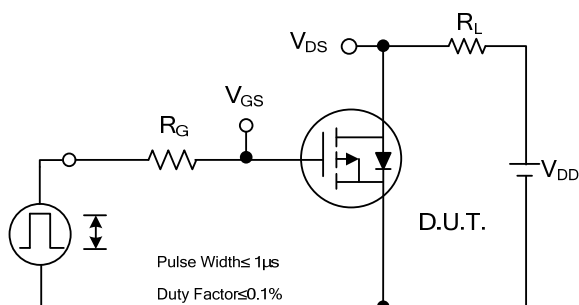


Peak Diode Recovery dv/dt Test Circuit

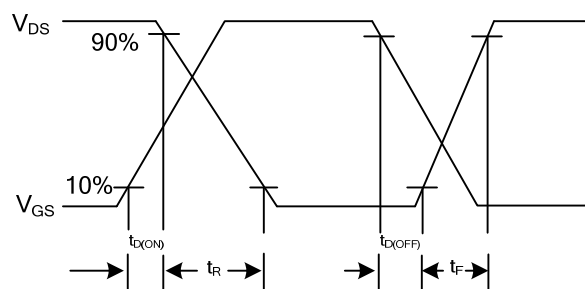


Peak Diode Recovery dv/dt Waveforms

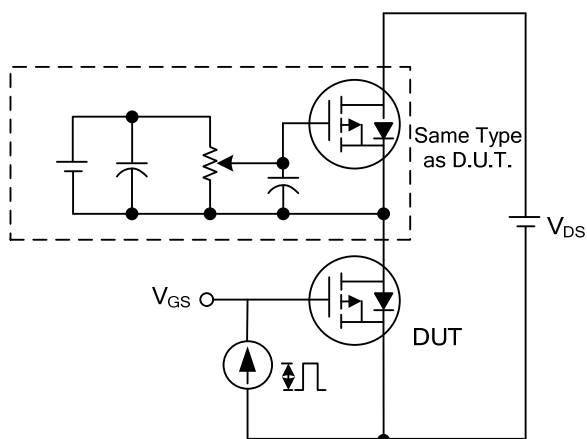
■ TEST CIRCUITS AND WAVEFORMS



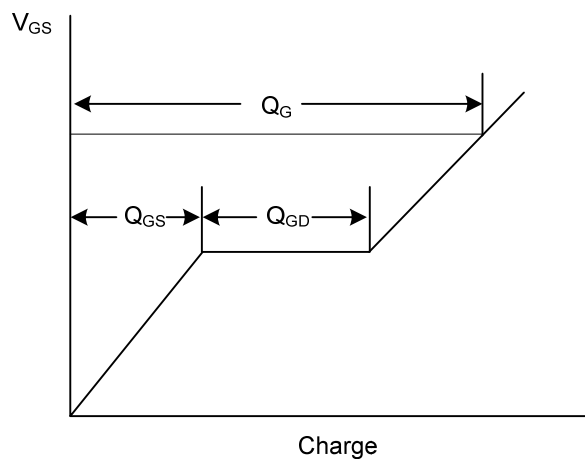
Switching Test Circuit



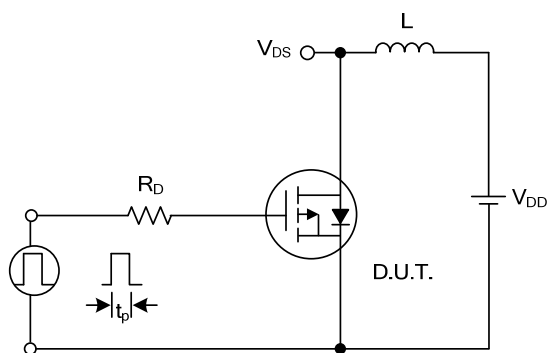
Switching Waveforms



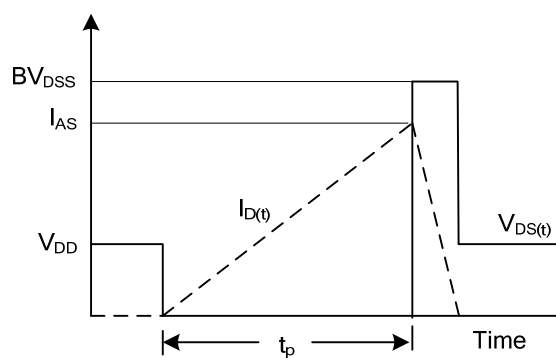
Gate Charge Test Circuit



Gate Charge Waveform

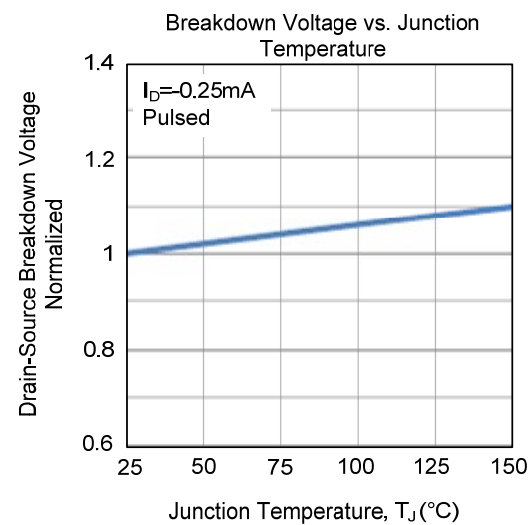
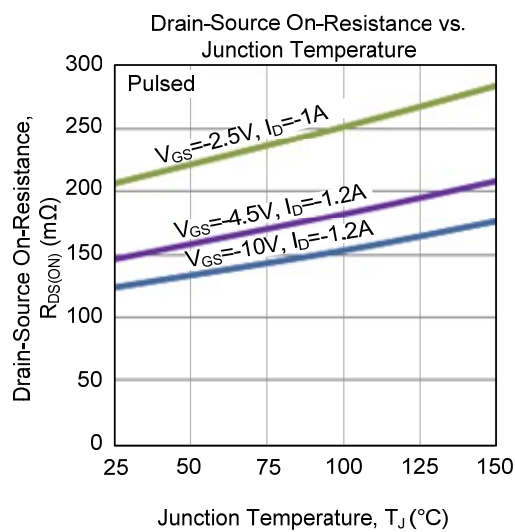
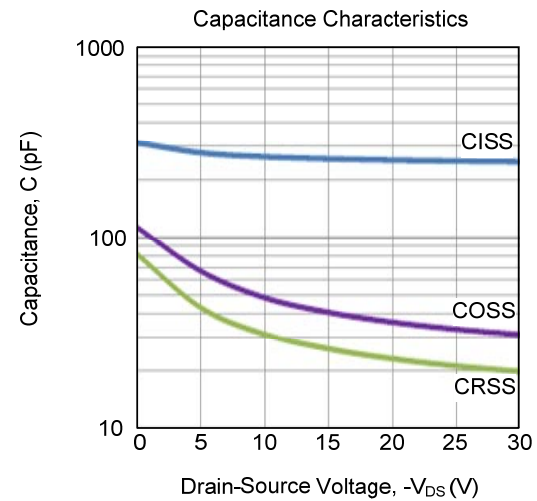
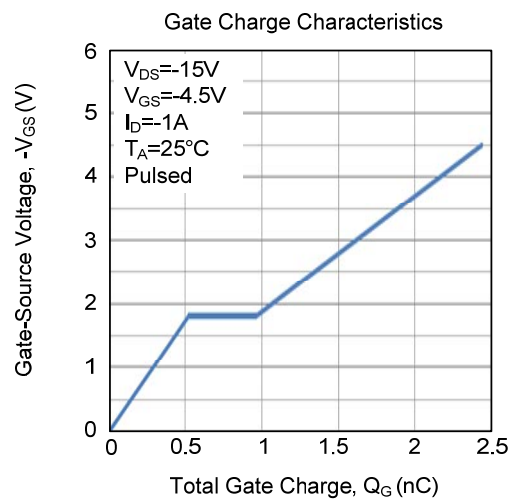
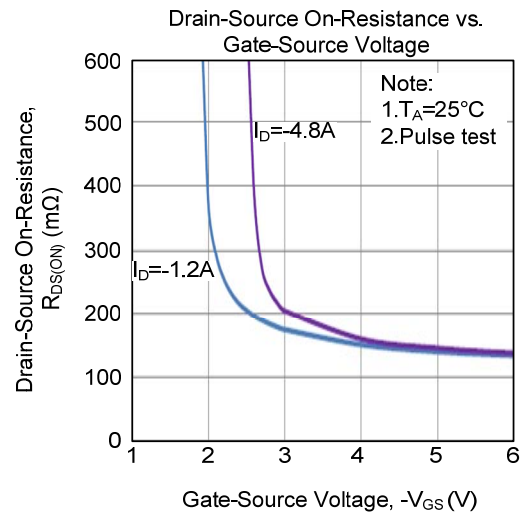
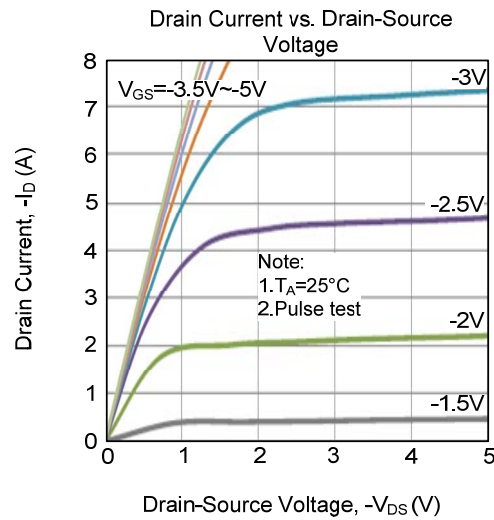


Unclamped Inductive Switching Test Circuit

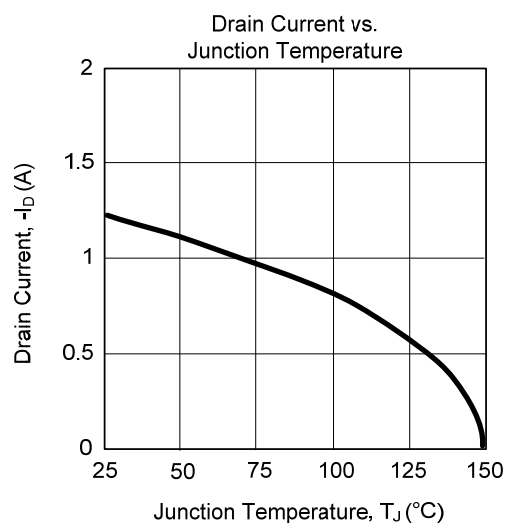
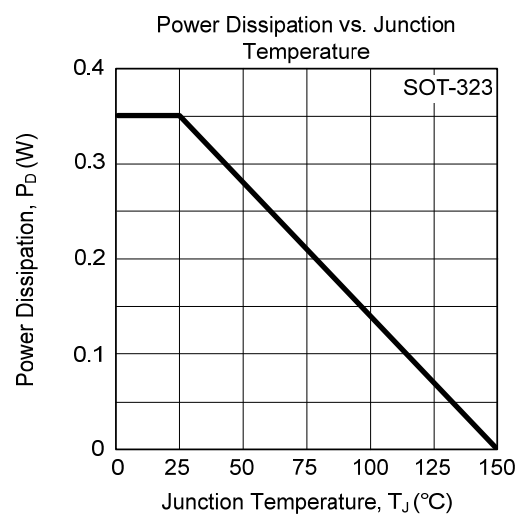
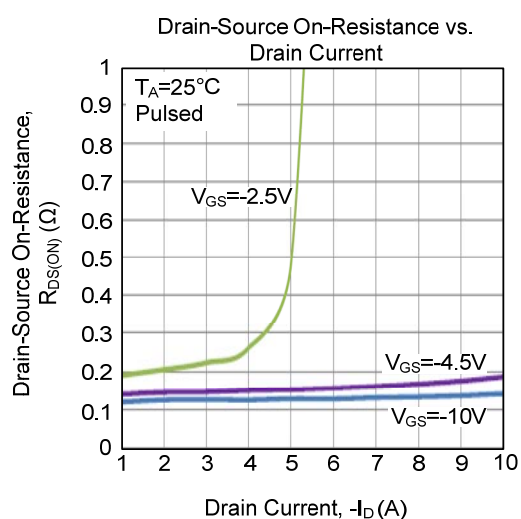
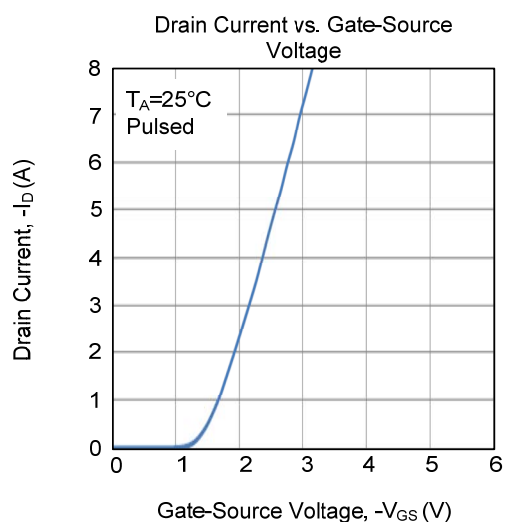
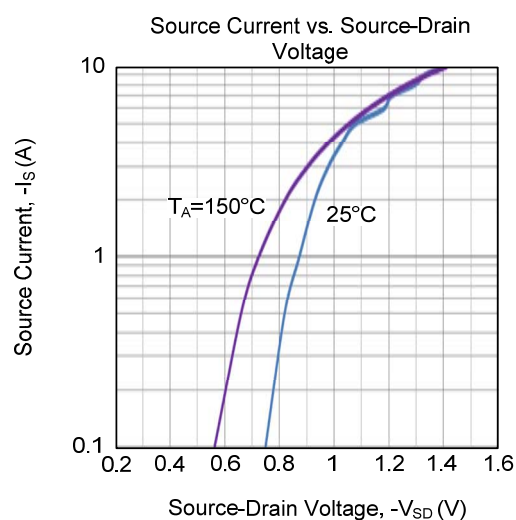
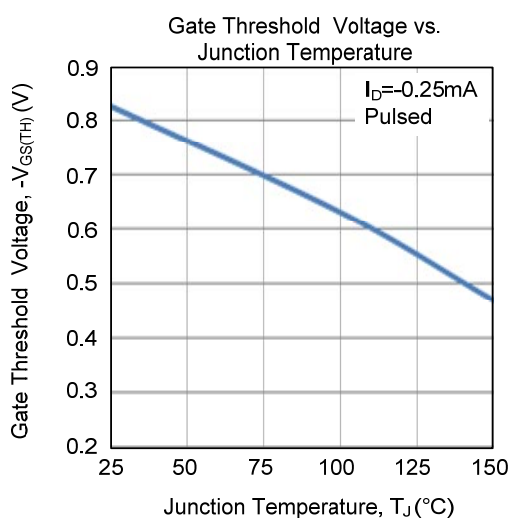


Unclamped Inductive Switching Waveforms

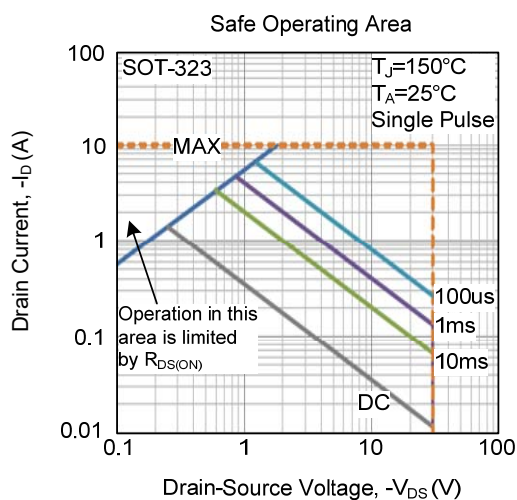
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. UTC reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.