

UNISONIC TECHNOLOGIES CO., LTD

UT75N02 **Preliminary Power MOSFET**

75A, 25V N-CHANNEL POWER MOSFET

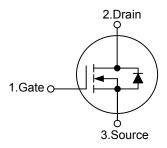
DESCRIPTION

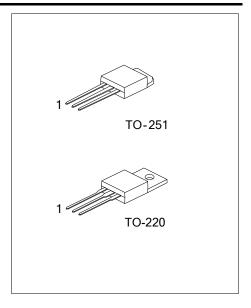
The UTC UT75N02 uses advanced trench technology to provide excellent R $_{DS(ON)}$, lo w gat e charg e an d o peration with low g ate voltages. This device is suitable for use as a load switch or in PWM applications.

FEATURES

- * $R_{DS(ON)}$ < 7m Ω @ V_{GS} =10V
- * $R_{DS(ON)}$ < 8m Ω @ V_{GS} =7V

SYMBOL

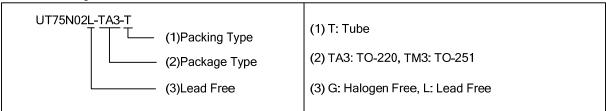




ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Dealing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UT75N02L-TA3-T	UT75N02G-TA3-T	TO-220	G	D	S	Tube	
UT75N02L-TM3-T	UT75N02G-TM3-T	TO-251	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER SYMBOL			RATINGS	UNIT
Drain Source Voltage		V _{DSS} 25		V
Gate-Source Voltage		V _{GSS} ±20		V
Continuous Drain Current		I _D 75		Α
Pulsed Drain Current (Note 2)		I _{DM} 170		Α
Avalanche Current		I _{AR} 60		Α
Avalanche Energy	L=0.1mH	E _{AS} 140		mJ
Repetitive Avalanche Energy (Note 3) L=0.05mH		E _{AR} 5.6		mJ
Power Dissipation	TO-220	- P _D	40	W
	TO-251		28	VV
Junction Temperature		T _J +	150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Note: 1.Absolute ma ximum ratings a re those v alues be youd which the d evice could be permanently dam aged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Pulse width limited by maximum junction temperature.
- 3. Duty cycle≤1%.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient	TO-220	0	62.5	°C/W	
	TO-251	θ_{JA}	110		
Junction to Case	TO-220	0	3.13	°C/M	
	TO-251	$\theta_{ m JC}$	4.53	°C/W	

■ ELECTRICAL CHARACTERISTICS (T_C = 25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		TYP	MAX	UNIT
OFF CHARACTERISTICS	•		•		•	
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250µA 25				V
Dunin Course Leekens Current		$V_{DS} = 20V$, $V_{GS} = 0V$			25	μΑ
Drain-Source Leakage Current	I _{DSS}	V_{DS} =20V, V_{GS} = 0V, T_{J} = 125°C			250	μA
Gate-Source Leakage Current	I _{GSS} V				±250	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$		1 1.5 3		V
On-State Drain Current (Note 1)	I _{D(ON)} V	_{DS} = 10V, V _{GS} = 10V				Α
Static Drain-Source On-Resistance	В	$V_{GS} = 10V, I_D = 30A$		5	7	mΩ
(Note 1)	R _{DS(ON)}	$V_{GS} = 7V, I_D = 24A$		6	8	mΩ
DYNAMIC PARAMETERS				-		_
Input Capacitance	C _{ISS}	V _{DS} =15V, V _{GS} =0 V, f=1MHz		0 0		pF
Output Capacitance	Coss			1800		pF
Reverse Transfer Capacitance	C _{RSS}			0		pF
SWITCHING PARAMETERS (Note 2)				-		_
Turn-ON Delay Time	t _{D(ON)}		7			ns
Turn-ON Rise Time	t_R	$V_{DS} = 15V, V_{GS} = 10V, I_{D} \approx 30A$ $R_{GS} = 2.5\Omega, R_{L} = 1\Omega,$				ns
Turn-OFF Delay Time	t _{D(OFF)}					ns
Turn-OFF Fall-Time	t_{F}		6			ns
Total Gate Charge	$Q_G V$	$_{DS}$ =0.5 $V_{(BR)DSS}$, V_{GS} =10 V , I_{D} =35 A	14	0		nC
Gate Source Charge	Q_{GS}		40			nC
Gate Drain Charge	Q_{GD}		75			nC
SOURCE- DRAIN DIODE RATINGS AND	CHARACT	ERISTICS				
Forward Voltage (Note 1)	V_{SD}	$I_F = I_S$, $V_{GS} = 0V$			1.3	V
Continuous Current	Is				75	Α

Notes: 1. Pulse test : Pulse Width≤300µsec, Duty Cycle≤2%

^{2.} Independent of operating temperature

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