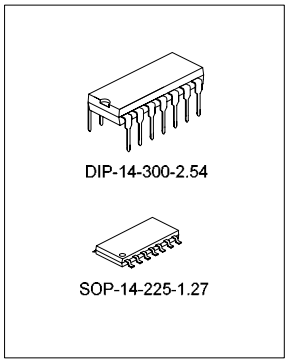
UTC324 LINEAR INTEGRATED CIRCUIT

QUAD OPERATIONAL AMPLIFIERS



DESCRIPTION

The UTC324 consists of four independent, high gain internally frequency compensated operational amplifiers which were designed specifically to operate from a single power supply over a wide voltage range.

Operation from split power supplies is also possible so long as the difference between the two supplies 3 Volts to 32 volts.

Application areas include transducer amplifier, DC gain blocks and all the conventional OP amp circuits which now can be easily implemented in single power supply system.

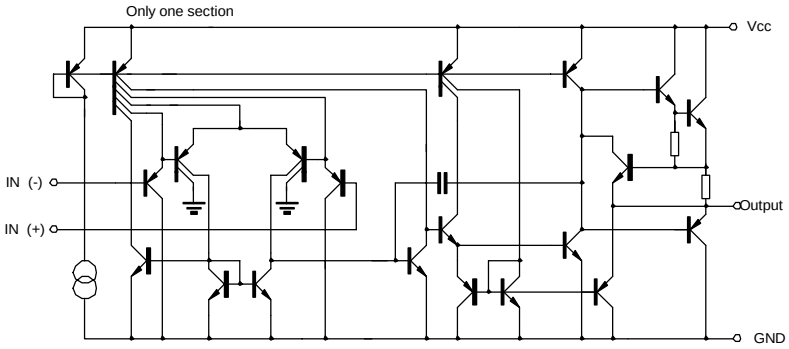
FEATURES

*Internally frequency compensated for unity gain
*Large DC voltage gain :100dB
*Wide operating supply range(Vcc=3V~32V)
*Input common-mode voltage includes ground
*Large output voltage swing: From 0V to Vcc-1.5V
*Power drain suitable for battery operation

ORDERING INFORMATION

Device	Package
UTC324D	DIP-14-300-2.54
UTC324E	SOP-14-225-1.27

BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

Characteristic	Symbol	Value	Unit
Supply Voltage	Vcc	±18 or 36	V
Differential input voltage	$V_{i(diff)}$	32	V
Input Voltage	VI	-0.3~32V	V
Power Dissipation	Pd	570	mW
Operating Temperature	Topr	0 to +70	°C

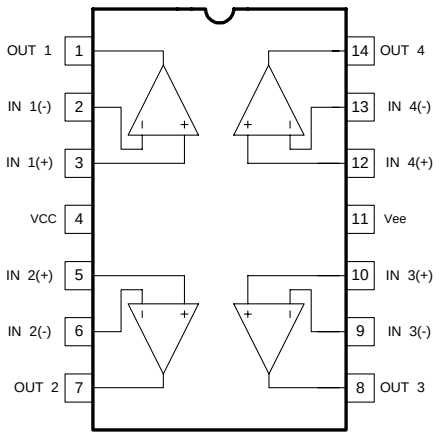
Storage Temperature	Tstg	-65 to 150	°C

ELECTRICAL CHARACTERISTICS(Ta=25°C)

(Vcc=5.0V,All voltage referenced to GND unless otherwise specified)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Input offset voltage	V_{IO}	V_{CM}=0 to Vcc-1.5 Vo(p)=1.4V,Rs=0		1.5	7.0	mV
Input offset current	I_{IO}			3.0	50	nA
Input Bias current	Ib			40	250	nA
Input Common-mode voltage range	$V_{I(R)}$	Vcc=30V	0	Vcc-1.5		V
Supply Current	Icc	R_L=∞,Vcc=30V		1.0	3	mA
		Vcc=5V		0.7	1.2	mA
Large signal Voltage Gain	G_V	Vcc=15V,R_L>2kΩ Vo(p)=1V to 11V	25	100		V/mV
Output voltage Swing	$V_{(OH)}$	Vcc=30V,R_L=2kΩ	26			V
		Vcc=30V,R_L=10kΩ	27	28		V
	$V_{(OL)}$	Vcc=5,R_L>10kΩ		5	20	mV
Common-mode rejection Ratio	CMRR		65	75		dB
Power supply rejection Ratio	PSRR		65	100		dB
Channel Separation	CS	f=1kHz to 20kHz		5	20	mV
Short circuit to GND	Isc			40	60	mA
Output current	Isource	VI(+)=1V,VI(-)=0 Vcc=15V,Vo(p)=2V	20	40		mA
	Isink	VI(+)=0V,VI(-)=1V Vcc=15V,Vo(p)=2V	10	13		mA
		VI(+)=1V,VI(-)=0 Vcc=15V,Vo(p)=200V	12	45		μA
Differential input voltage	VI(diff)				Vcc	V

PIN CONFIGURATION



TYPICAL CHARACTERISTICS PERFORMANCE

Fig.1 Input Voltage Range

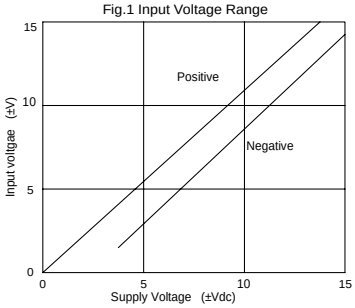


Fig.2 Input Current

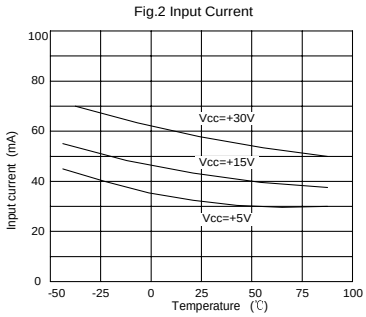


Fig.3 Supply Current

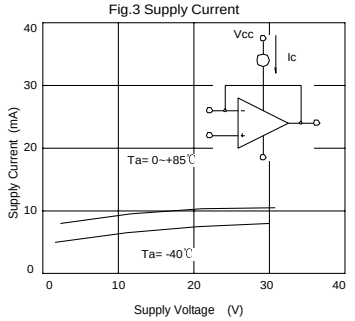


Fig.4 Voltage Gain

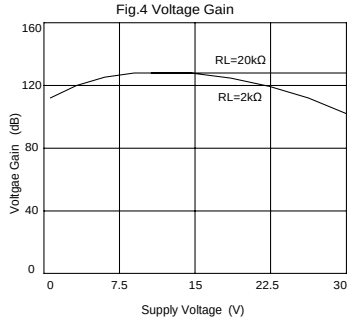


Fig.5 Open Loop Frequency response

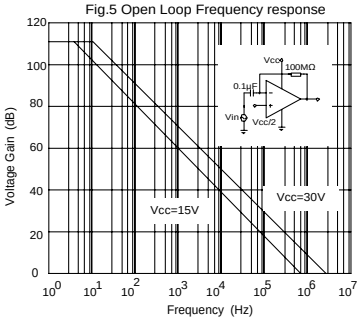


Fig.6 Common-mode rejection Ratio

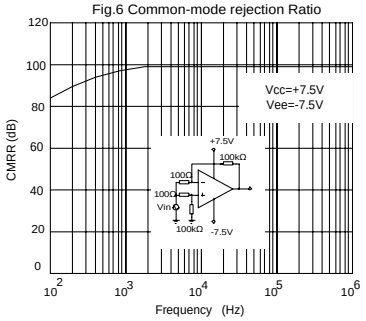


Fig.7

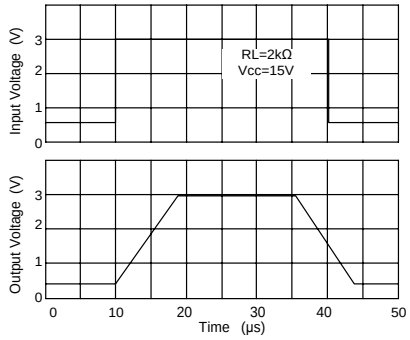


Fig.8 voltage Follower pulse response (small signal)

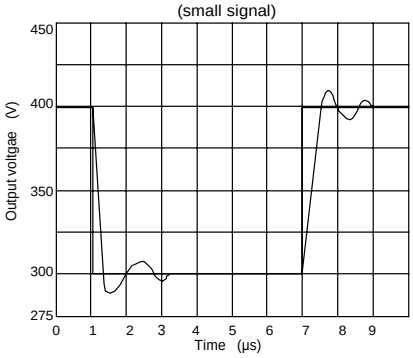


Fig.9 Large signal Frequency Response

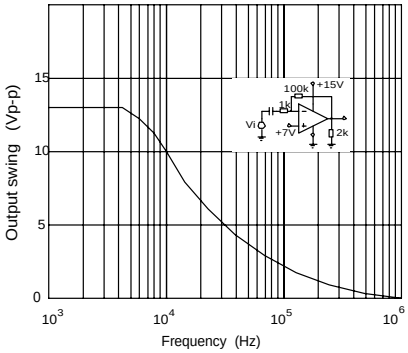


Fig.10 Output Characteristics current sourcing

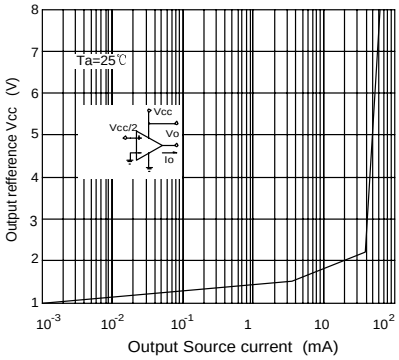


Fig.11 Output Characteristics Current sinking

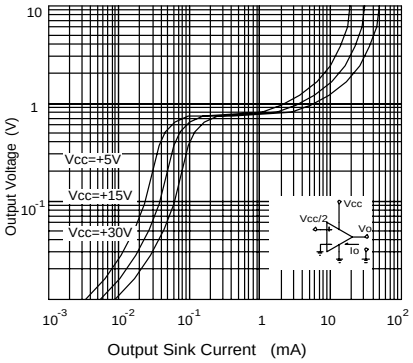
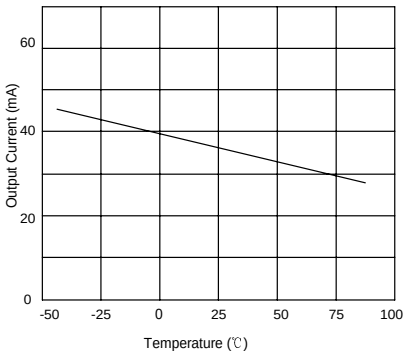
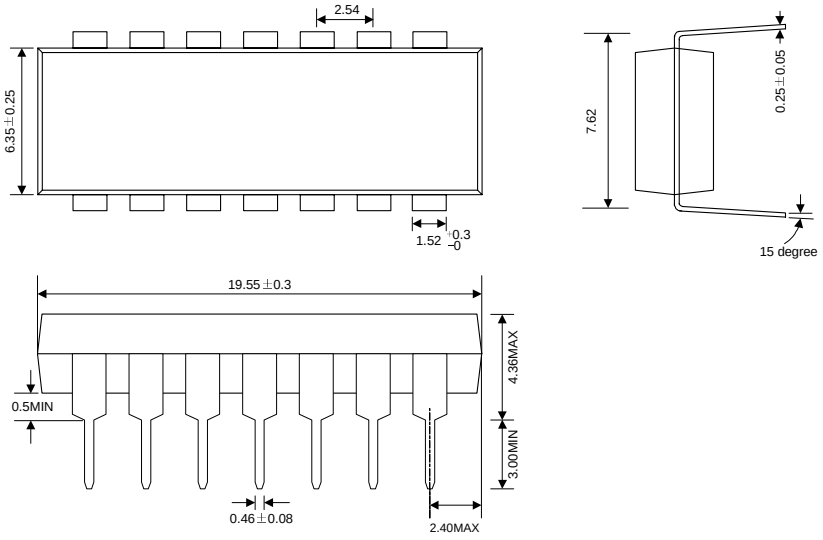


Fig.12 Current Limiting

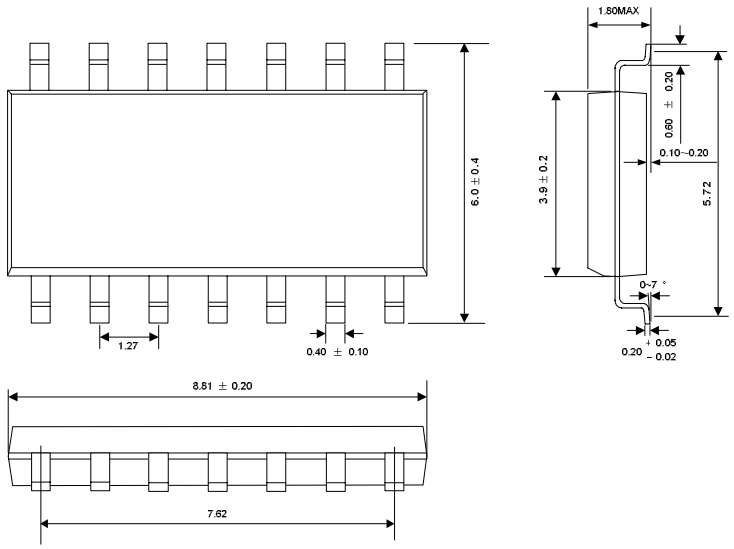


PACKAGE OUTLINE

DIP-14-300-2.54 Unit: mm



SOP-14-225-1.27 UNIT: mm



Attach

Revision History

Data	REV	Description	Page
	1.0	Original	
2003.10.15	1.1	Add “CHIP TOPOGRAPHY” Add “PAD COORDINATES”	5 5
2004.07.20	1.2	Add”SOP-14-225-1.27” Add”DIP-14-300-2.54、SOP-14-225-1.27”Package out line	1 5