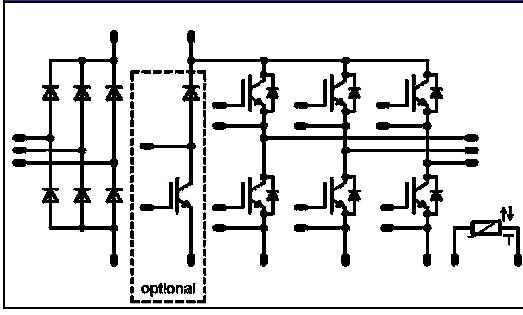


<b>flowPIM 1</b>		<b>1200V/25A</b>
<b>Features</b>		
<ul style="list-style-type: none"> <li>• 3~rectifier, optional BRC, Inverter, NTC</li> <li>• Very compact housing, easy to route</li> <li>• IGBT4 / EmCon4 technology for low saturation losses and improved EMC behaviour</li> </ul>		
<b>Target Applications</b>		
<ul style="list-style-type: none"> <li>• Industrial drives</li> <li>• Embedded Drives</li> </ul>		
<b>Types</b>		
<ul style="list-style-type: none"> <li>• V23990-P589-A41-PM</li> <li>• V23990-P589-A41Y-PM</li> <li>• V23990-P589-A418-PM</li> <li>• V23990-P589-C41-PM</li> </ul>		
<b>flow1 housing</b>		
12mm housing Solder pins	17mm housing Solder pins	17mm housing Pressfit pins
<b>Schematic</b>		
		

## Maximum Ratings

T<sub>j</sub>=25°C, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
<b>Input Rectifier Diode</b>				
Repetitive peak reverse voltage	V <sub>RRM</sub>		1600	V
DC forward current	I <sub>FAV</sub>	T <sub>j</sub> =T <sub>j</sub> max T <sub>c</sub> =80°C	33 47	A
Surge forward current	I <sub>FSM</sub>	t <sub>p</sub> =10ms half sine wave	250	A
I <sup>2</sup> t-value	I <sup>2</sup> t		310	A <sup>2</sup> s
Power dissipation per Diode	P <sub>tot</sub>	T <sub>j</sub> =T <sub>j</sub> max T <sub>c</sub> =80°C	37 60	W
Maximum Junction Temperature	T <sub>j</sub> max		150	°C

## Inverter Transistor

Collector-emitter break down voltage	V <sub>CE</sub>		1200	V
DC collector current	I <sub>C</sub>	T <sub>j</sub> =T <sub>j</sub> max T <sub>c</sub> =80°C	27 34	A
Pulsed collector current	I <sub>Cpulse</sub>	t <sub>p</sub> limited by T <sub>j</sub> max	75	A
Turn off safe operating area		V <sub>CE</sub> ≤ 1200V, T <sub>j</sub> ≤ Top max	50	A
Power dissipation per IGBT	P <sub>tot</sub>	T <sub>j</sub> =T <sub>j</sub> max T <sub>c</sub> =80°C	73 111	W
Gate-emitter peak voltage	V <sub>GE</sub>		±20	V
Short circuit ratings	t <sub>sc</sub> V <sub>CC</sub>	T <sub>j</sub> ≤150°C V <sub>GE</sub> =15V	10 800	μs V
Maximum Junction Temperature	T <sub>j</sub> max		175	°C

## Maximum Ratings

$T_j=25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
<b>Inverter Diode</b>				
Peak Repetitive Reverse Voltage	$V_{RRM}$		1200	V
DC forward current	$I_F$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	25 32	A
Repetitive peak forward current	$I_{FRM}$	$t_p$ limited by $T_j\max$	50	A
Power dissipation per Diode	$P_{tot}$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	52 79	W
Maximum Junction Temperature	$T_j\max$		175	°C
<b>Brake Transistor</b>				
Collector-emitter break down voltage	$V_{CE}$		1200	V
DC collector current	$I_C$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	18 22	A
Pulsed collector current	$I_{Cpuls}$	$t_p$ limited by $T_j\max$	45	A
Turn off safe operating area		$VCE \leq 1200\text{V}$ , $T_j \leq T_{j\max}$	50	A
Power dissipation per IGBT	$P_{tot}$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	53 80	W
Gate-emitter peak voltage	$V_{GE}$		$\pm 20$	V
Short circuit ratings	$t_{sc}$ $V_{CC}$	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	10 800	$\mu\text{s}$ V
Maximum Junction Temperature	$T_j\max$		175	°C
<b>Brake Diode</b>				
Peak Repetitive Reverse Voltage	$V_{RRM}$		1200	V
DC forward current	$I_F$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	14 19	A
Repetitive peak forward current	$I_{FRM}$	$t_p$ limited by $T_j\max$	20	A
Power dissipation per Diode	$P_{tot}$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	29 44	W
Maximum Junction Temperature	$T_j\max$		175	°C
<b>Thermal Properties</b>				
Storage temperature	$T_{stg}$		-40...+125	°C
Operation temperature under switching condition	$T_{op}$		-40...+( $T_{j\max} - 25$ )	°C
<b>Insulation Properties</b>				
Insulation voltage	$V_{is}$	$t=2\text{s}$ DC voltage	4000	V
Creepage distance			min 12,7	mm
Clearance			min 12,7	mm
Comparative tracking index	CTI		>200	

**Characteristic Values**

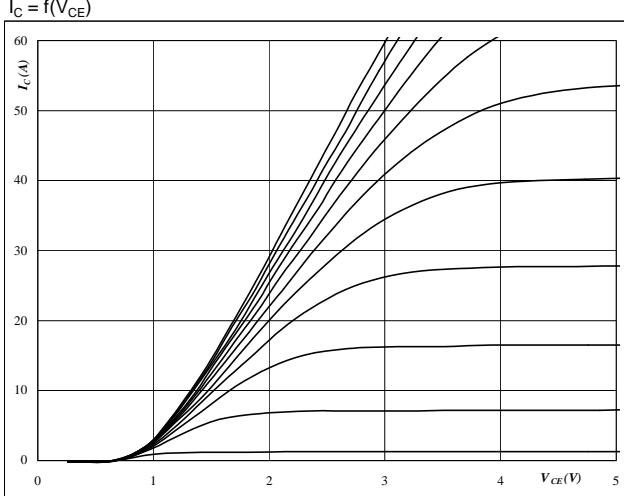
Parameter	Symbol	Conditions				Value			Unit	
			$V_{GE}$ [V] or $V_{GS}$ [V]	$V_r$ [V] or $V_{CE}$ [V] or $V_{DS}$ [V]	$I_c$ [A] or $I_F$ [A] or $I_D$ [A]	$T_j$	Min	Typ	Max	
<b>Input Rectifier Diode</b>										
Forward voltage	$V_F$				30	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$	0,8	1,16 1,13	1,6	V
Threshold voltage (for power loss calc. only)	$V_{to}$				30	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		0,90 0,78		V
Slope resistance (for power loss calc. only)	$r_t$				30	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		8,00 11,00		$\text{m}\Omega$
Reverse current	$I_r$			1500		$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$			2	$\text{mA}$
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50um $\lambda = 1 \text{ W/mK}$						1,89		K/W
<b>Inverter Transistor</b>										
Gate emitter threshold voltage	$V_{GE(\text{th})}$	$V_{CE}=V_{GE}$			0,00085	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$	5,2	5,8	6,4	V
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$		15		25	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$	1,9	1,94 2,40	2,4	V
Collector-emitter cut-off current incl. Diode	$I_{CES}$		0	1200		$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$			0,0024	$\text{mA}$
Gate-emitter leakage current	$I_{GES}$		20	0		$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$			120	$\text{nA}$
Integrated Gate resistor	$R_{gint}$							-		$\Omega$
Turn-on delay time	$t_{d(on)}$	$R_{goff}=32 \Omega$ $R_{gon}=32 \Omega$	$\pm 15$	600	25	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		126 126		ns
Rise time	$t_r$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		21 28		
Turn-off delay time	$t_{d(off)}$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		220 284		
Fall time	$t_f$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		74 100		
Turn-on energy loss per pulse	$E_{on}$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		1,64 2,53		mWs
Turn-off energy loss per pulse	$E_{off}$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		1,38 2,17		
Input capacitance	$C_{ies}$	$f=1\text{MHz}$	$0$	25		$T_j=25^\circ\text{C}$		1430		pF
Output capacitance	$C_{oss}$							115		
Reverse transfer capacitance	$C_{rss}$							85		
Gate charge	$Q_{\text{Gate}}$		$\pm 15$			$T_j=25^\circ\text{C}$		200		nC
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50um $\lambda = 1 \text{ W/mK}$						1,30		K/W
<b>Inverter Diode</b>										
Diode forward voltage	$V_F$				25	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$	1,3	1,97 1,94	2,2	V
Peak reverse recovery current	$I_{RRM}$	$R_{gon}=32 \Omega$	$\pm 15$	600	25	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		32 34		A
Reverse recovery time	$t_{rr}$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		265 436		
Reverse recovered charge	$Q_{rr}$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		2,50 4,81		
Peak rate of fall of recovery current	$\frac{di(\text{rec})}{dt}$ max					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		1722 580		
Reverse recovered energy	$E_{rec}$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		0,98 1,94		
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50um $\lambda = 1 \text{ W/mK}$						1,83		K/W

### Characteristic Values

Parameter	Symbol	Conditions				Value			Unit				
			$V_{GE}$ [V] or $V_{GS}$ [V]	$V_T$ [V] or $V_{CE}$ [V] or $V_{DS}$ [V]	$I_c$ [A] or $I_F$ [A] or $I_D$ [A]	$T_j$	Min	Typ	Max				
<b>Brake Transistor</b>													
Gate emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=V_{GE}$			0,00085	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$	5	5,8	6,5	V			
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		15	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$	1,6	1,88 2,30	2,2	V			
Collector-emitter cut-off incl diode	$I_{CES}$		0	1200		$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$			0,005	mA			
Gate-emitter leakage current	$I_{GES}$		20	0		$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$			200	nA			
Integrated Gate resistor	$R_{gint}$							-		$\Omega$			
Turn-on delay time	$t_{d(on)}$	$R_{goff}=32\ \Omega$ $R_{gon}=32\ \Omega$	$\pm 15$	600	15	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		87 88		ns			
Rise time	$t_r$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		24 29					
Turn-off delay time	$t_{d(off)}$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		194 258					
Fall time	$t_f$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		77 111					
Turn-on energy loss per pulse	$E_{on}$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		0,950 1,381		mWs			
Turn-off energy loss per pulse	$E_{off}$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		0,824 1,273					
Input capacitance	$C_{ies}$							900					
Output capacitance	$C_{oss}$	$f=1\text{MHz}$	0	25		$T_j=25^\circ\text{C}$		80		pF			
Reverse transfer capacitance	$C_{rss}$							55					
Gate charge	$Q_{Gate}$							120		nC			
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50μm $\lambda = 1\text{ W/mK}$						1,80		K/W			
<b>Brake Diode</b>													
Diode forward voltage	$V_F$				10	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$	1,3	1,85 1,76	2,2	V			
Reverse leakage current	$I_r$			1200		$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$			5	$\mu\text{A}$			
Peak reverse recovery current	$I_{RRM}$	$R_{gon}=32\ \Omega$	$\pm 15$	600	15	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		10 12		A			
Reverse recovery time	$t_{rr}$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		324 538		ns			
Reverse recovered charge	$Q_{rr}$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		1,38 1,38		$\mu\text{C}$			
Peak rate of fall of recovery current	$d(i_{rec})/\text{max dt}$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		46 44		$\text{A}/\mu\text{s}$			
Reverse recovery energy	$E_{rec}$					$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		0,581 1,081		mWs			
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50μm $\lambda = 1\text{ W/mK}$						3,28		K/W			
<b>Thermistor</b>													
Rated resistance	$R$					$T_j=25^\circ\text{C}$		22000		$\Omega$			
Deviation of R25	$\Delta R/R$					$T_j=25^\circ\text{C}$	-5		5	%			
Power dissipation	$P$					$T_j=25^\circ\text{C}$		200		mW			
Power dissipation constant						$T_j=25^\circ\text{C}$		2		$\text{mW/K}$			
B-value	$B_{(25/50)}$	Tol. ±3%				$T_j=25^\circ\text{C}$		3950		K			
B-value	$B_{(25/100)}$	Tol. ±3%				$T_j=25^\circ\text{C}$		3996		K			
Vincotech NTC Reference						$T_j=25^\circ\text{C}$			B				

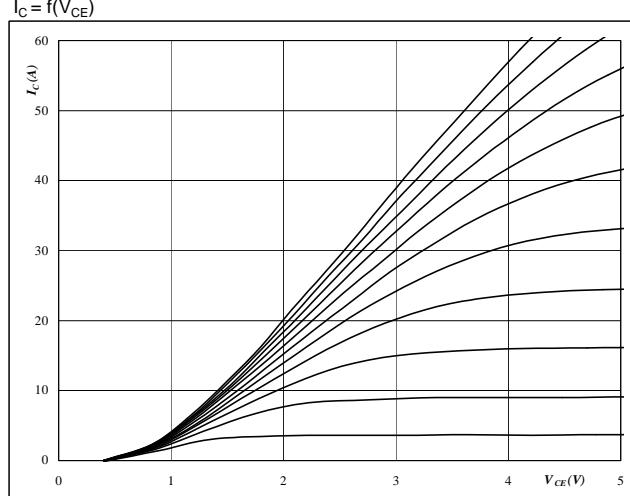
## Output Inverter

**Figure 1**  
**Typical output characteristics**  
 $I_C = f(V_{CE})$



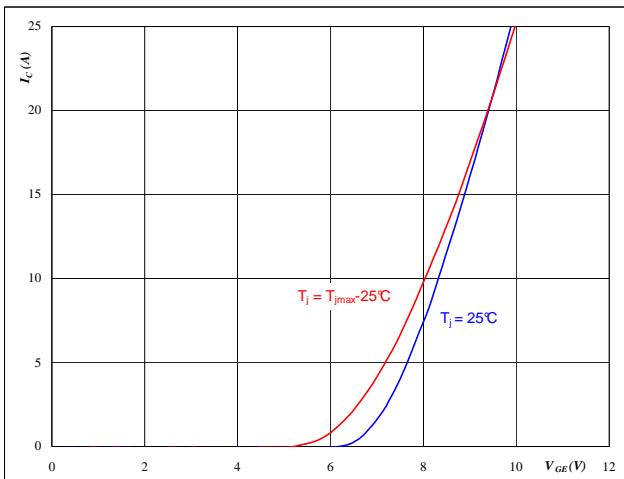
**At**  
 $t_p = 250 \mu s$   
 $T_j = 25^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 2**  
**Typical output characteristics**  
 $I_C = f(V_{CE})$



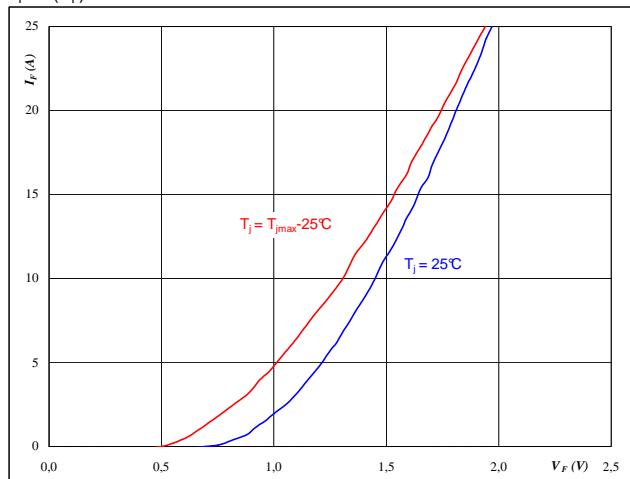
**At**  
 $t_p = 250 \mu s$   
 $T_j = 150^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 3**  
**Typical transfer characteristics**  
 $I_C = f(V_{GE})$



**At**  
 $t_p = 250 \mu s$   
 $V_{CE} = 10 V$

**Figure 4**  
**Typical diode forward current as a function of forward voltage**  
 $I_F = f(V_F)$



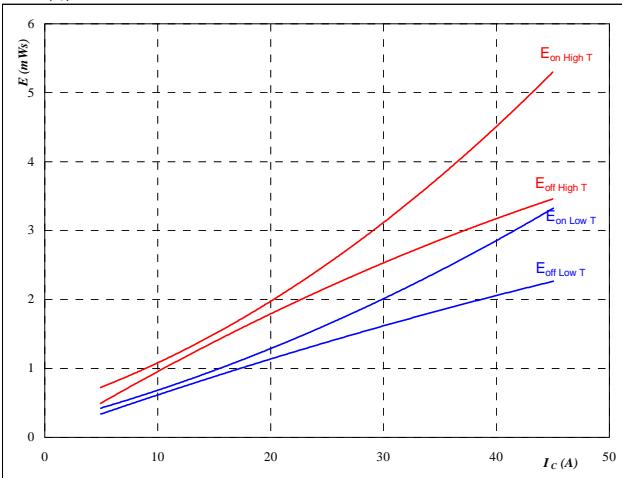
**At**  
 $t_p = 250 \mu s$

## Output Inverter

**Figure 5**

**Typical switching energy losses  
as a function of collector current**

$$E = f(I_C)$$



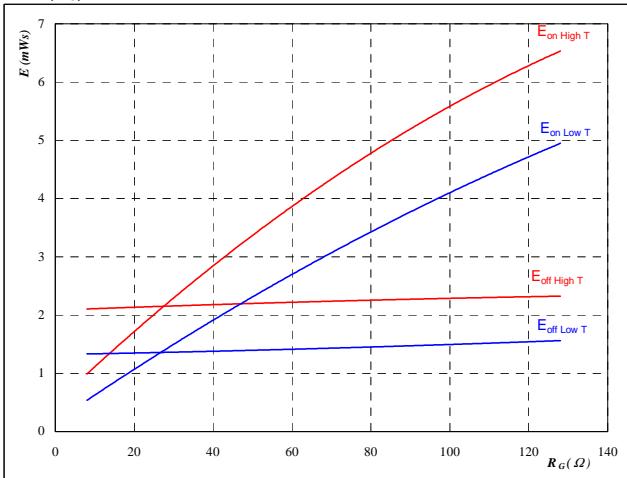
With an inductive load at

T <sub>j</sub> =	25/150	°C
V <sub>CE</sub> =	600	V
V <sub>GE</sub> =	±15	V
R <sub>gon</sub> =	32	Ω
R <sub>goff</sub> =	32	Ω

**Figure 6**

**Typical switching energy losses  
as a function of gate resistor**

$$E = f(R_G)$$



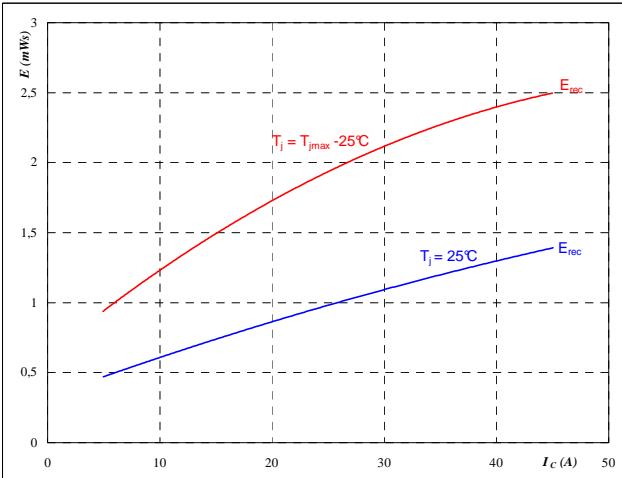
With an inductive load at

T <sub>j</sub> =	25/150	°C
V <sub>CE</sub> =	600	V
V <sub>GE</sub> =	±15	V
I <sub>C</sub> =	25	A

**Figure 7**
**Output inverter FWD**

**Typical reverse recovery energy loss  
as a function of collector current**

$$E_{rec} = f(I_C)$$



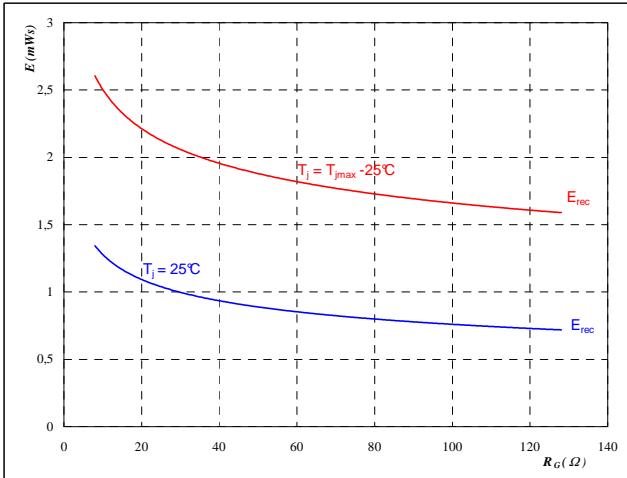
With an inductive load at

T <sub>j</sub> =	25/150	°C
V <sub>CE</sub> =	600	V
V <sub>GE</sub> =	±15	V
R <sub>gon</sub> =	32	Ω

**Figure 8**
**Output inverter FWD**

**Typical reverse recovery energy loss  
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



With an inductive load at

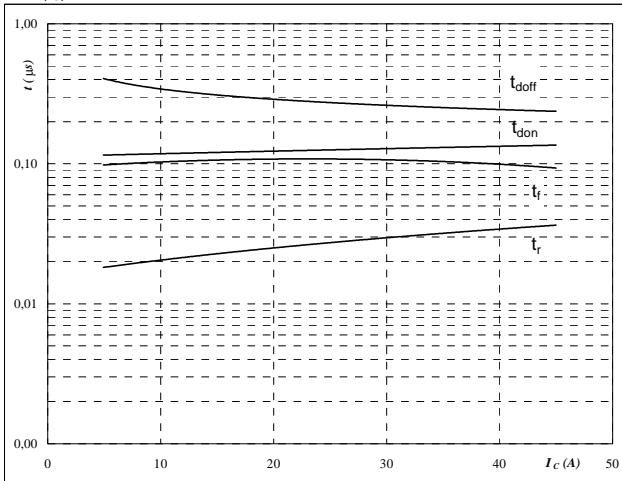
T <sub>j</sub> =	25/150	°C
V <sub>CE</sub> =	600	V
V <sub>GE</sub> =	±15	V
I <sub>C</sub> =	25	A

## Output Inverter

**Figure 9**

**Typical switching times as a function of collector current**

$$t = f(I_C)$$



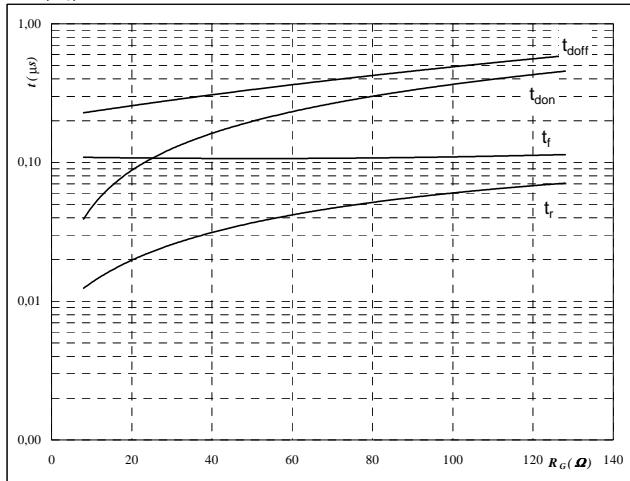
With an inductive load at

T <sub>j</sub> =	150	°C
V <sub>CE</sub> =	600	V
V <sub>GE</sub> =	±15	V
R <sub>gon</sub> =	32	Ω
R <sub>goff</sub> =	32	Ω

**Figure 10**

**Typical switching times as a function of gate resistor**

$$t = f(R_G)$$



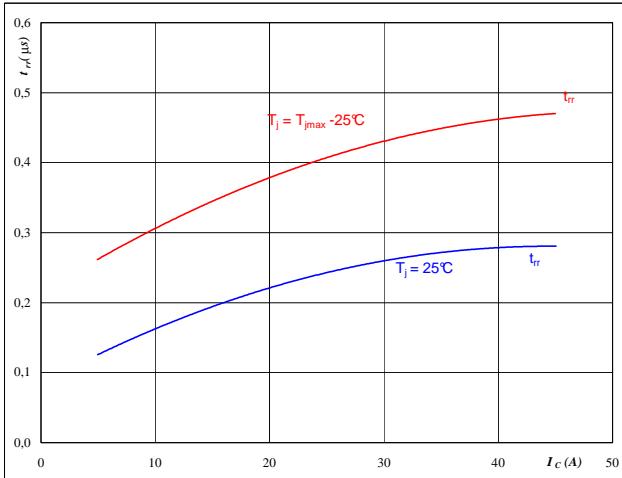
With an inductive load at

T <sub>j</sub> =	150	°C
V <sub>CE</sub> =	600	V
V <sub>GE</sub> =	±15	V
I <sub>C</sub> =	25	A

**Figure 11**
**Output inverter FWD**

**Typical reverse recovery time as a function of collector current**

$$t_{rr} = f(I_C)$$



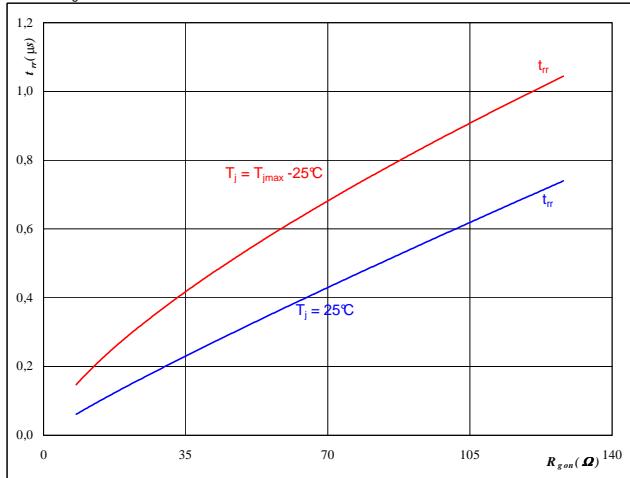
At

T <sub>j</sub> =	25/150	°C
V <sub>CE</sub> =	600	V
V <sub>GE</sub> =	±15	V
R <sub>gon</sub> =	32	Ω

**Figure 12**
**Output inverter FWD**

**Typical reverse recovery time as a function of IGBT turn on gate resistor**

$$t_{rr} = f(R_{gon})$$



At

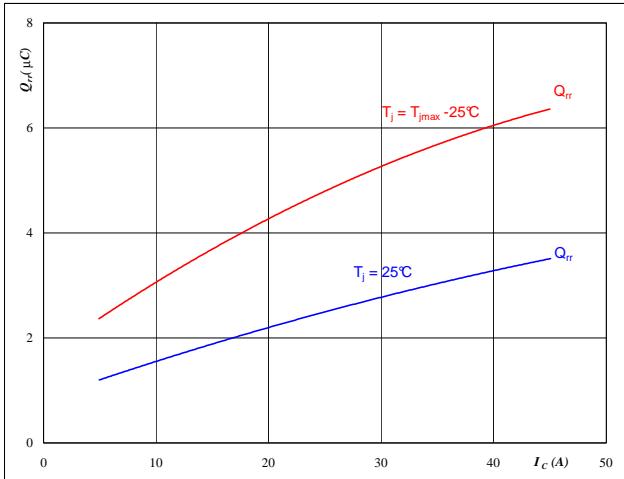
T <sub>j</sub> =	25/150	°C
V <sub>R</sub> =	600	V
I <sub>F</sub> =	25	A
V <sub>GE</sub> =	±15	V

## Output Inverter

**Figure 13**

Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_C)$$

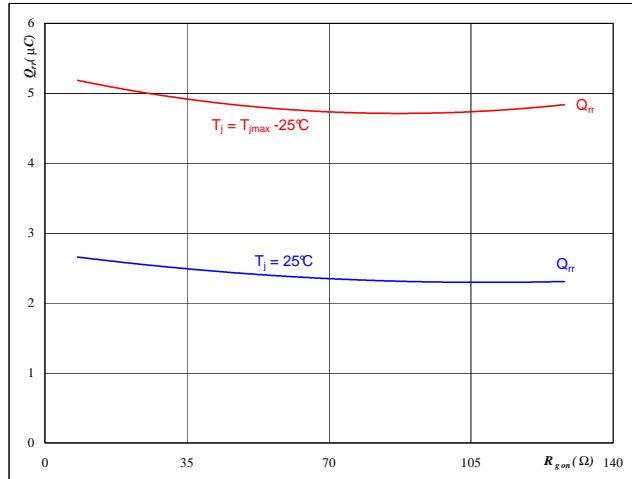

**At**

$$\begin{aligned} T_j &= \textcolor{red}{25/150} \quad {}^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ R_{gon} &= 32 \quad \Omega \end{aligned}$$

**Output inverter FWD**
**Figure 14**

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$$Q_{rr} = f(R_{gon})$$

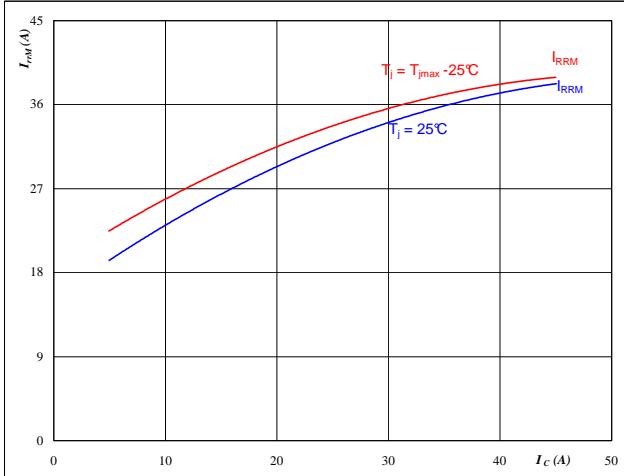

**At**

$$\begin{aligned} T_j &= \textcolor{red}{25/150} \quad {}^\circ C \\ V_R &= 600 \quad V \\ I_F &= 25 \quad A \\ V_{GE} &= \pm 15 \quad V \end{aligned}$$

**Figure 15**
**Output inverter FWD**

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_C)$$

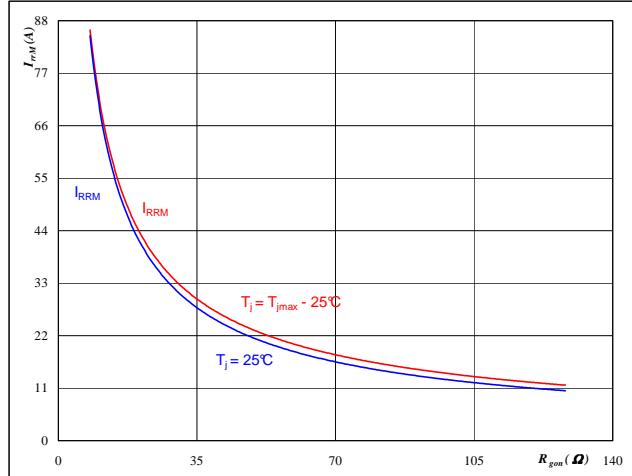

**At**

$$\begin{aligned} T_j &= \textcolor{red}{25/150} \quad {}^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ R_{gon} &= 32 \quad \Omega \end{aligned}$$

**Figure 16**
**Output inverter FWD**

Typical reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RRM} = f(R_{gon})$$

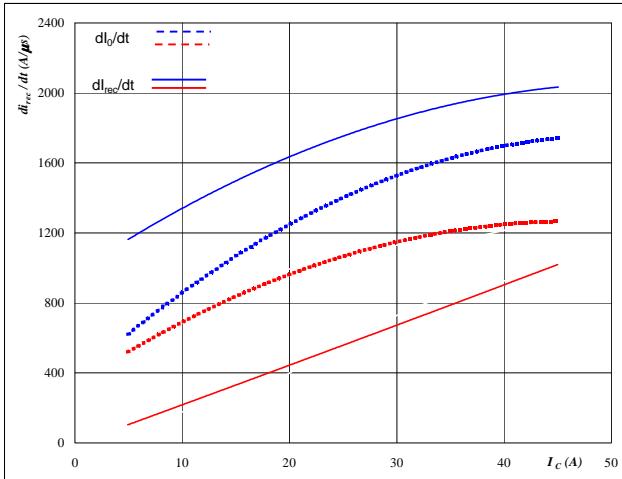

**At**

$$\begin{aligned} T_j &= \textcolor{red}{25/150} \quad {}^\circ C \\ V_R &= 600 \quad V \\ I_F &= 25 \quad A \\ V_{GE} &= \pm 15 \quad V \end{aligned}$$

## Output Inverter

**Figure 17**

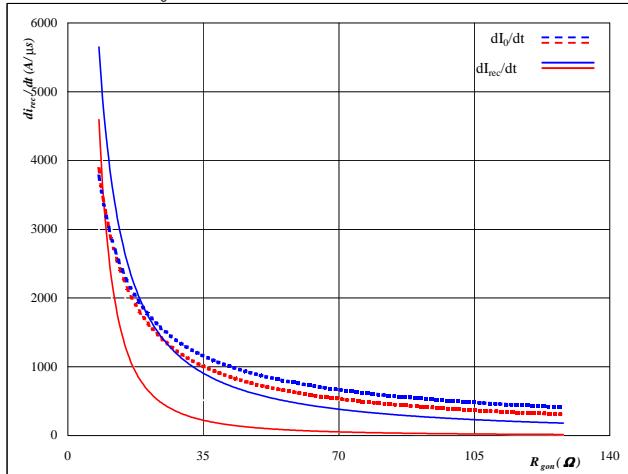
Typical rate of fall of forward  
and reverse recovery current as a  
function of collector current  
 $dI_0/dt, dI_{rec}/dt = f(I_C)$


**At**

$T_j = 25/150$  °C  
 $V_{CE} = 600$  V  
 $V_{GE} = \pm 15$  V  
 $R_{gon} = 32$  Ω

**Output inverter FWD**
**Figure 18**

Typical rate of fall of forward  
and reverse recovery current as a  
function of IGBT turn on gate resistor  
 $dI_0/dt, dI_{rec}/dt = f(R_{gon})$

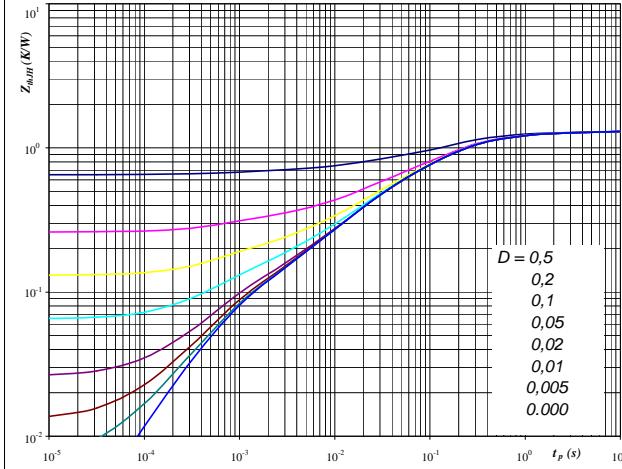

**At**

$T_j = 25/150$  °C  
 $V_R = 600$  V  
 $I_F = 25$  A  
 $V_{GE} = \pm 15$  V

**Figure 19**

IGBT transient thermal impedance  
as a function of pulse width

$$Z_{thJH} = f(t_p)$$

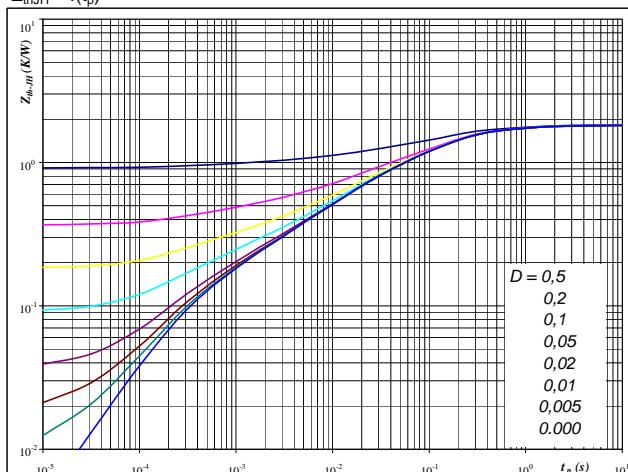

**At**

$D = t_p / T$   
 $R_{thJH} = 1.30$  K/W       $R_{thJH} = 1.11$  K/W

**Output inverter IGBT**
**Figure 20**

FWD transient thermal impedance  
as a function of pulse width

$$Z_{thJH} = f(t_p)$$


**At**

$D = t_p / T$   
 $R_{thJH} = 1.83$  K/W       $R_{thJH} = 1.55$  K/W

### IGBT thermal model values

Thermal grease		Phase change interface	
R (C/W)	Tau (s)	R (C/W)	Tau (s)
0,07	3,4E+00	0,06	3,4E+00
0,32	4,1E-01	0,27	4,1E-01
0,59	1,0E-01	0,50	1,0E-01
0,24	1,3E-02	0,20	1,3E-02
0,08	8,3E-04	0,07	8,3E-04

### FWD thermal model values

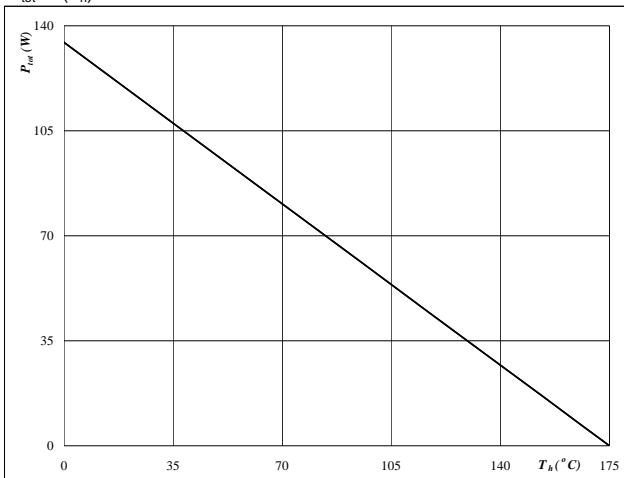
Thermal grease		Phase change interface	
R (C/W)	Tau (s)	R (C/W)	Tau (s)
0,04	9,4E+00	0,04	9,4E+00
0,25	7,1E-01	0,21	7,1E-01
0,83	1,3E-01	0,71	1,3E-01
0,44	1,9E-02	0,37	1,9E-02
0,16	2,5E-03	0,14	2,5E-03
0,10	3,1E-04	0,09	3,1E-04

## Output Inverter

**Figure 21**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$

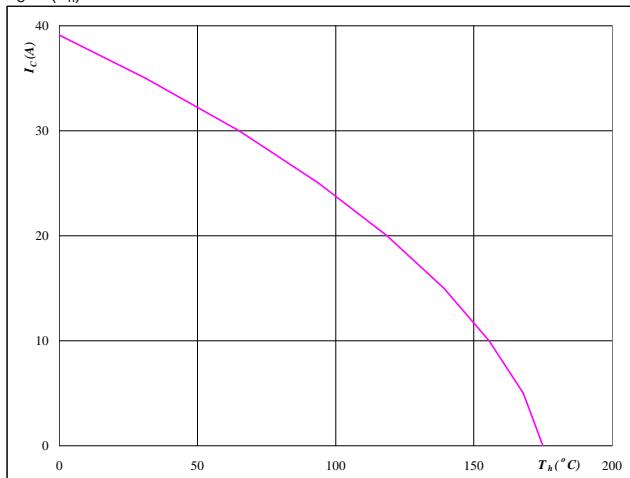

**At**

$$T_j = 175 \quad ^\circ\text{C}$$

**Output inverter IGBT**
**Figure 22**

**Collector current as a function of heatsink temperature**

$$I_C = f(T_h)$$

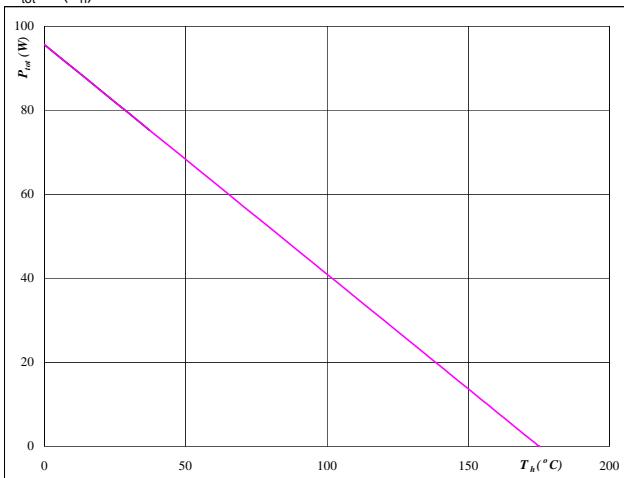

**At**

$$T_j = 175 \quad ^\circ\text{C}$$

**Output inverter IGBT**
**Figure 23**
**Output inverter FWD**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$

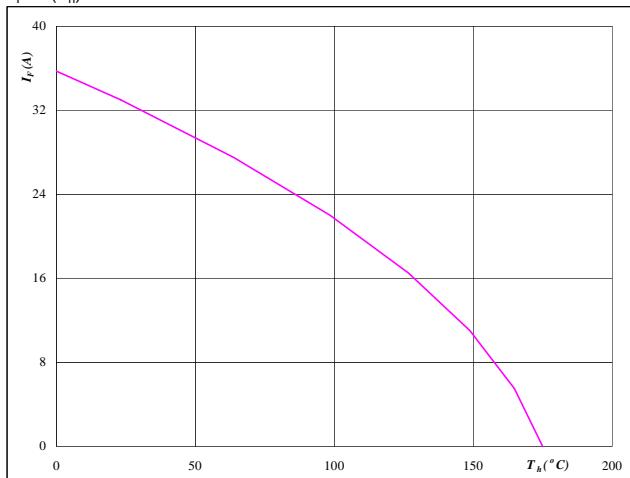

**At**

$$T_j = 175 \quad ^\circ\text{C}$$

**Figure 24**
**Output inverter FWD**

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$


**At**

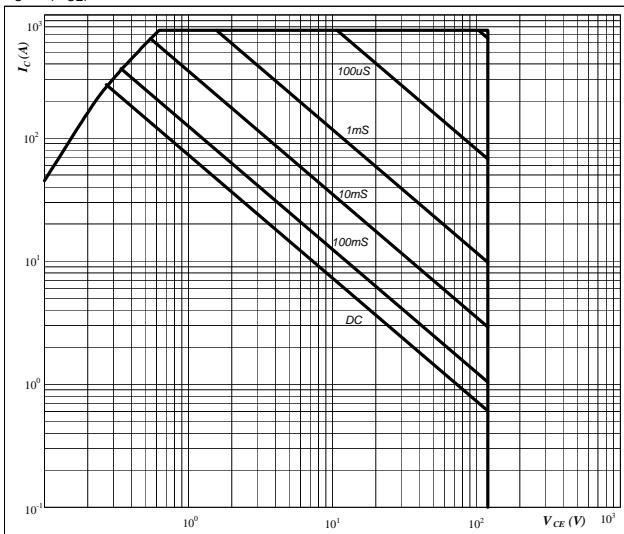
$$T_j = 175 \quad ^\circ\text{C}$$

## Output Inverter

**Figure 25**

**Safe operating area as a function of collector-emitter voltage**

$$I_C = f(V_{CE})$$


**At**

D = single pulse

T<sub>h</sub> = 80 °C

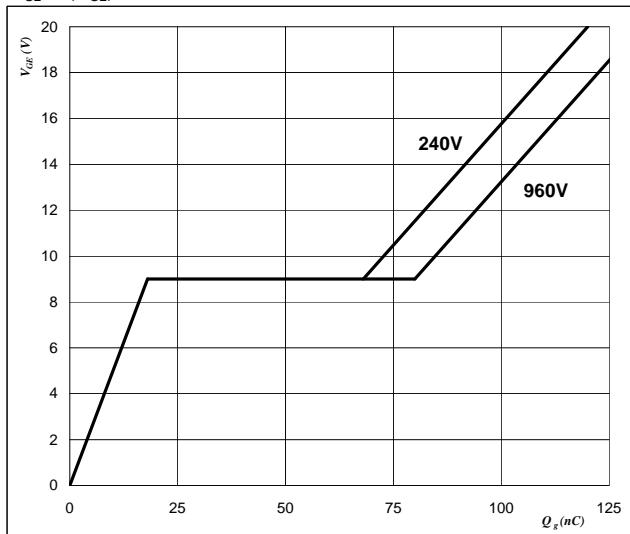
V<sub>GE</sub> = ±15 V

T<sub>j</sub> = T<sub>jmax</sub> °C

**Output inverter IGBT**
**Figure 26**

**Gate voltage vs Gate charge**

$$V_{GE} = f(Q_{GE})$$

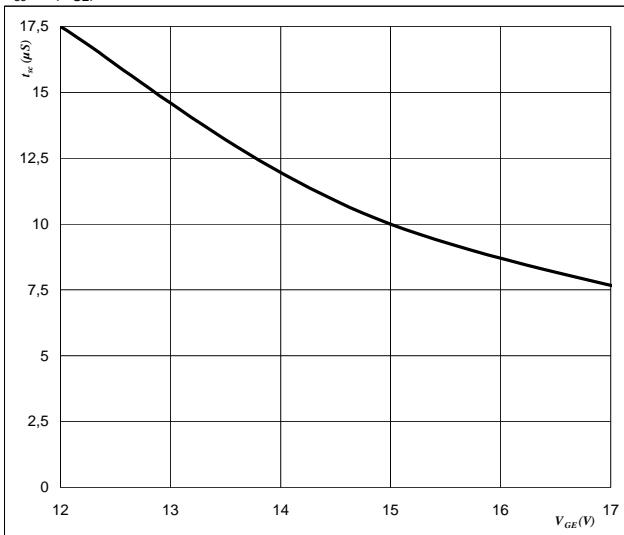

**At**

I<sub>C</sub> = 25 A

**Figure 27**

**Short circuit withstand time as a function of gate-emitter voltage**

$$t_{sc} = f(V_{GE})$$


**At**

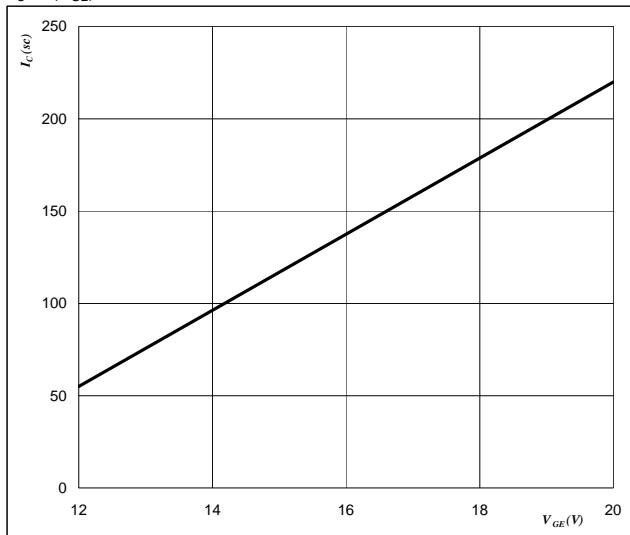
V<sub>CE</sub> = 1200 V

T<sub>j</sub> ≤ 175 °C

**Output inverter IGBT**
**Figure 28**

**Typical short circuit collector current as a function of gate-emitter voltage**

$$I_C = f(V_{GE})$$

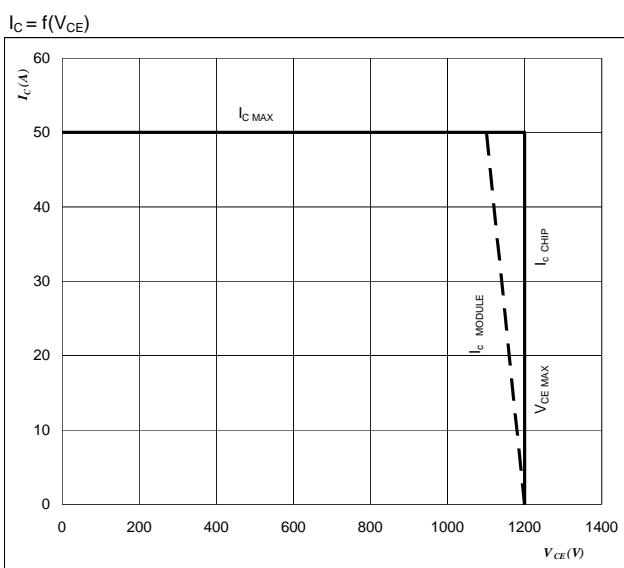

**At**

V<sub>CE</sub> ≤ 1200 V

T<sub>j</sub> = 175 °C

**Figure 29**  
**Reverse bias safe operating area**

IGBT



**At**

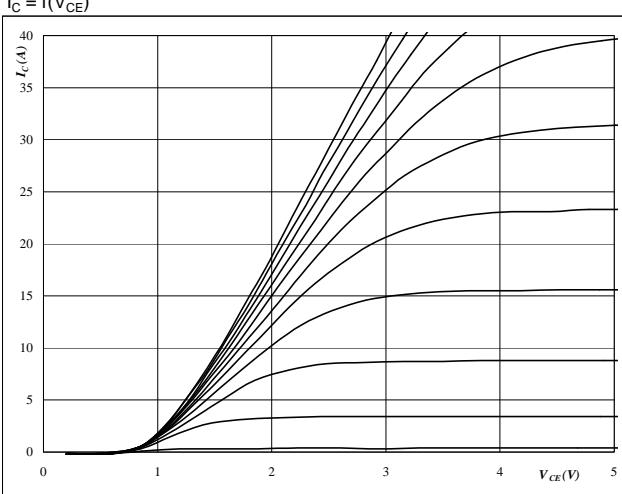
$T_j = T_{jmax} - 25 \text{ } ^\circ\text{C}$

$U_{ccminus} = U_{ccplus}$

Switching mode : 3 level switching

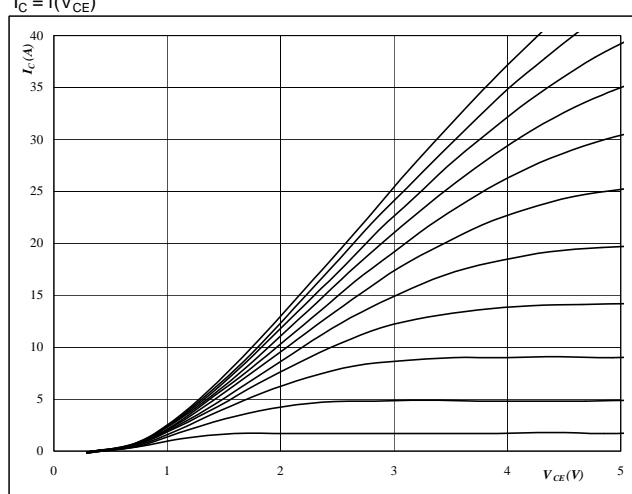
## Brake

**Figure 1**  
**Typical output characteristics**  
 $I_C = f(V_{CE})$



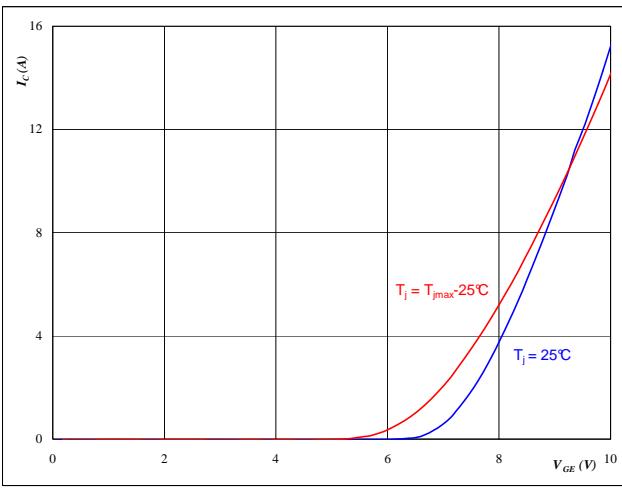
**At**  
 $t_p = 250 \mu s$   
 $T_j = 25^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 2**  
**Typical output characteristics**  
 $I_C = f(V_{CE})$



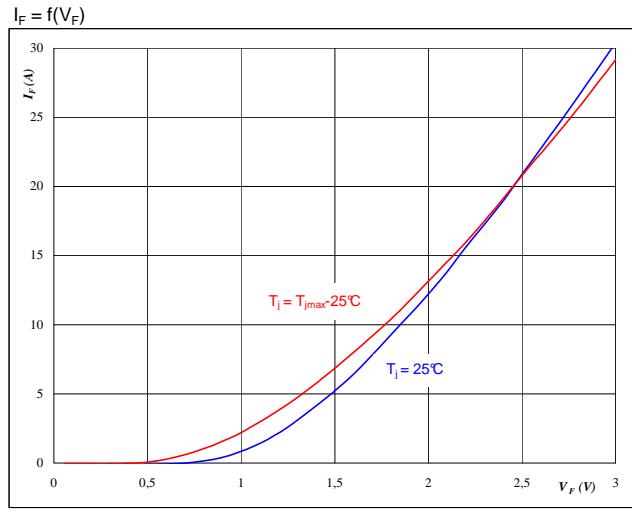
**At**  
 $t_p = 250 \mu s$   
 $T_j = 150^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 3**  
**Typical transfer characteristics**  
 $I_C = f(V_{GE})$



**At**  
 $t_p = 250 \mu s$   
 $V_{CE} = 10 V$

**Figure 4**  
**Typical diode forward current as a function of forward voltage**  
 $I_F = f(V_F)$



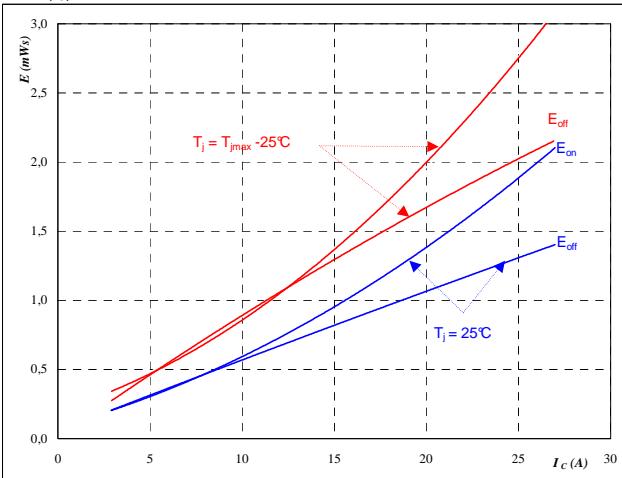
**At**  
 $t_p = 250 \mu s$

## Brake

**Figure 5**

**Typical switching energy losses  
as a function of collector current**

$$E = f(I_C)$$



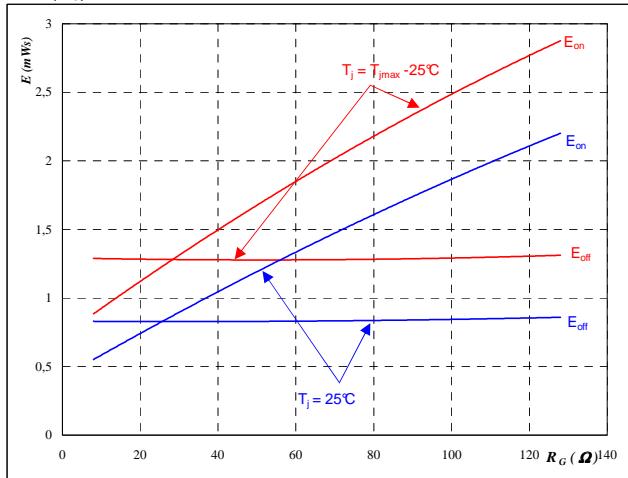
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 32 \quad \Omega \\ R_{goff} &= 32 \quad \Omega \end{aligned}$$

**Brake IGBT**
**Figure 6**

**Typical switching energy losses  
as a function of gate resistor**

$$E = f(R_G)$$



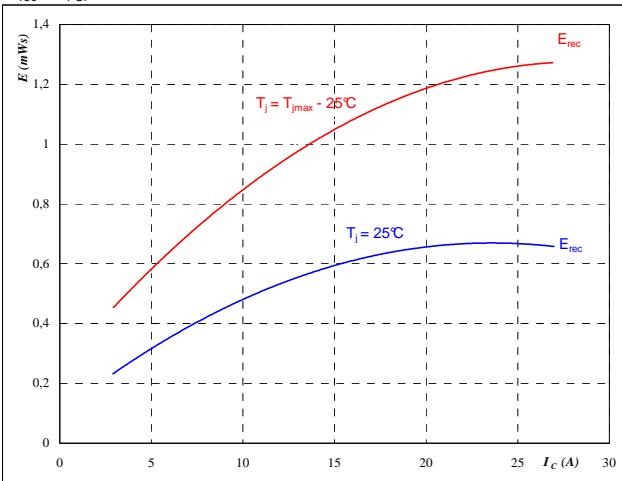
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 15 \quad \text{A} \end{aligned}$$

**Figure 7**

**Typical reverse recovery energy loss  
as a function of collector current**

$$E_{rec} = f(I_C)$$



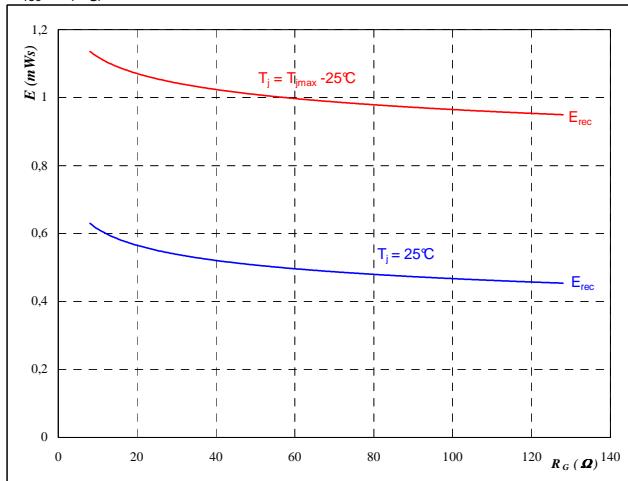
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 32 \quad \Omega \end{aligned}$$

**Brake FWD**
**Figure 8**

**Typical reverse recovery energy loss  
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



With an inductive load at

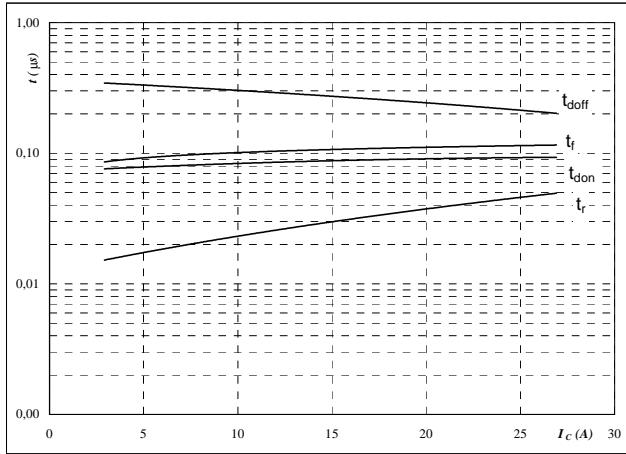
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 15 \quad \text{A} \end{aligned}$$

## Brake

**Figure 9**

Brake IGBT

**Typical switching times as a function of collector current**  
 $t = f(I_C)$



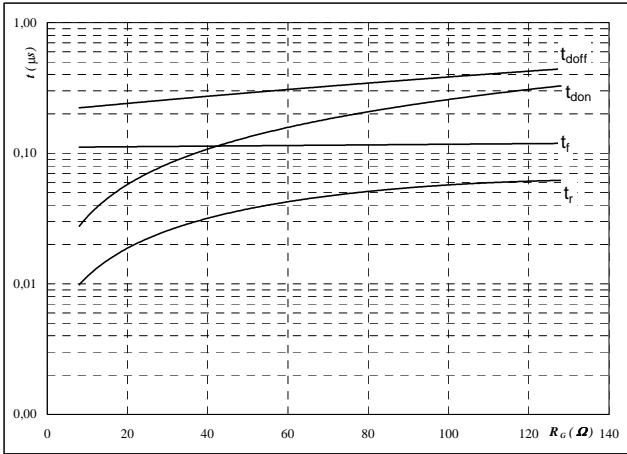
With an inductive load at

$T_j = 25/150 \text{ } ^\circ\text{C}$   
 $V_{CE} = 600 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{gon} = 32 \Omega$   
 $R_{goff} = 32 \Omega$

**Figure 10**

Brake IGBT

**Typical switching times as a function of gate resistor**  
 $t = f(R_G)$



With an inductive load at

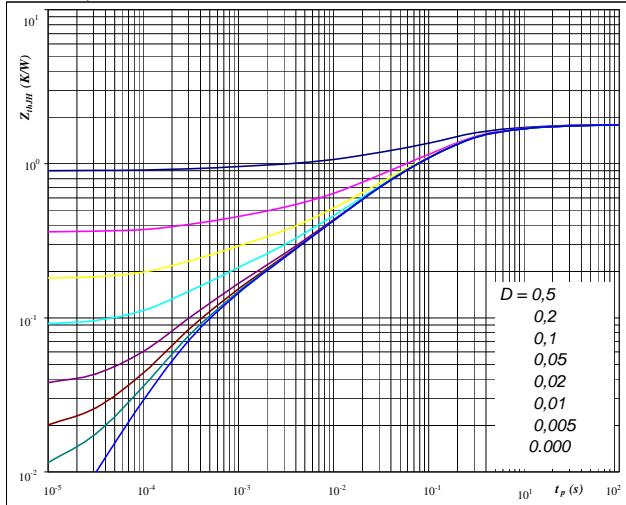
$T_j = 25/150 \text{ } ^\circ\text{C}$   
 $V_{CE} = 600 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $I_C = 15 \text{ A}$

**Figure 11**

Brake IGBT

**IGBT transient thermal impedance as a function of pulse width**

$Z_{thJH} = f(t_p)$



**At** Thermal grease **D =** tp / T

R<sub>thJH</sub> = 1,80 K/W Phase change interface

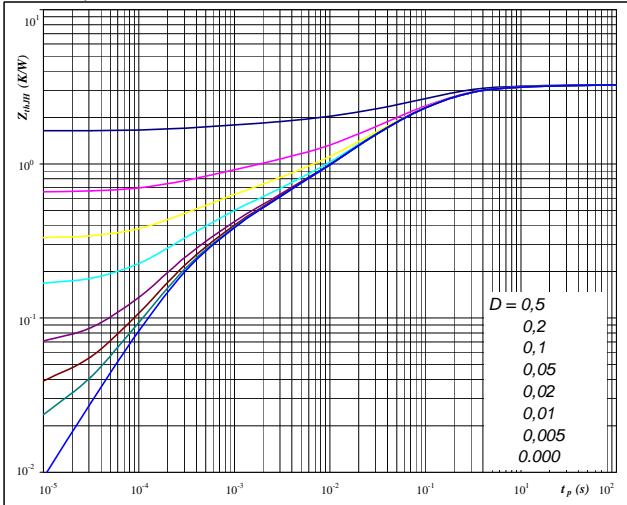
R<sub>thJH</sub> = 1,53 K/W

**Figure 12**

Brake FWD

**FWD transient thermal impedance as a function of pulse width**

$Z_{thJH} = f(t_p)$



**At** Thermal grease **D =** tp / T

R<sub>thJH</sub> = 3,28 K/W Phase change interface

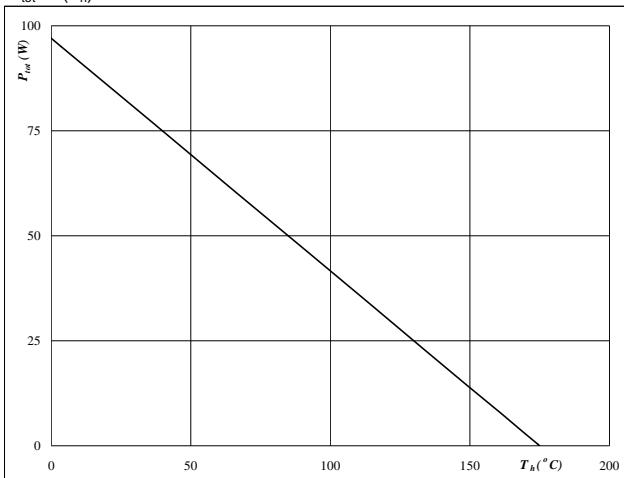
R<sub>thJH</sub> = 2,78 K/W

## Brake

**Figure 13**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$

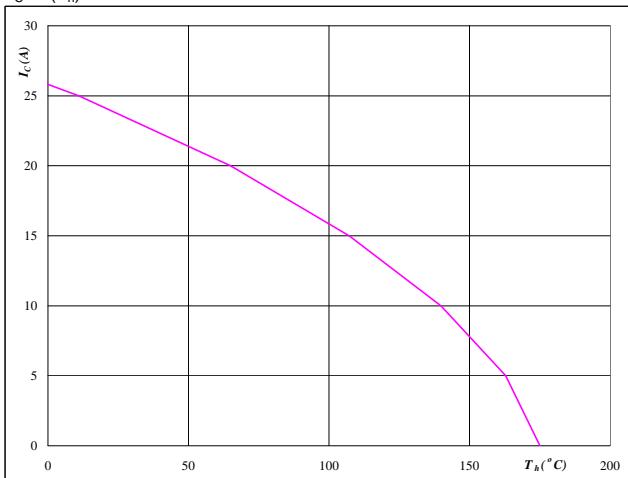

**At**

$$T_j = 175 \quad {}^\circ\text{C}$$

**Brake IGBT**
**Figure 14**

**Collector current as a function of heatsink temperature**

$$I_C = f(T_h)$$


**At**

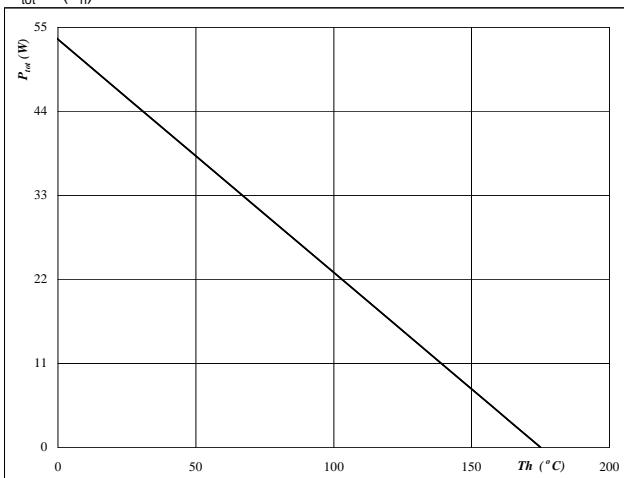
$$T_j = 175 \quad {}^\circ\text{C}$$

$$V_{GE} = 15 \quad \text{V}$$

**Figure 15**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$

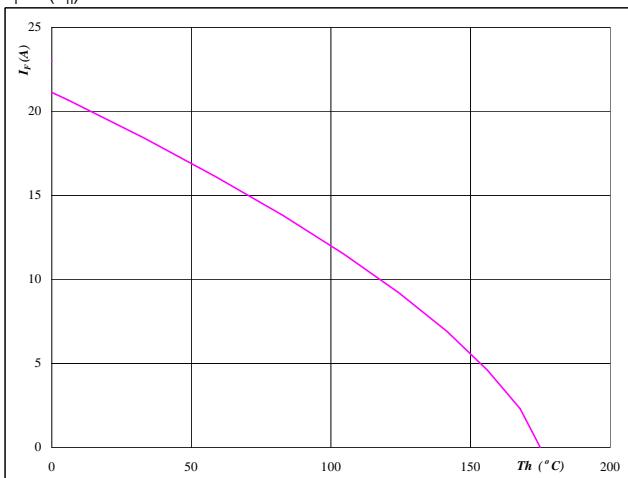

**At**

$$T_j = 175 \quad {}^\circ\text{C}$$

**Brake FWD**
**Figure 16**

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$


**At**

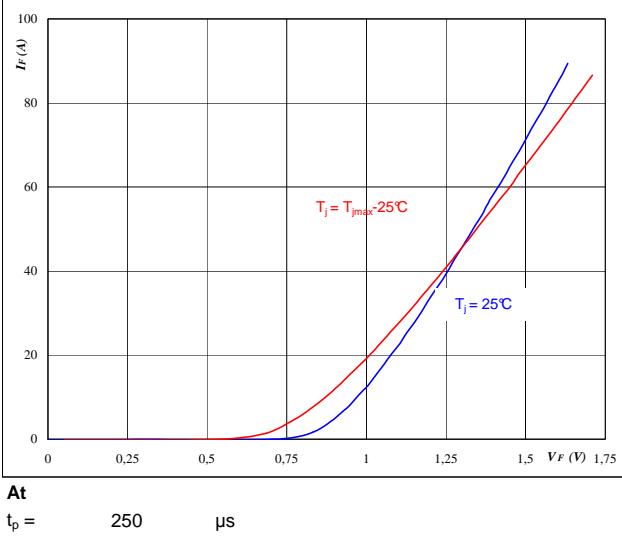
$$T_j = 175 \quad {}^\circ\text{C}$$

## Input Rectifier Bridge

**Figure 1**

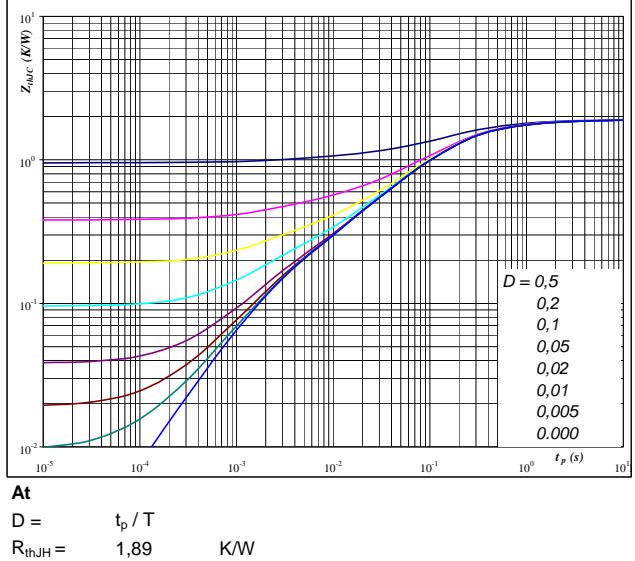
**Typical diode forward current as a function of forward voltage**

$$I_F = f(V_F)$$


**Rectifier diode**
**Figure 2**

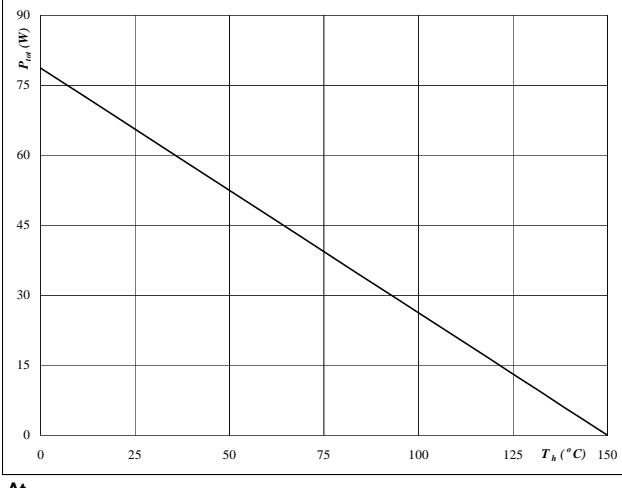
**Diode transient thermal impedance as a function of pulse width**

$$Z_{thJH} = f(t_p)$$


**Rectifier diode**
**Figure 3**

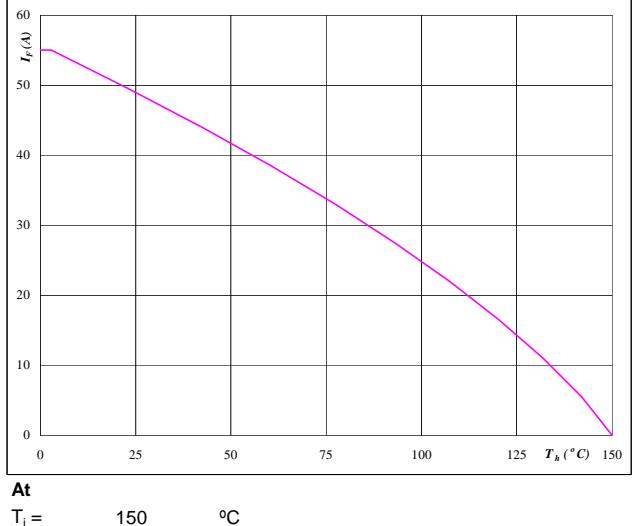
**Power dissipation as a function of heatsink temperature**

$$P_{tot} = f(T_h)$$


**Rectifier diode**
**Figure 4**

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$


**Rectifier diode**

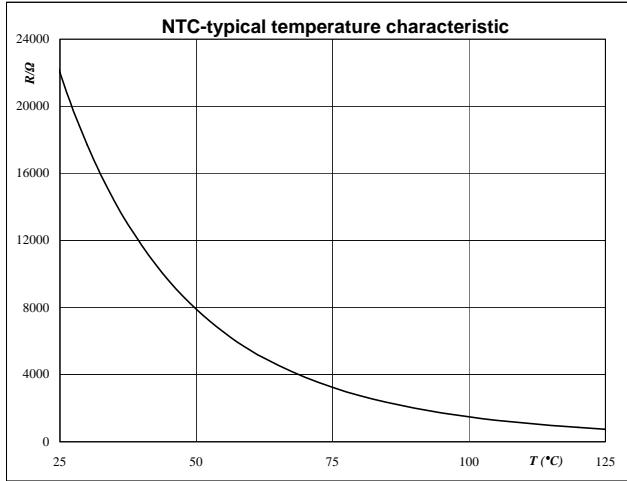
## Thermistor

**Figure 1**

Thermistor

**Typical NTC characteristic  
as a function of temperature**

$$R_T = f(T)$$


**Figure 2**

Thermistor

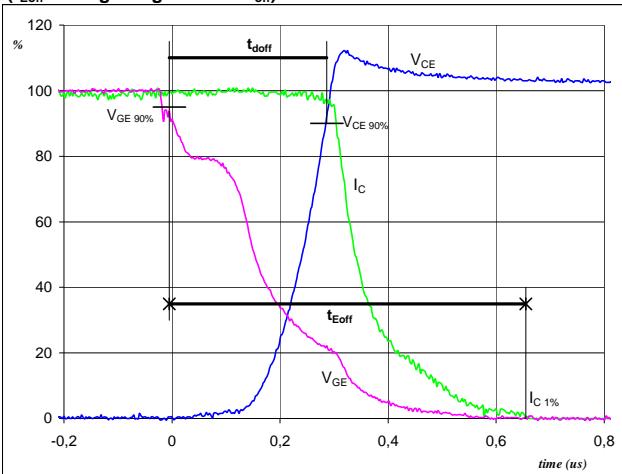
**Typical NTC resistance values**

$$R(T) = R_{25} \cdot e^{\left( B_{25/100} \left( \frac{1}{T} - \frac{1}{T_{25}} \right) \right)} \quad [\Omega]$$

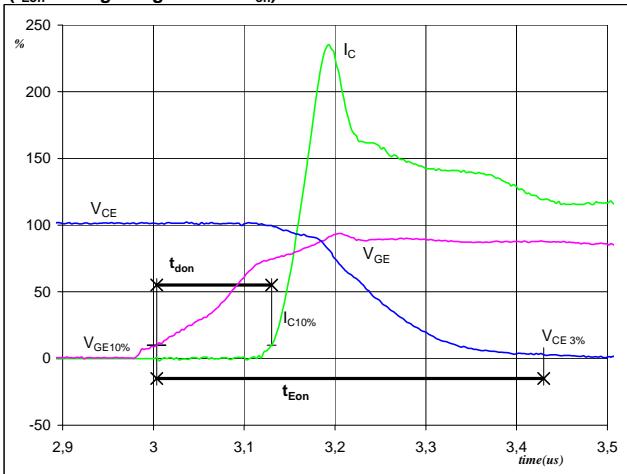
## Switching Definitions Output Inverter

**General conditions**

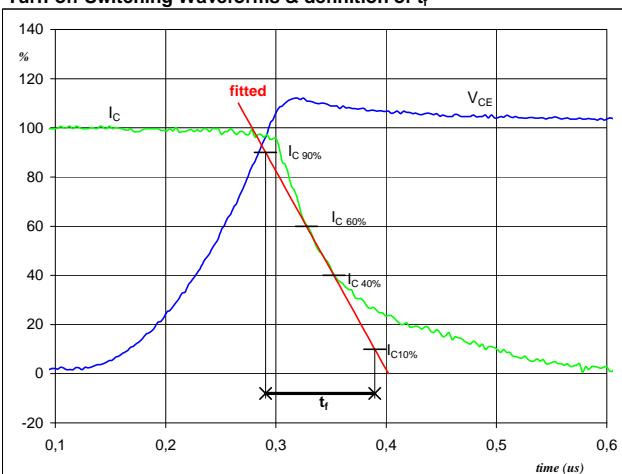
$T_j$	=	150 °C
$R_{gon}$	=	32 Ω
$R_{goff}$	=	32 Ω

**Figure 1**
**Output inverter IGBT**
**Turn-off Switching Waveforms & definition of  $t_{doff}$ ,  $t_{Eoff}$**   
 $(t_{Eoff} = \text{integrating time for } E_{off})$ 


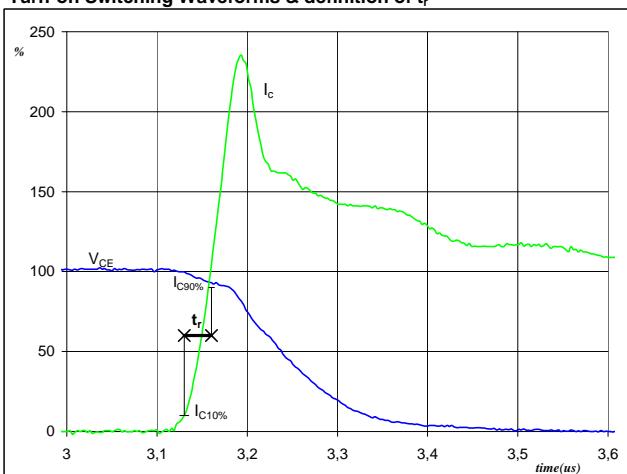
$V_{GE}(0\%) = -15$  V  
 $V_{GE}(100\%) = 15$  V  
 $V_C(100\%) = 600$  V  
 $I_C(100\%) = 25$  A  
 $t_{doff} = 0,28$  μs  
 $t_{Eoff} = 0,66$  μs

**Figure 2**
**Output inverter IGBT**
**Turn-on Switching Waveforms & definition of  $t_{don}$ ,  $t_{Eon}$**   
 $(t_{Eon} = \text{integrating time for } E_{on})$ 


$V_{GE}(0\%) = -15$  V  
 $V_{GE}(100\%) = 15$  V  
 $V_C(100\%) = 600$  V  
 $I_C(100\%) = 25$  A  
 $t_{don} = 0,13$  μs  
 $t_{Eon} = 0,43$  μs

**Figure 3**
**Output inverter IGBT**
**Turn-off Switching Waveforms & definition of  $t_f$** 


$V_C(100\%) = 600$  V  
 $I_C(100\%) = 25$  A  
 $t_f = 0,10$  μs

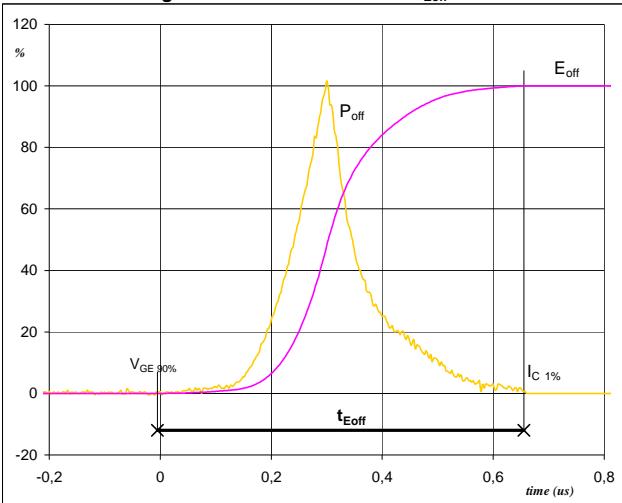
**Figure 4**
**Output inverter IGBT**
**Turn-on Switching Waveforms & definition of  $t_r$** 


$V_C(100\%) = 600$  V  
 $I_C(100\%) = 25$  A  
 $t_r = 0,03$  μs

## Switching Definitions Output Inverter

**Figure 5**

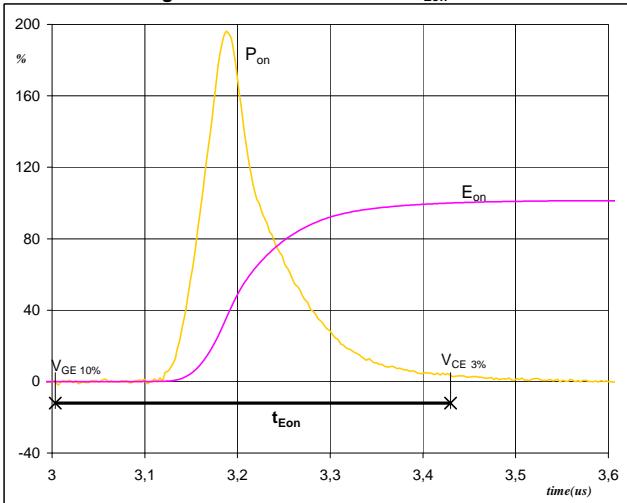
Output inverter IGBT

**Turn-off Switching Waveforms & definition of  $t_{Eoff}$** 


$P_{off} (100\%) = 15,01 \text{ kW}$   
 $E_{off} (100\%) = 2,17 \text{ mJ}$   
 $t_{Eoff} = 0,66 \mu\text{s}$

**Figure 6**

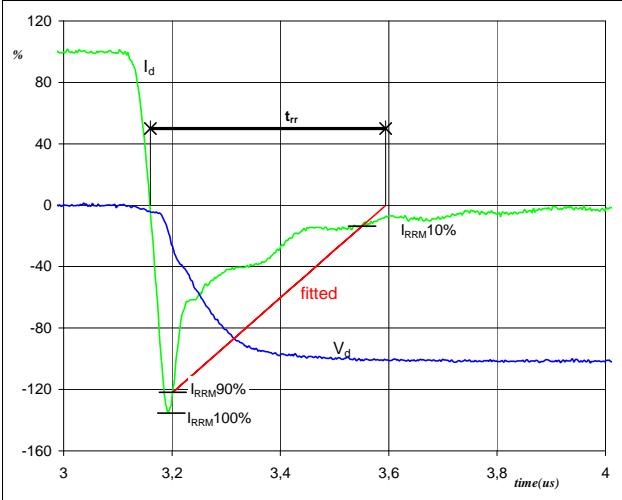
Output inverter IGBT

**Turn-on Switching Waveforms & definition of  $t_{Eon}$** 


$P_{on} (100\%) = 15,01 \text{ kW}$   
 $E_{on} (100\%) = 2,53 \text{ mJ}$   
 $t_{Eon} = 0,43 \mu\text{s}$

**Figure 7**

Output inverter IGBT

**Turn-off Switching Waveforms & definition of  $t_{rr}$** 


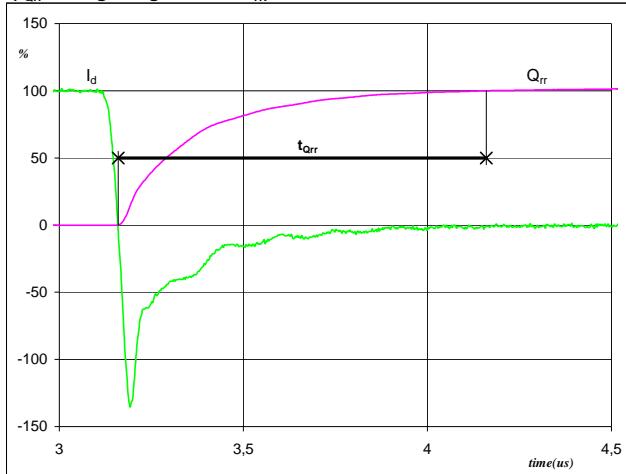
$V_d (100\%) = 600 \text{ V}$   
 $I_d (100\%) = 25 \text{ A}$   
 $I_{RRM} (100\%) = 10 \text{ A}$   
 $t_{rr} = 0,10 \mu\text{s}$

## Switching Definitions Output Inverter

**Figure 8**

Output inverter FWD

**Turn-on Switching Waveforms & definition of  $t_{Qrr}$**   
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$

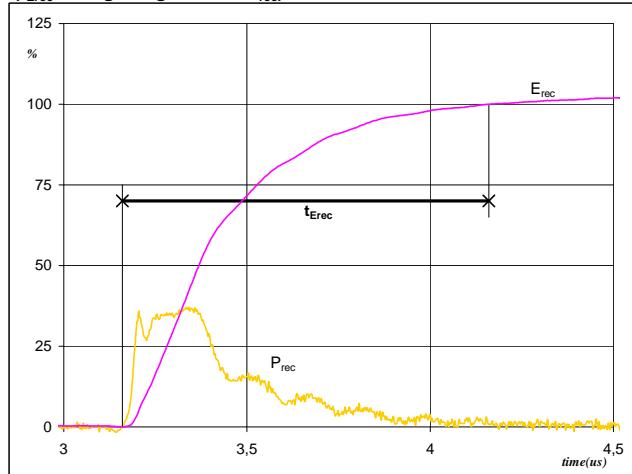


$I_d(100\%) = 25 \text{ A}$   
 $Q_{rr}(100\%) = 4,81 \mu\text{C}$   
 $t_{Qrr} = 1,00 \mu\text{s}$

**Figure 9**

Output inverter FWD

**Turn-on Switching Waveforms & definition of  $t_{Erec}$**   
 $(t_{Erec} = \text{integrating time for } E_{rec})$



$P_{rec}(100\%) = 15,01 \text{ kW}$   
 $E_{rec}(100\%) = 1,94 \text{ mJ}$   
 $t_{Erec} = 1,00 \mu\text{s}$

### Ordering Code and Marking - Outline - Pinout

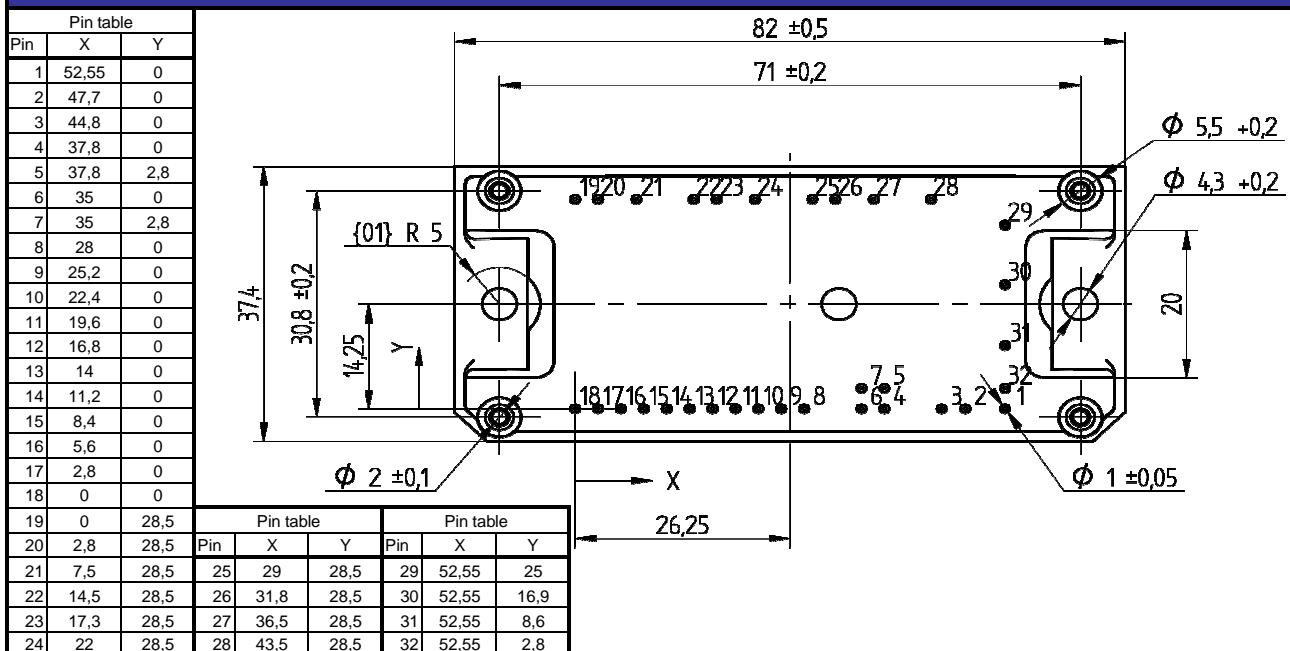
#### Ordering Code & Marking

Version	Ordering Code	in DataMatrix as	in packaging barcode as
17mm housing with solder pins and break	V23990-P589-A41-PM	P589-A41-PM	P589-A41-PM
17mm housing with pressfit pins and break	V23990-P589-A41Y-PM	P589-A41Y-PM	P589-A41Y-PM
12mm housing with solder pins and break	V23990-P589-A418-PM	P589-A418-PM	P589-A418-PM
17mm housing with solder pins w/o break	V23990-P589-C41-PM	P589-C41-PM	P589-C41-PM

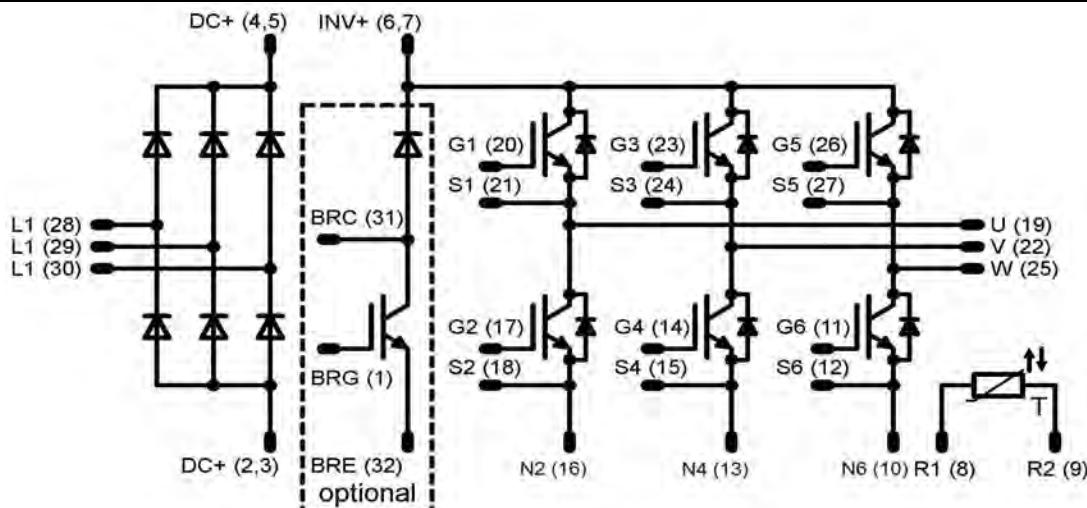
#### Features

	A version	C version
Rectifier	3-leg	3-leg
Break IGBT	✓	w/o pin 1,31,32
Break FWD	✓	
Inverter IGBT	✓	✓
Inverter FWD	✓	✓

#### Outline



#### Pinout



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1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in labelling can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.