

P-Channel 30-V (D-S) MOSFET

V _{DS}	-30	V	
RDS(on),typ	VGS=10V	9	mΩ
R _D S(on),typ	VGS=4.5V	17	mΩ
ID		-30	Α

DFN 3x3 EP

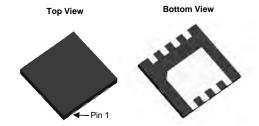
FEATURES

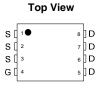
- TrenchFET® power MOSFET
- 100 % R_g and UIS tested

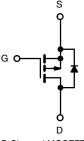
ROHS COMPLIANT HALOGEN FREE

APPLICATIONS

- Notebook battery charging
- · Notebook adapter switch







P-0	Char	വല	MC	SF	FT

ABSOLUTE MAXIMUM RATINGS (TA =	25 °C, unless other	wise noted)		
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	V _{DS}	-30	V	
Gate-Source Voltage		V_{GS}	± 25	v
	T _C = 25 °C		-30 ^d	
Continuous Drain Current (T, = 150 °C)	T _C = 70 °C		-25 ^d	
Continuous Drain Current (1) = 130 °C)	T _A = 25 °C	I _D	-13.9 ^{a, b}	
	T _A = 70 °C		-11.1 ^{a, b}	A
Pulsed Drain Current	I _{DM}	-120	^	
Continuous Source-Drain Diode Current	T _C = 25 °C		-35 ^d	
Continuous Source-Drain Diode Current	T _A = 25 °C	ls -	-3 a, b	
Avalanche Current		I _{AS}	-29	
Single-Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	42	mJ
	T _C = 25 °C		52	
Maximum Power Dissipation	T _C = 70 °C		33	w
Maximum Fower Dissipation	T _A = 25 °C	P _D	3.7 ^{a, b}	VV
	T _A = 70 °C		2.4 ^{a, b}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C	
Soldering Recommendations (Peak Temperature) e, f		260		

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum Junction-to-Ambient a, c	t ≤ 10 s	R _{thJA}	26	33	°C/W	
Maximum Junction-to-Case	Steady State	R _{thJC}	1.9	2.4	0/00	

Notes

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. Maximum under steady state conditions is 81 °C/W.
- d. Package limited.
- e. The DFN 3 x 3 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- f. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.
- g. Based on T_C = 25 °C.



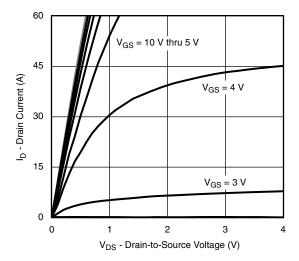
PARAMETER	SYMBOL TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static				•	•		
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-30	-	-	V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 050 A	-	-25	-	1//00	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$	I _D = -250 μA	-	4.7	-	mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-1.2	-	-2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 25 \text{ V}$	-	-	± 100	nA	
Zana Cata Valtana Busin Commant	,	V _{DS} = -30 V, V _{GS} = 0 V		-1	1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -30 V, V _{GS} = 0 V, T _J = 55 °C	-	-	-5	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge -10 \text{ V}, V_{GS} = -10 \text{ V}$	-30	-	-	Α	
Drain Course On State Resistance 3	D	V _{GS} = -10 V, I _D = -13.9 A	-	9	-	mΩ	
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = -4.5 \text{ V}, I_D = -10.3 \text{ A}$	-	17	-		
Forward Transconductance ^a	9 _{fs}	V _{DS} = -15 V, I _D = -13.9 A	-	35	-	S	
Dynamic ^b							
Input Capacitance	C _{iss}		-	1800	-	pF	
Output Capacitance	C _{oss}	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	370	-		
Reverse Transfer Capacitance	C _{rss}		-	312	-		
Falal Cala Charac	_	V _{DS} = -15 V, V _{GS} = -10 V, I _D = -13.9 A	-	-	15	j	
Total Gate Charge	Q_g		-	-	13	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -13.9 \text{ A}$	-	-	6		
Gate-Drain Charge	Q_{gd}		-	-	11		
Gate Resistance	R _g	f = 1 MHz	0.4	2	4	Ω	
Turn-On Delay Time	t _{d(on)}		-	11	22		
Rise Time	t _r	$V_{DD} = -15 \text{ V}, R_L = 1.35 \Omega$	-	9	18		
Turn-Off DelayTime	t _{d(off)}	$I_D \cong -11.1 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$	-	32	50		
Fall Time	t _f		-	9	18	ne	
Turn-On Delay Time	t _{d(on)}		-	40	60	ns	
Rise Time	t _r	V_{DD} = -15 V, R_L = 1.35 Ω	-	43	65		
Turn-Off DelayTime	t _{d(off)}	$I_D \cong$ -11.1 A, $V_{GEN} =$ -4.5 V, $R_g =$ 1 Ω	-	30	45		
Fall Time	t _f		-	11	22		
Drain-Source Body Diode Characteris	stics						
Continuous Source-Drain Diode Current	Is	T _C = 25 °C	-	-	-35	А	
Pulse Diode Forward Current	I _{SM}		-	-	-60		
Body Diode Voltage	V _{SD}	I _S = -11.1 A, V _{GS} = 0 V	-	-0.8	-1.2	V	
Body Diode Reverse Recovery Time	t _{rr}		-	33	50	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	l _F = -11.1 A, dl/dt = 100 A/μs,	-	30	45	nC	
Reverse Recovery Fall Time	t _a	T _J = 25 °C	-	18	-		
Reverse Recovery Rise Time	t _b	1	_	16	-	ns	

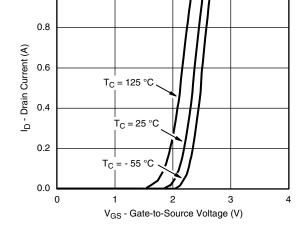
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



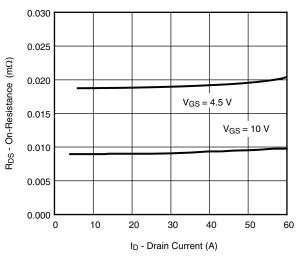


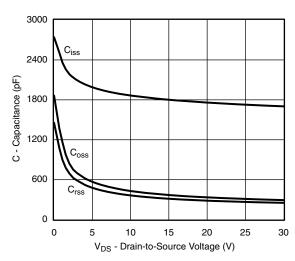


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Output Characteristics

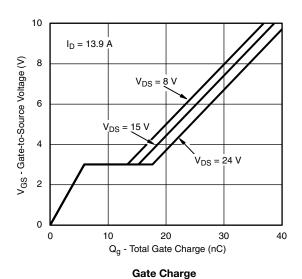


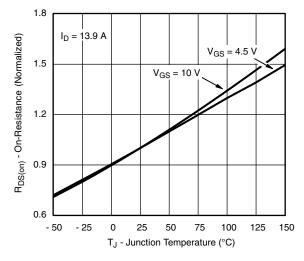




On-Resistance vs. Drain Current

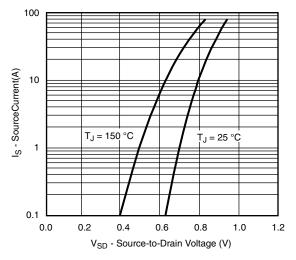
Capacitance

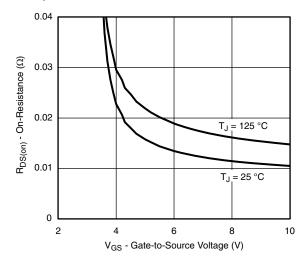




On-Resistance vs. Junction Temperature

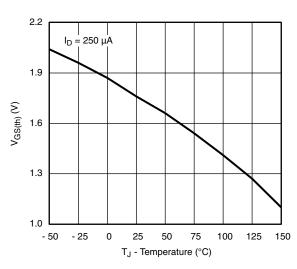


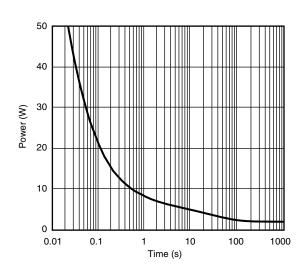




Source-Drain Diode Forward Voltage

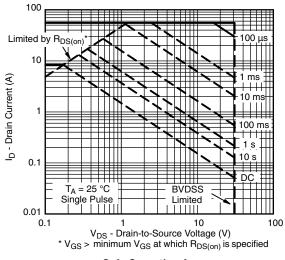
On-Resistance vs. Gate-to-Source Voltage





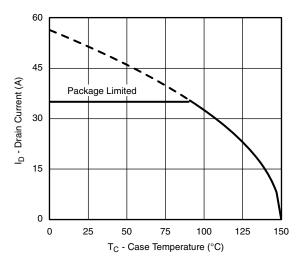
Threshold Voltage

Single Pulse Power, Junction-to-Ambient

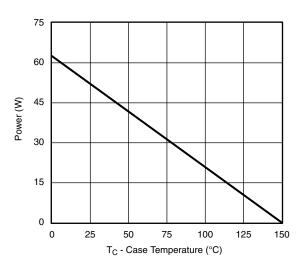


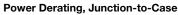
Safe Operating Area

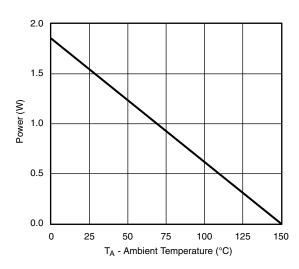




Current Derating*



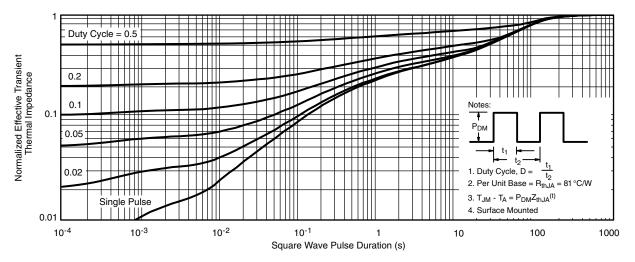




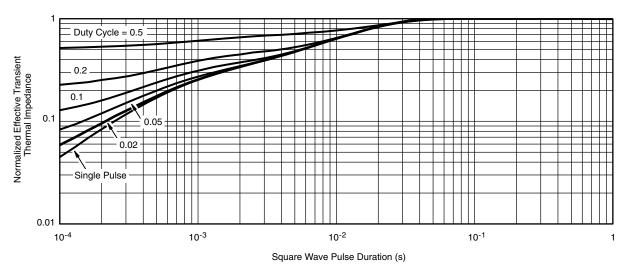
Power Derating, Junction-to-Ambient

^{*} The power dissipation P_D is based on T_J (max.) = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





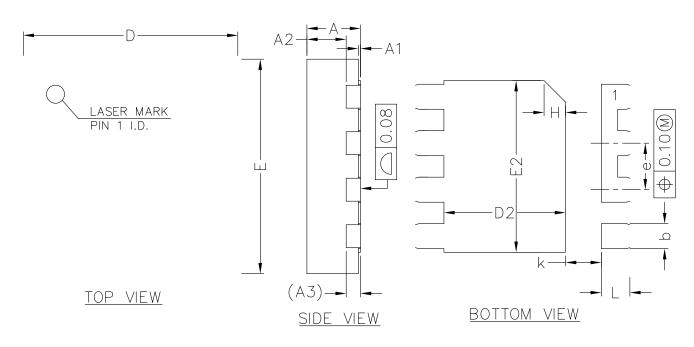
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case



DFN3x3 PACKAGE OUTLINE





COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
А	0.70	0.75	0.80
A1	0.00	0.02	0.05
A2	0.50	0.55	0.60
А3		0.20REF	
b	0.30	0.35	0.40
D	2.90	3.00	3.10
Е	2.90	3.00	3.10
D2	1.60	1.70	1.80
E2	2.30	2.40	2.50
е	0.55	0.65	0.75
K	0.40	0.50	0.60
L	0.35	0.40	0.45



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