

## Vishay General Semiconductor

# **Dual Low-Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.34 \text{ V}$  at  $I_F = 2.5 \text{ A}$ 





PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 5.0 A			
V <sub>RRM</sub>	45 V			
I <sub>FSM</sub>	100 A			
$V_{F}$ at $I_{F} = 5.0 \text{ A}$	0.41 V			
T <sub>J</sub> max.	150 °C			
Package	TO-263AB			
Diode variations	Common cathode			

#### **FEATURES**

• Trench MOS Schottky technology



· Low forward voltage drop, low power losses

· High efficiency operation

(e3)

 Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C

RoHS COMPLIANT

 Material categorization: For definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>

#### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

### **MECHANICAL DATA**

Case: TO-263AB

Molding compound meets UL 94 V-0 flammability rating

Base P/N-E3 - RoHS-compliant, commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	VBT1045C	UNIT	
Maximum repetitive peak reverse voltage		$V_{RRM}$	45	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	10	Α	
	per diode		5		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	100	А	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 2.5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.44	-	V	
	I <sub>F</sub> = 5.0 A			0.49	0.58		
	I <sub>F</sub> = 2.5 A	T <sub>A</sub> = 125 °C		0.34	-		
	I <sub>F</sub> = 5.0 A			0.41	0.50		
Reverse current per diode	$V_R = 45 \text{ V}$ $T_A = 25 \text{ °C}$ $T_A = 125 \text{ °C}$	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	500	μA	
		¹R <sup>(−)</sup>	5	15	mA		

#### **Notes**

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	VBT1045C	UNIT	
Typical thermal resistance	per diode	$R_{ hetaJC}$	3.5	- °C/W	
	per device		2.5		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-263AB	VBT1045C-E3/4W	1.38	4W	50/tube	Tube	
TO-263AB	VBT1045C-E3/8W	1.38	8W	800/reel	Tape and reel	

## **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

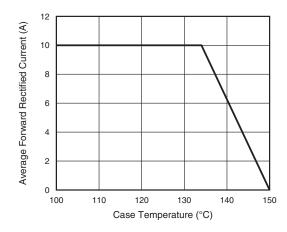


Fig. 1 - Maximum Forward Current Derating Curve

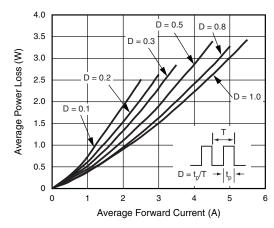


Fig. 2 - Forward Power Loss Characteristics Per Diode



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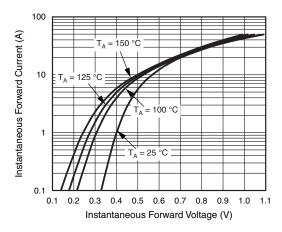


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

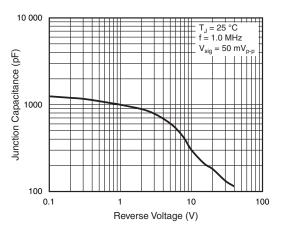


Fig. 5 - Typical Junction Capacitance Per Diode

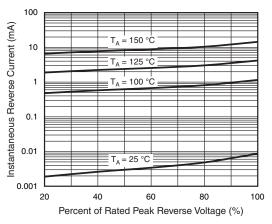


Fig. 4 - Typical Reverse Characteristics Per Diode

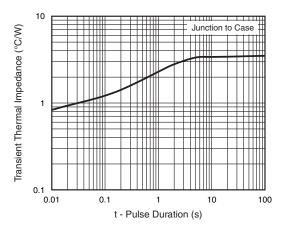


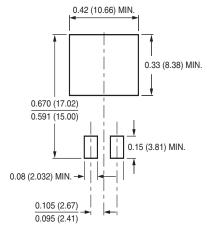
Fig. 6 - Typical Transient Thermal Impedance Per Diode

### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

#### 0.411 (10.45) 0.190 (4.83) 0.380 (9.65) 0.160 (4.06) 0.055 (1.40) 0.245 (6.22) 0.045 (1.14) MIN. 0.055 (1.40) 0.360 (9.14) 0.047 (1.19) 0.320 (8.13) 0.624 (15.85) 0.591 (15.00) Κ 2 -0 to 0.01 (0 to 0.254) 0.110 (2.79) 0.110 (2.79) 0.037 (0.940) 0.021 (0.53) 0.027 (0.686) 0.014 (0.36) 0.105 (2.67) 0.140 (3.56) 0.095 (2.41) 0.205 (5.20) 0.110 (2.79) 0.195 (4.95)

**TO-263AB** 

### **Mounting Pad Layout**





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Vishay

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