

## VCB1 Half Size DIP

### Featuring

3.0 or 5.0 Vdc Option  
 Low Cost  
 1 to 160 MHz  
 Enable/Disable Option  
 TTL/CMOS Compatible



**Frequency Range** 32.768 KHz to 160 MHz

80 to 160 MHz uses a low jitter internal (<50 ps) multiplier IC which will also affect phase noise performance.

**Standard Frequencies** See [Standard Frequency Table](#)

**Package Options** B1 = 0.5" x 0.5" x 0.2" Half Size DIP

**Voltage Options/ Load Drive**  
 A = +5.0 Vdc  $\pm 10\%$  15pF  
 B = +3.3 Vdc  $\pm 10\%$  15pF  
 C = +3.0 Vdc  $\pm 5\%$  15pF  
 E = +5.0 Vdc  $\pm 10\%$  50pF  
 F = +3.3 Vdc  $\pm 5\%$  50pF

**Electrical Options**  
 0 = No Tristate 60/40 Symmetry  
 1 = Tristate 60/40 Symmetry  
 2 = No Tristate 55/45 Symmetry  
 3 = Tristate 55/45 Symmetry  
 5 = Enable Option 60/40 Symmetry  
 6 = Enable Option 55/45 Symmetry

**Enable/Disable**  
 Logic 1 = Enable  
 Logic 0 = Disable

**Stability Options**  
 A =  $\pm 100$  PPM 0°C to +70°C  
 B =  $\pm 50$  PPM 0°C to +70°C  
 C =  $\pm 100$  PPM -40°C to +85°C  
 D =  $\pm 50$  PPM -40°C to +85°C  
 E =  $\pm 25$  PPM 0°C to +70°C  
 F =  $\pm 25$  PPM -40°C to +85°C  
 G =  $\pm 20$  PPM 0°C to +70°C

**Start-Up** 10 ms Maximum

**Aging** <5.0 PPM/year at +40°C dynamic

**Load** HCMOS/TTL

**Current** 50 mA Maximum

**Standard Packaging** Anti Static Tubes

**VCB1-A0A-125M000**

**Typical P/N**

**B1** = 0.5 x 0.5 x 0.2 Half Size DIP

**A** = +5.0 Vdc

**0** = No tristate 60/40 symmetry

**A** = ±100 PPM 0°C to +70°C

[Generate your own part number!](#)

We welcome your custom requests and will issue a custom part number for items that are not listed.

