

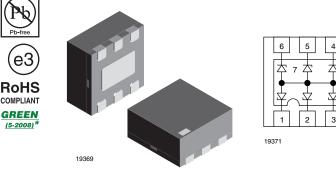
**Vishay Semiconductors** 

### 6-Line ESD-Protection Diode Array in LLP75

#### Features

VISHA

- Ultra compact LLP75-7A package
- 6-line ESD-protection
- Low leakage current I<sub>R</sub> < 1 μA</li>
- Low load capacitance C<sub>D</sub> = 40 pF
- ESD-immunity acc. IEC 61000-4-2
  - ± 30 kV contact discharge ± 30 kV air discharge
- Working voltage range V<sub>RWM</sub> = 5 V
- · Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC





Marking (example only)



Dot = Pin 1 marking XX = Date code YY = Type code (see table below)

#### **Ordering Information**

Device name	Device name Ordering code		Minimum order quantity		
VESD05A6-HA3	VESD05A6-HA3-GS08	3000	15000		

(5-2008)

#### Package Data

Device name	Package name	Type code	Weight	t Compound Moisture sensitivity level S flammability rating		Soldering conditions
VESD05A6-HA3	LLP75-7A	AF	5 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals

\* Please see document "Vishay Green and Halogen-Free Definitions (5-2008)" http://www.vishay.com/doc?99902

# VESD05A6-HA3

#### **Vishay Semiconductors**

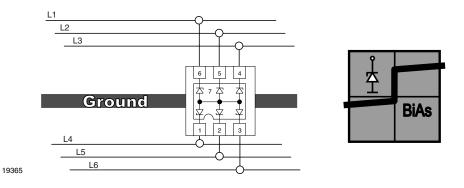


#### **Absolute Maximum Ratings**

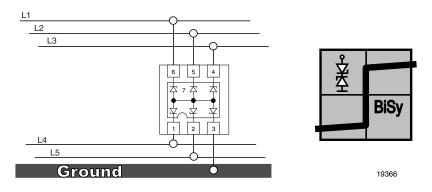
Rating	Test condition	Symbol	Value	Unit	
Peak pulse current	BiAs-Mode: each input (pin 1 - pin 6) to ground (pin 2); acc. IEC 61000-4-5; t <sub>p</sub> = 8/20 μs; single shot			5	А
Peak pulse power	BiAs-mode: each input (pin 1 - pin 6) to ground (pir acc. IEC 61000-4-5; t <sub>p</sub> = 8/20 μs; single shot	P <sub>PP</sub>	60	W	
ESD immunity	Acc. IEC61000-4-2; 10 pulses BiAs-mode: each input (pin 1 - pin 6) to ground (pin 2)	Contact discharge	V <sub>ESD</sub>	± 30	kV
		Air discharge	V <sub>ESD</sub>	± 30	kV
Operating temperature	Junction temperature			- 40 to + 125	°C
Storage temperature			T <sub>STG</sub>	- 55 to + 150	°C

#### **Application Note:**

a) With the VESD05A6-HA3 6 different signal or data lines can be clamped to ground. Due to the different clamping levels in forward and reverse direction the VESD05A6-HA3 clamping behavior is <u>Bi</u>directional and <u>Asymmetrical (BiAs)</u>.



b) If symmetrical clamping behaviour is required the **VESD05A6-HA3** can also be used as a <u>Bi</u>directional <u>Sy</u>mmetrical protection device protecting up to 5 lines. In this case pin no. 7 must not be connected.





#### **Vishay Semiconductors**

#### **Electrical Characteristics**

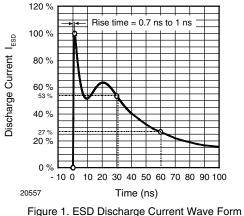
Ratings at 25 °C, ambient temperature unless otherwise specified

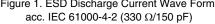
#### VESD05A6-HA3

BiAs mode (between pin 1, 2, 3, 4, 5 or 6 and pin 7)

Parameter	Test conditions/remarks	Symbol	Min.	Тур.	Max.	Unit
Protection paths	Number of lines which can be protected	N <sub>lines</sub>			6	lines
Reverse stand off voltage	at I <sub>R</sub> = 1 μA	V <sub>RWM</sub>	5			V
Max. reverse current	at V <sub>R</sub> = 5 V	I <sub>R</sub>		< 0.1	1	μΑ
Reverse break down voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	6	6.6	7.5	V
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>		8.1	10	V
	at I <sub>PP</sub> = I <sub>PPM</sub> = 5 A	V <sub>C</sub>		11.3	12	V
Forward clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>F</sub>		1.5	1.8	V
	at I <sub>PP</sub> = I <sub>PPM</sub> = 5 A	V <sub>F</sub>		3.2	4.5	V
Line capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	CD		40	50	pF
	at V <sub>R</sub> = 2.5 V; f = 1 MHz	CD		24		pF

**Typical Characteristics** T<sub>amb</sub> = 25 °C, unless otherwise specified





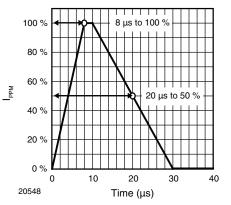


Figure 2. 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

# VESD05A6-HA3

### **Vishay Semiconductors**



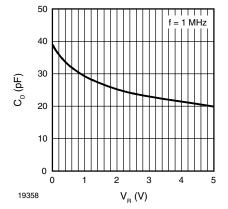


Figure 3. Typical Capacitance C<sub>D</sub> vs. Reverse Voltage V<sub>R</sub>

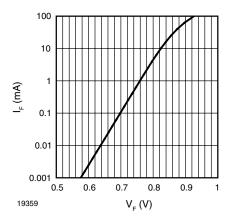


Figure 4. Typical Forward Current  $\rm I_F$  vs. Forward Voltage  $\rm V_F$ 

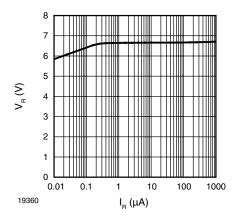


Figure 5. Typical Reverse Voltage V<sub>R</sub> vs. Reverse Current I<sub>R</sub>

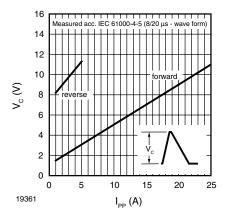


Figure 6. Typical Clamping Voltage vs. Peak Pulse Current IPP

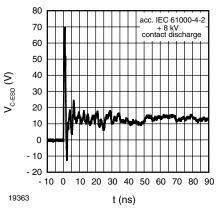


Figure 7. Typical Clamping Performance on + 8 kV - ESD Events (acc. IEC 61000-4-2)

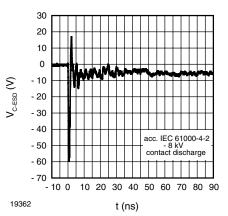


Figure 8. Typical Clamping Performance on - 8 kV - ESD Events (acc. IEC 61000-4-2)



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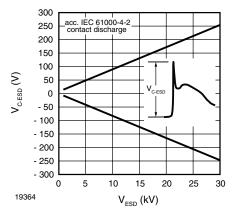
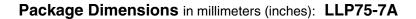
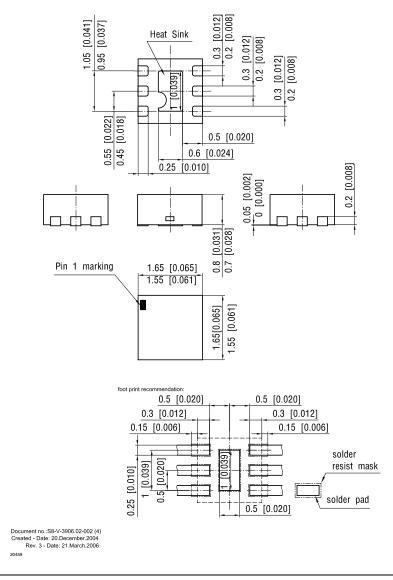


Figure 9. Typical max. Clamping Voltage at ESD Contact Discharge (acc. IEC 61000-4-2)





# VESD05A6-HA3

#### Vishay Semiconductors



#### **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany



Vishay

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