

Product Features

- 1.7 2.7 GHz bandwidth
- 26.6 dB Attenuation Range
- +39.5 dBm Output IP3
- +22 dBm P1dB
- Constant IP3 & P1dB over attenuation range
- Single voltage supply
- Pb-free 6mm 28-pin QFN package
- MTTF > 1000 years

Applications

• Xmit & Rcv AGC circuitry for mobile infrastructure

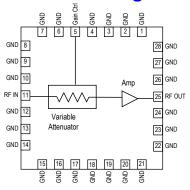
Product Description

The VG111 is a 1.7-2.7GHz high dynamic range variable gain amplifier (VGA) packaged in a low profile Pb-free / RoHS-compliant surface-mount leadless package that measures 6 x 6 mm square.

The +22 dBm output compression point and +39.5 dBm output intercept point of the amplifier are maintained over the entire attenuation range, making the VG111 ideal for use in transmitter and receiver AGC circuits and as a variable gain stage following an LNA in high dynamic range receiver front ends.

Superior thermal design allows the product to have a minimum MTTF rating of 1000 years at a mounting temperature of +85° C. All devices are 100% RF & DC tested and packaged on tape and reel for automated surfacemount assembly.

Functional Diagram



Specifications (1)

Parameter	Unit	Min	Тур	Max
Operational Bandwidth	MHz	1700		2700
Test Frequency (1)	MHz		1900	
Gain at min. attenuation	dB	12	14	
Input Return Loss	dB		12	
Output Return Loss	dB		11	
Output P1dB	dBm		+22	
Output IP3 (2)	dBm	+37	+39.5	
Noise Figure at min. attenuation	dB		4.3	
Gain Variation Range (3)	dB	23.5	26.6	32.5
Group Delay	ns		0.6	
Supply Voltage	V	+5		
Amplifier Current, Pin 25	mA	120	150	180
Gain Control Voltage, V _{CTRL}	V	0		4.5
Gain Control Current, VcTRL=4.5V	mA			20

- 1. Test conditions unless otherwise noted: 25°C, Vdd = +5 V in a tuned application circuit. Vctrl is the control voltage through a BJT transistor and a 100 Ω dropping resistor as shown in the same application circuit.
- 30IP measured with two tones at an output power of +5 dBm/tone separated by 10 MHz. The suppression on the largest IM3 product is used to calculate the 3OIP using a 2:1 rule.

 3. The gain variation range is measured as the difference in gain with Vctrl = 0V and Vctrl = 4.5V at

Typical Performance

Parameter	Units		Туріса	ıl
Frequency (1)	MHz	1900	2140	2600
Gain at min. attenuation	dB	14	13.3	8.1
Input Return Loss	dB	12	14	6.8
Output Return Loss	dB	11	14	10.5
Output P1dB	dBm	+22	+22	+21
Output IP3 ⁽²⁾	dBm	+39.5	+39.5	+39.5
Noise Figure at min. attenuation	dB	4.3	4.5	
Gain Variation Range	dB	26.6	26.6	33.6
Supply Voltage	V		+5	
Amplifier Current, Pin25	mA	150		

Not Recommended for **New Designs**

Recommended Replacement Part: TQM8M9074

Absolute Maximum Rating

Parameter	Rating
Storage Temperature	-55 to +125 °C
Amplifier Supply Voltage (pin 25)	+6 V
Pin 5 (Gain Control) Current	30 mA
RF Input Power (continuous)	+12 dBm
Junction Temperature, Tj	+160° C
Thermal Resistance, Rth	59 °C / W

Operation of this device above any of these parameters may cause permanent damage.

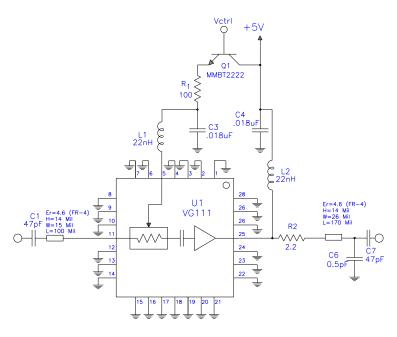
Ordering Information

Part No.	Description
VG111-F	1.7-2.7GHz Variable Gain Amplifier (lead-free/RoHS-compliant QFN package)

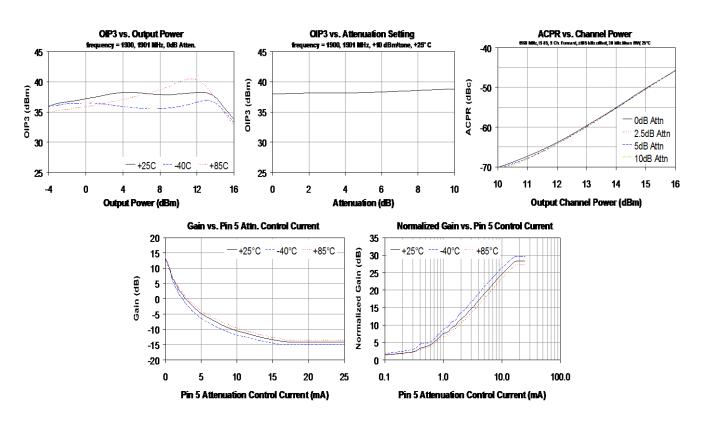
Standard T/R size = 500 pieces on a 7" reel



Application Circuit: 1.8 - 2.0 GHz

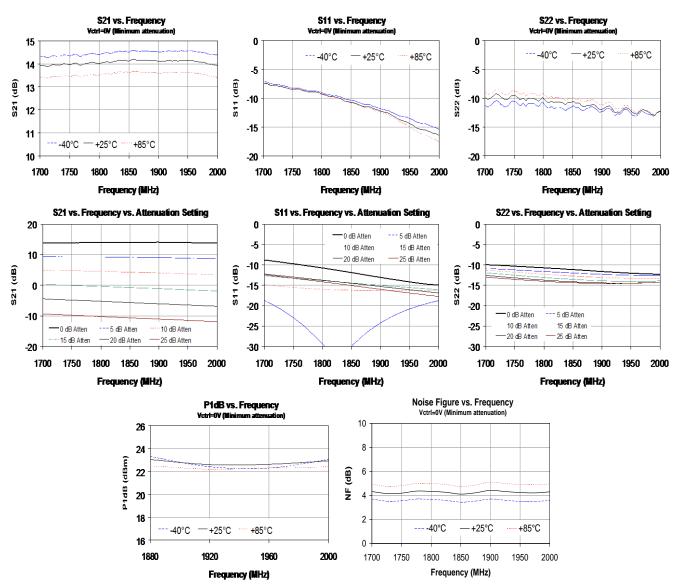


- The amplifier is biased through Pin 25 and should be connected directly into a voltage regulator.
- Components shown in the silkscreen but not on the schematic are not used for this circuit.
- Distances are shown from the edge-to-edge for the land pattern.



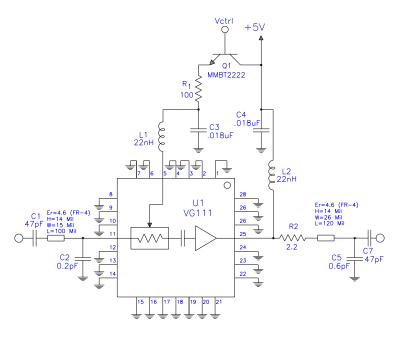


VG111 1.8-2.0 GHz Application Circuit Performance (cont'd)

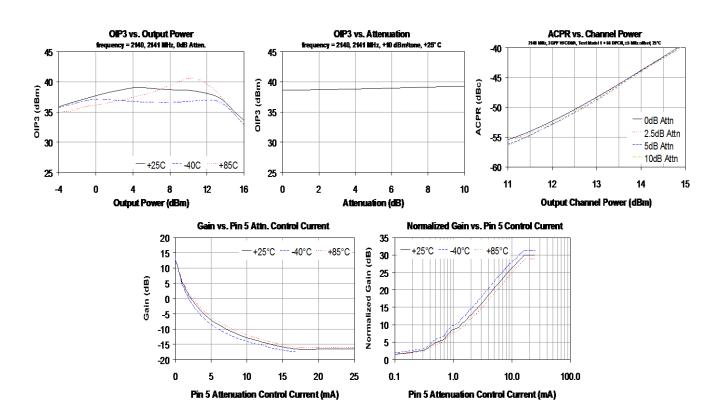




Application Circuit: 2.0 – 2.2 GHz

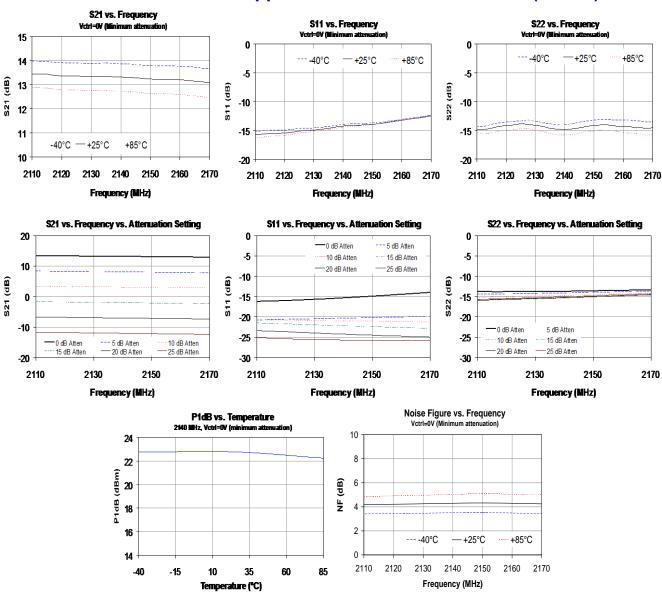


- The amplifier is biased through Pin 25 and should be connected directly into a voltage regulator.
- Components shown in the silkscreen but not on the schematic are not used for this circuit.
- Distances are shown from the edge-to-edge for the land pattern.



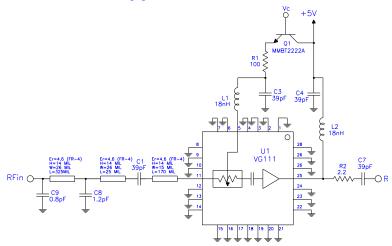


VG111 2.0 - 2.2 GHz Application Circuit Performance (cont'd)

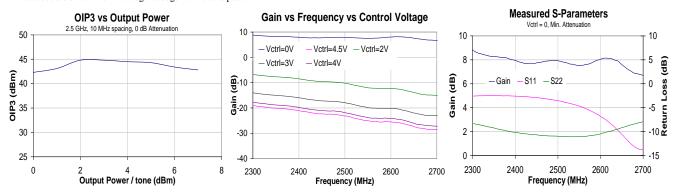




Application Circuit: 2.4 – 2.7 GHz Reference Design



- The amplifier is biased through Pin 25 and should be connected directly into a voltage regulator.
- Components shown in the silkscreen but not on the schematic are not used for this circuit.
- Distances are shown from the edge-to-edge for the land pattern.

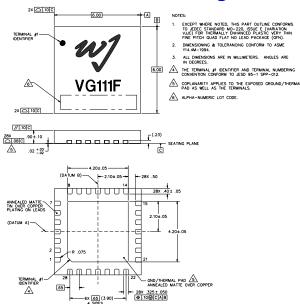




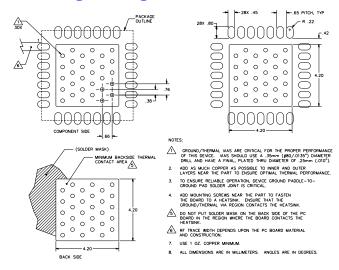
Mechanical Information

This package is lead-free/RoHS-compliant. The plating material on the pins is annealed matte tin over copper. It is compatible with both lead-free (maximum 260 °C reflow temperature) and leaded (maximum 245 °C reflow temperature) soldering processes.

Outline Drawing



Mounting Configuration / Land Pattern



Product Marking

The component will be lasermarked with a "VG111F" designator with an alphanumeric lot code on the top surface of the package.

Tape and reel specifications for this part will be located on the website in the "Application Notes" section.

ESD / MSL Information



ESD Rating: Class 1B

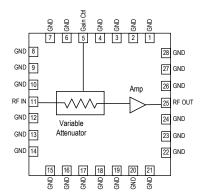
Value: Passes ≥ 500V to <1000V
Test: Human Body Model (HBM)
Standard: JEDEC Standard JESD22-A114

ESD Rating: Class IV

Value: Passes ≥ 1000V to <2000V
Test: Charged Device Model (CDM)
Standard: JEDEC Standard JESD22-C101

MSL Rating: Level 2 at +260°C convection reflow Standard: JEDEC Standard J-STD-020

Functional Pin Layout



Function	Pin No	
Gain Control	5	
RF Input	11	
RF Output / DC bias	25	
No Connect or GND	All other pins	
Ground	Backside copper	

The even numbered pins are hard grounded to the backside paddle internally.