

MAXIMUM RATINGS (Unless otherwise noted, Ta=25°C)

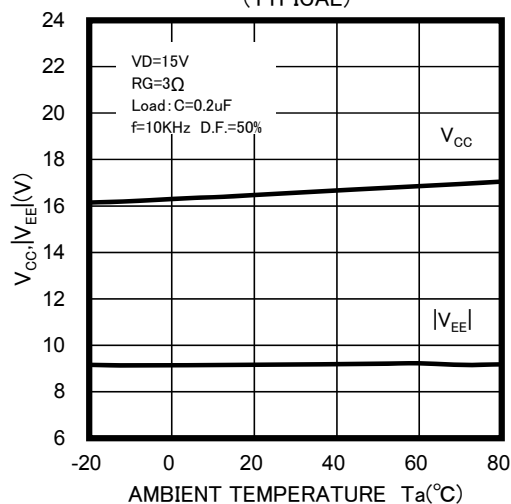
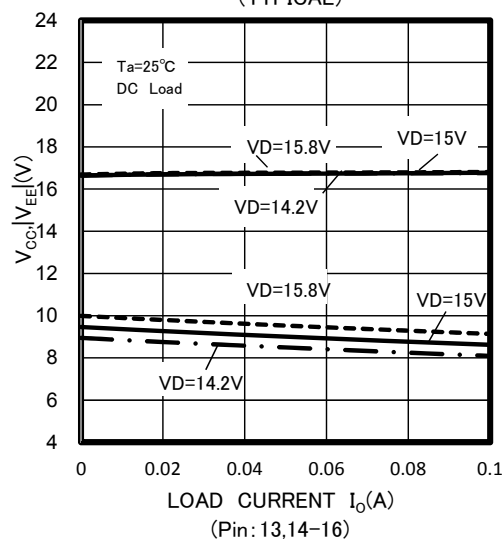
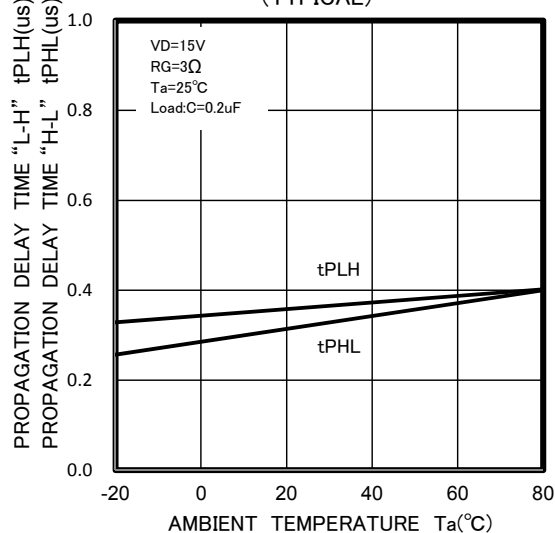
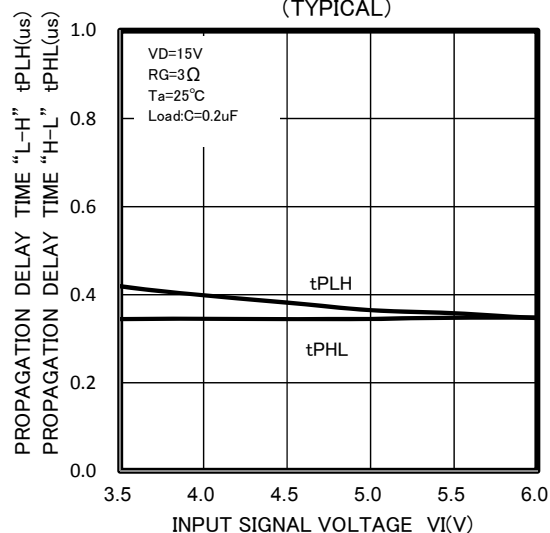
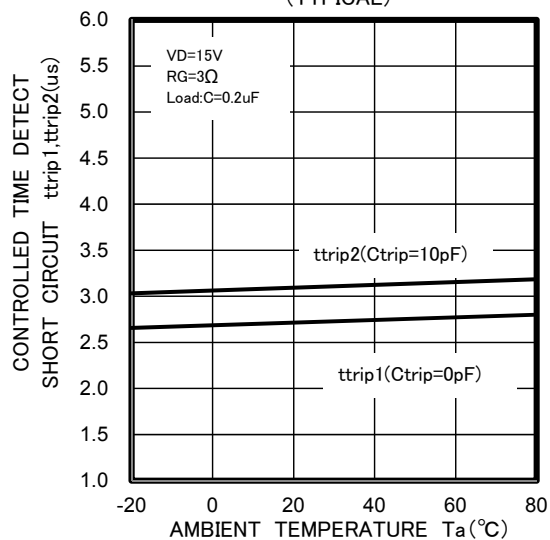
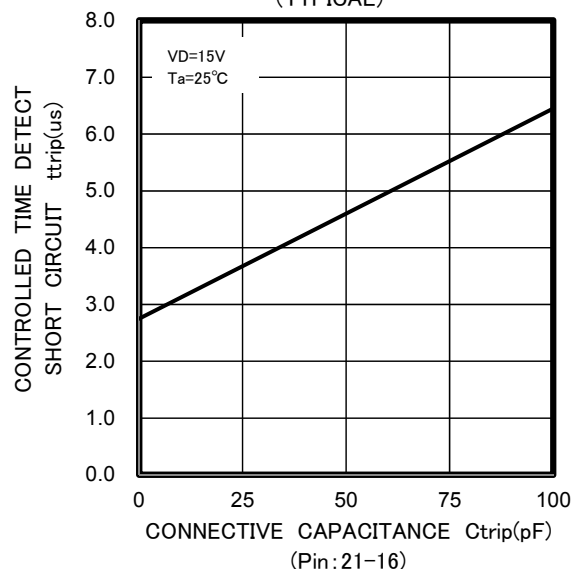
Symbol	Parameter	Conditions	Ratings	Unit
VD	Supply voltage	DC	-1 ~ 16.5	V
VI	Input signal voltage	Applied between pin 6 – 7 50% duty cycle, pulse width 1ms	-1 ~ +7	V
VO	Output voltage	When the output voltage is "H"	VCC	V
IOHP	Output current	Pulse width 2μs	-5	A
IOLP			5	A
Viso	Isolation voltage	Sine wave voltage 60Hz, for 1min.	2500	Vrms
TC	Case temperature	Surface temperature	100	°C
Topr	Operating temperature	No condensation allowable	-40 ~ +70	°C
Tstg	Storage temperature	No condensation allowable	-40 ~ +100 (*1)	°C
I _{FO}	Fault output current	Applied pin 20	20	mA
V _{R22}	Input voltage to pin 22	Applied pin 22	50	V
I _{drive}	Gate drive current	Gate average current	100 (*2)	mA

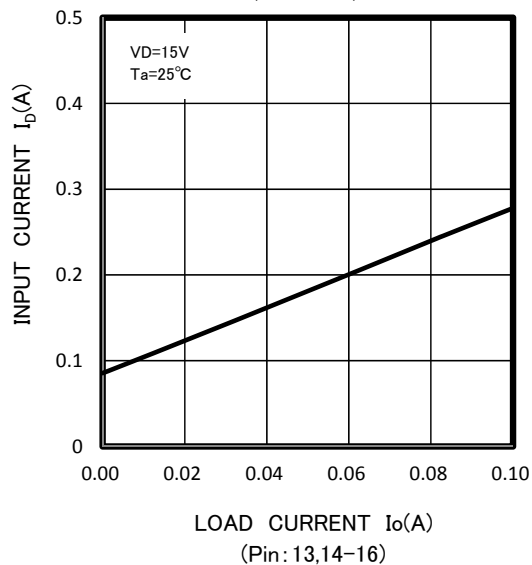
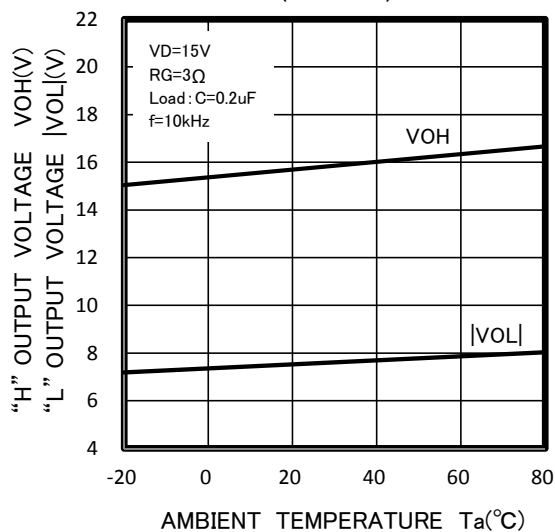
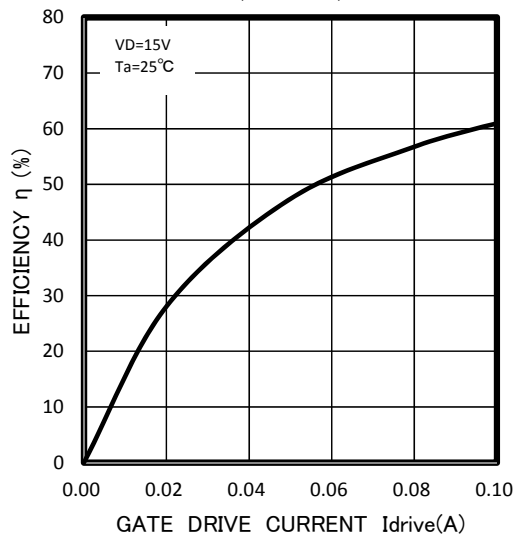
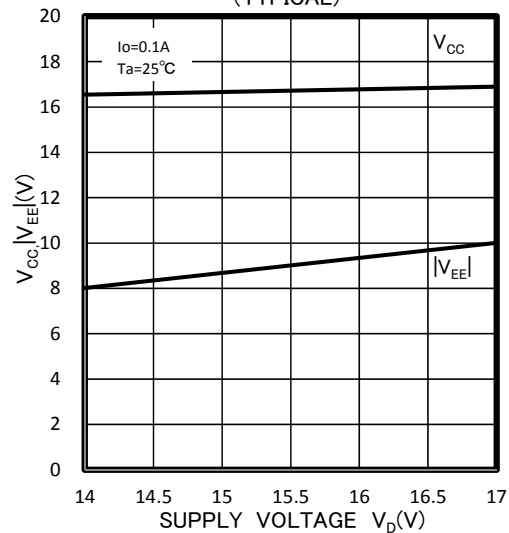
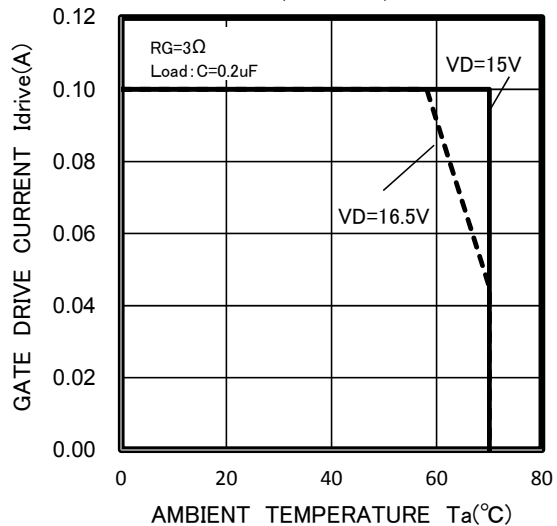
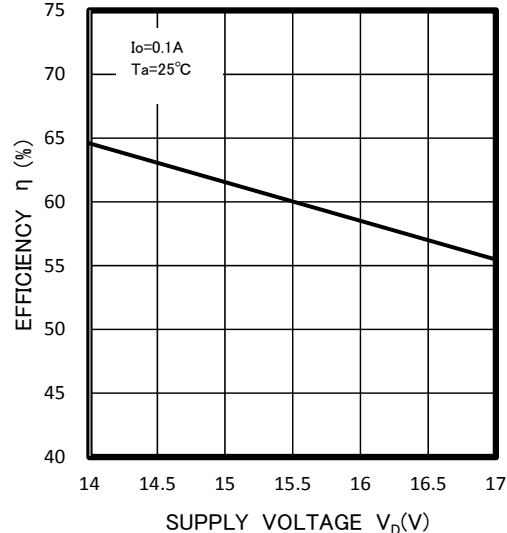
(*1) Differs from H/C condition

(*2) Refer to I_{drive}-Ta CHARACTERISTICS (Needs derating)**ELECTRICAL CHARACTERISTICS** (Unless otherwise noted, Ta=25°C, VD=15V, RG=3Ω)

Symbol	Parameter	Conditions	Limits			Unit
			Min	Typ	Max	
VD	Supply voltage	Recommended range	14.2	15	15.8	V
V _{IN}	Pull-up voltage on input side	Recommended range	4.75	5	5.25	V
I _{IH}	"H" input current	Recommended range	10	13	16	mA
f	Switching frequency	Recommended range	-	-	20	kHz
R _G	Gate resistance	Recommended range	2	-	-	Ω
I _{IH}	"H" input current	V _{IN} = 5V	-	13	-	mA
V _{CC}	Gate positive supply voltage	—	15.2	-	16.9	V
V _{EE}	Gate negative supply voltage	—	-6	-	-11.5	V
η	Gate supply efficiency	Load current = 100mA η = (V _{CC} +I _{VEE} I) x 0.1 / (15 x I _D) x 100	55	60	-	%
V _{OH}	"H" output voltage	10k ohm connected between pin 17-15	14	15.3	16.5	V
V _{OL}	"L" output voltage	10k ohm connected between pin 17-15	-5.5	-	-11	V
t _{PLH}	"L-H" propagation time	I _{IH} = 13mA	0.2	0.4	1	μs
t _r	"L-H" rise time	I _{IH} = 13mA	-	0.4	1	μs
t _{PHL}	"H-L" propagation time	I _{IH} = 13mA	0.2	0.4	1	μs
t _f	"H-L" fall time	I _{IH} = 13mA	-	0.3	1	μs
t _{timer}	Timer	Between start and cancel (under input sign "L")	1	-	2	ms
I _{FO}	Fault output current	Applied pin 20, R = 4.7k Ω	-	5	-	mA
t _{trip1}	Controlled time detect short circuit 1	Pin 22 : 15V and more, pin 21 : open	-	2.7	-	μs
t _{trip2}	Controlled time detect short circuit 2 (*4)	Pin 22: 15V and more, pin 29-21,22 : 10pF (connective capacitance)	-	3.1	-	μs
V _{SC}	SC detect voltage	Collector voltage of module	15	-	-	V

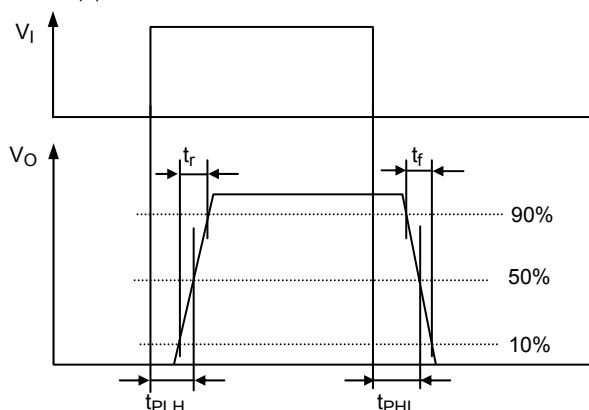
(*4) Length of wiring of condenser controlled time detect short-circuit is within 5cm from pin 16 and 21 coming and going.

PERFORMANCE CURVES **$V_{CC}, |V_{EE}| - T_a$ CHARACTERISTICS
(TYPICAL)** **$V_{CC}, |V_{EE}| - I_O$ CHARACTERISTICS
(TYPICAL)** **$t_{PLH}, t_{PHL} - T_a$ CHARACTERISTICS
(TYPICAL)** **$t_{PLH}, t_{PHL} - V_I$ CHARACTERISTICS
(TYPICAL)** **$t_{trip} - T_a$ CHARACTERISTICS
(TYPICAL)** **$t_{trip} - C_{trip}$ CHARACTERISTICS
(TYPICAL)**

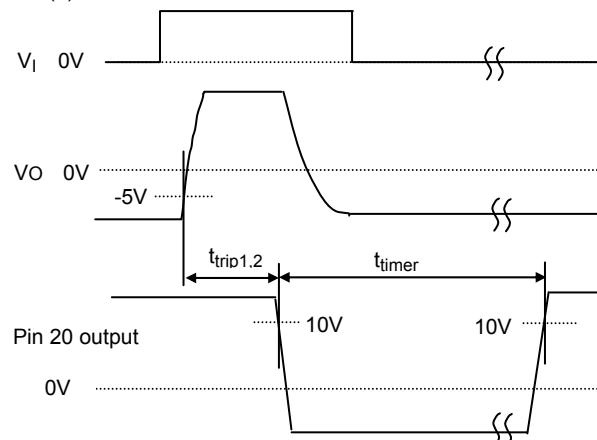
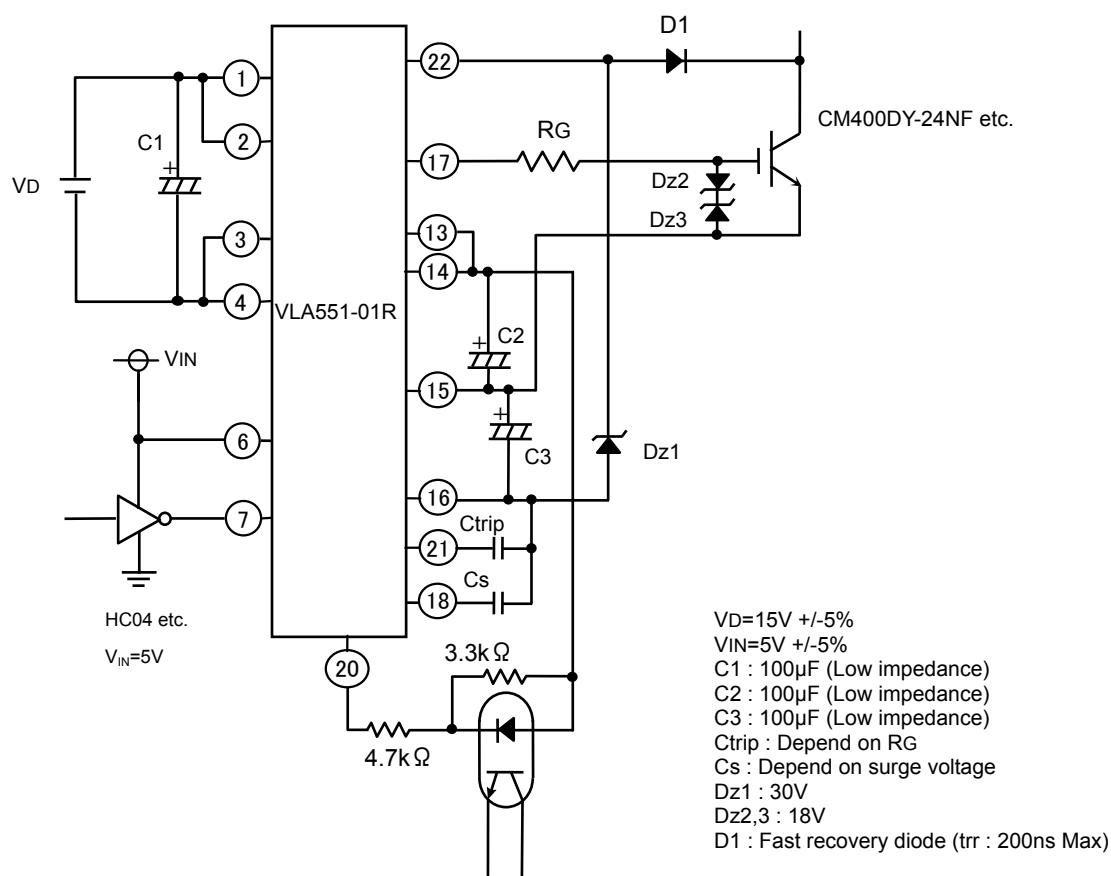
I_D - I_O CHARACTERISTICS
(TYPICAL) **$VOH, |VOL|$ - T_a CHARACTERISTICS**
(TYPICAL) **η - I_{drive} CHARACTERISTICS**
(TYPICAL) **$V_{CC}, |V_{EE}|$ - V_D CHARACTERISTICS**
(TYPICAL) **I_{drive} - T_a CHARACTERISTICS**
(TYPICAL) **η - V_D CHARACTERISTICS**
(TYPICAL)

DEFINITION OF CHARACTERISTICS

(1) SWITCHING OPERATION



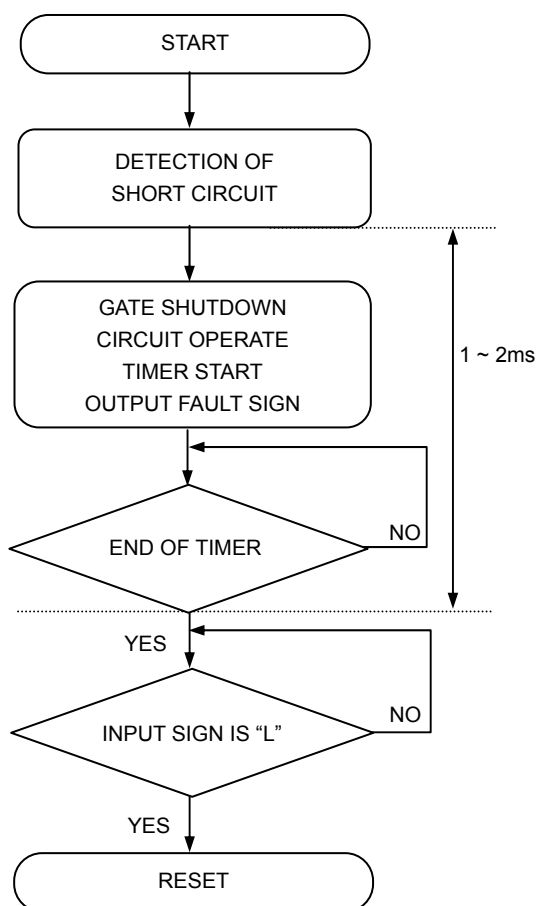
(2) OPERATION OF SHORT CIRCUIT PROTECTION

**APPLICATION EXAMPLE****PRECAUTION**

- (1) Voltage compensate capacitors are expected to be located as close as possible to the Hybrid IC.
- (2) Minimize the area of closed circuit of gate circuit so as not to be affected by induction noise.
- (3) D1 requires approximately the same voltage of power modules.
- (4) When recovery current flow in D1, pin 22 is applied high voltage .
In that case , counterplan for protection which insert a zener diode between pin 22 and 16 are necessary like above diagram.
- (5) When you make late speed of reverse bias at the time of short circuit protection operation,
please adjust and connect a capacitor between pin 16 and 18.

OPERATION OF PROTECTION CIRCUIT

- (1) In case the gate voltage is "H" and the collector voltage is high, this Hybrid IC will recognize the circuit as short circuit and immediately reduce the gate voltage. Besides, put out a fault sign ("L") which inform that protection circuit is operating at the same time from pin 20.
- (2) The protection circuit reset and resorted to ordinary condition if input sign is "L" when the predetermined time(1 ~ 2ms) passed. ("L" period needs 15 μ s or more)
- (3) When the output rises, the controlled time detect short circuit (Typ 2.7 μ s) is set up so that on-time of IGBT can be secured properly. It is possible to adjust that time by connecting the capacitor (Ctrip) between pin 16 and 21.

OPERATION FLOW ON DETECTING SHORT CIRCUIT

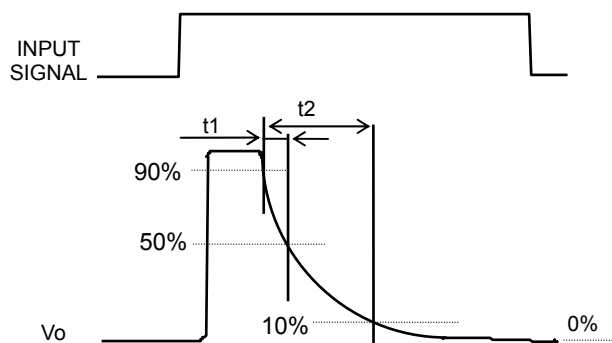
Note : "L" output voltage with protection circuit operating is about VEE + 2V.

ADJUSTMENT OF OUTPUT FALL TIME

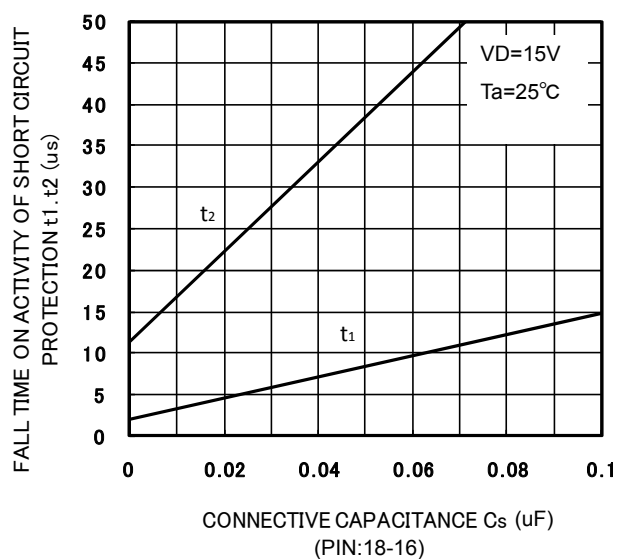
(When the protection circuit is operating)

In case you want to decrease the speed of reverse bias when the protection circuit is operating, you can adjust that speed by connecting the capacitor (Cs) between pin 16 and 18.

(Please refer to under figures.)



t1,t2 VS. Cs CHARACTERISTICS
(TYPICAL)



FOR SAFETY USING

Great detail and careful attention are given to the production activity of Hics, such as the development, the quality of production, and in its reliability. However the reliability of Hics depends not only on their own factors but also in their condition of usage. When handling Hics, please note the following cautions.

CAUTIONS	
Packing	The materials used in packing Hics can only withstand normal external conditions. When exposed to outside shocks, rain and certain environmental contaminants, the packing materials will deteriorates. Please take care in handling.
Carrying	1) Don't stack boxes too high. Avoid placing heavy materials on boxes. 2) Boxes must be positioned correctly during transportation to avoid breakage. 3) Don't throw or drop boxes. 4) Keep boxes dry. Avoid rain or snow. 5) Minimal vibration and shock during transportation is desirable.
Storage	When storing Hics, please observe the following notices or possible deterioration of their electrical characteristics, risk of solder ability, and external damage may occur. 1) Devices must be stored where fluctuation of temperature and humidity is minimal, and must not be exposed to direct sunlight. Store at the normal temperature of 5 to 30 degrees Celsius with humidity at 40 to 60%. 2) Avoid locations where corrosive gasses are generated or where much dust accumulates. 3) Storage cases must be static proof. 4) Avoid putting weight on boxes.
Extended storage	When extended storage is necessary, Hics must be kept non-processed. When using Hics which have been stored for more than one year or under severe conditions, be sure to check that the exterior is free from flaw and other damages.
Maximum ratings	To prevent any electrical damages, use Hics within the maximum ratings. The temperature, current, voltage, etc. must not exceed these conditions.
Polarity	To protect Hics from destruction and deterioration due to wrong insertion, make sure of polarity in inserting leads into the board holes, conforming to the external view for the terminal arrangement.

**Keep safety first in your circuit designs!**

• ISAHAYA Electronics Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (1) placement of substitutive, auxiliary circuits, (2) use of non-flammable material or (3) prevention against any malfunction or mishap.

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