

DESCRIPTION

VLA552-01R is a hybrid integrated circuit designed for driving n-channel IGBT modules in any gate-amplifier application. Electrical isolation voltage between input and output is 4000Vrms (for 1minute). This device includes the isolated type DC-DC converter for a Gate drive. Therefore design of the gate power supply is not required.

The system of built-in short circuit protection provides a margin to time by function to maintain reverse bias for a predetermined time after the detection of short circuit.

Recommended IGBT modules:

$V_{CES} = 1200V$ series up to 3600A class

$V_{CES} = 1700V$ series up to 3600A class

FEATURES

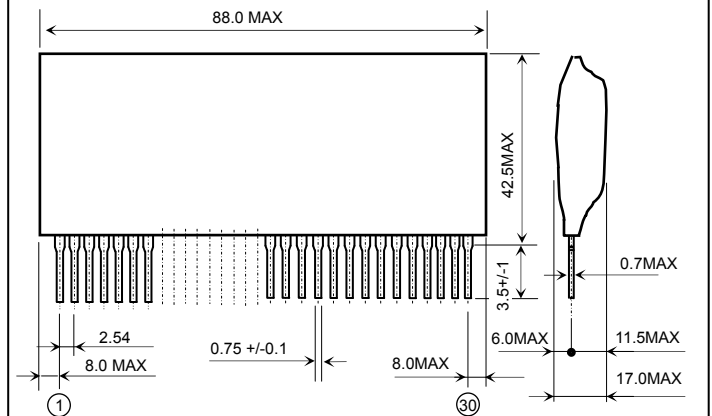
- Built-in the isolated type DC-DC converter for gate drive
- SIP outline allows more space on mother board
- Built-in short circuit protection (With a pin for fault output)
- Possible to assist collector clamp circuit
- Variable fall time on activity of short circuit protection
- Electrical isolation voltage between input and output is 4000Vrms (for 1minute)
- CMOS compatible input.

APPLICATIONS

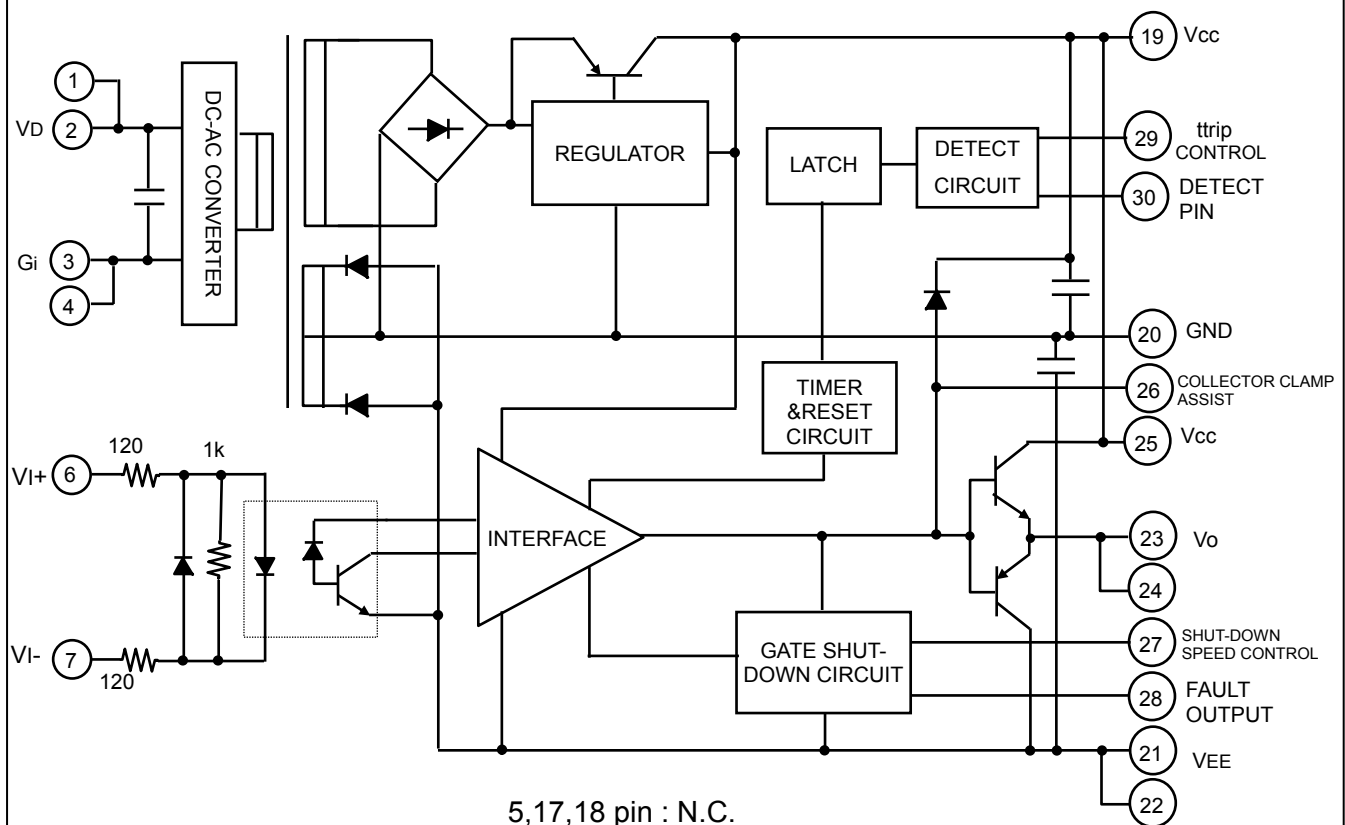
To drive IGBT modules for general industrial use apparatus.

OUTLINE DRAWING

Dimensions : mm



BLOCK DIAGRAM



MAXIMUM RATINGS (Unless otherwise noted, Ta=25°C)

Symbol	Parameter	Conditions	Ratings	Unit
VD	Supply voltage	DC	-1 ~ 16.5	V
VI	Input signal voltage	Applied between pin 6 – 7 50% duty cycle, pulse width 1ms	-7 ~ +7	V
IOHP	Output current	Pulse width 3μs	-24	A
IOLP			24	A
Viso	Isolation voltage	Sine wave voltage 60Hz, for 1min. , R.H.<60%	4000	Vrms
TC	Case temperature	Surface temperature	100	°C
Topr	Operating temperature	No condensation allowable	-25 ~ +70	°C
Tstg	Storage temperature	No condensation allowable	-40 ~ +100 (*1)	°C
I _{FO}	Fault output current	Applied pin 28	20	mA
V _{R30}	Input voltage to pin 30	Applied pin 30	60	V
I _{drive}	Gate drive current	Gate average current	210 (*2)	mA

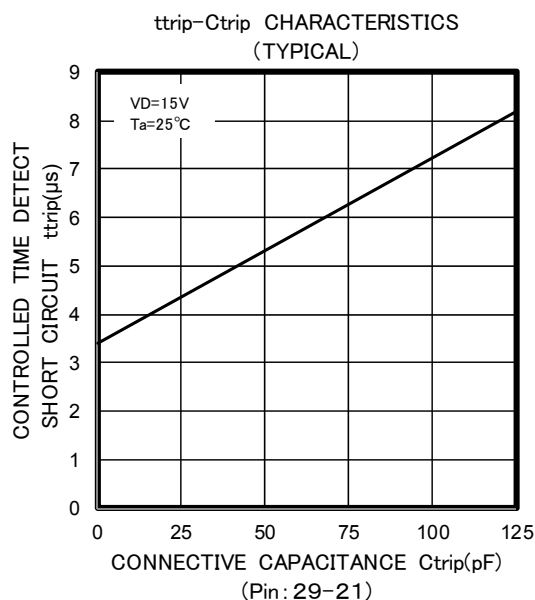
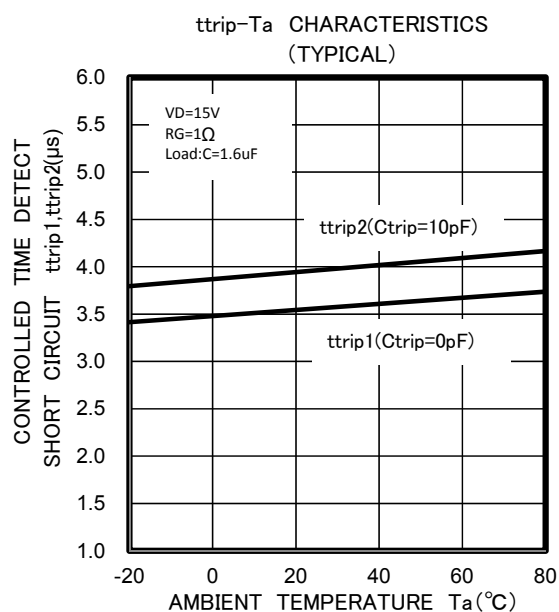
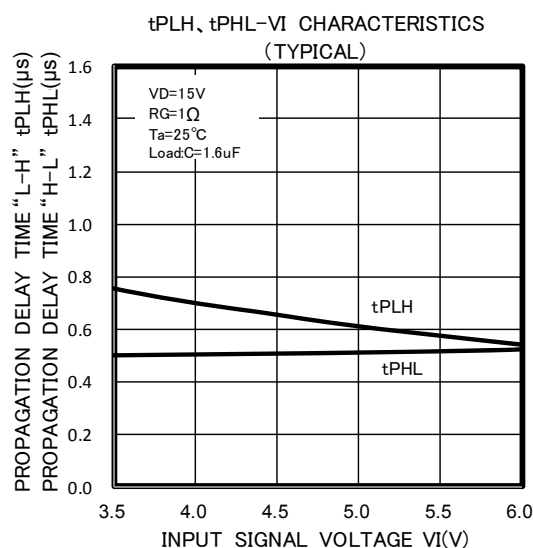
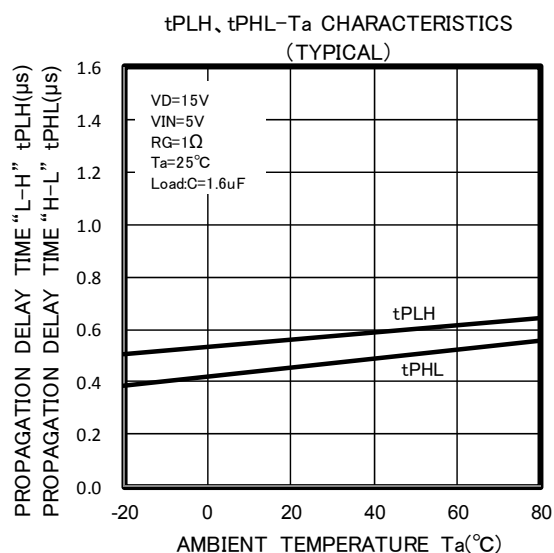
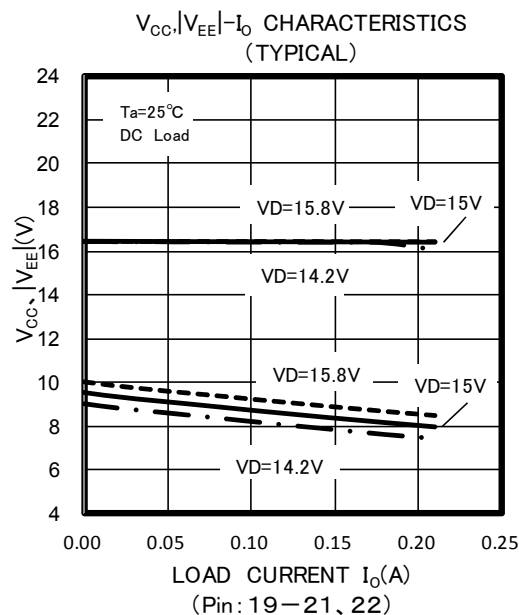
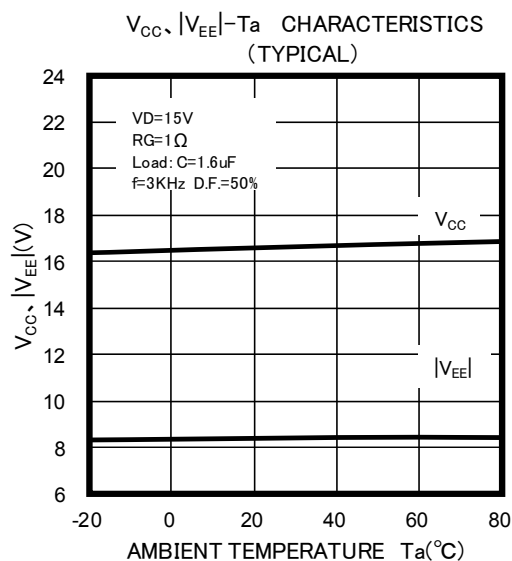
(*1) Differs from H/C condition

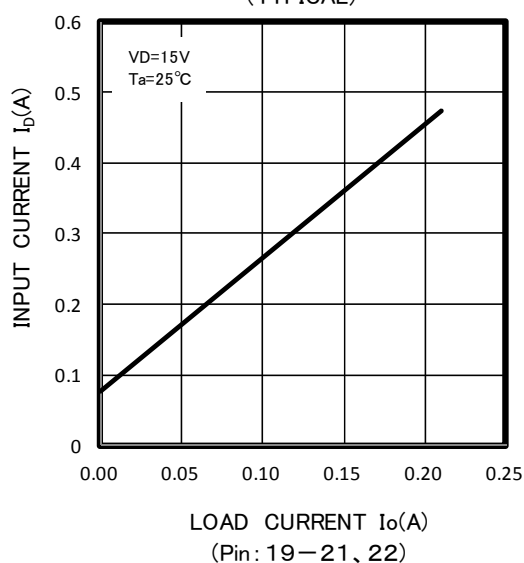
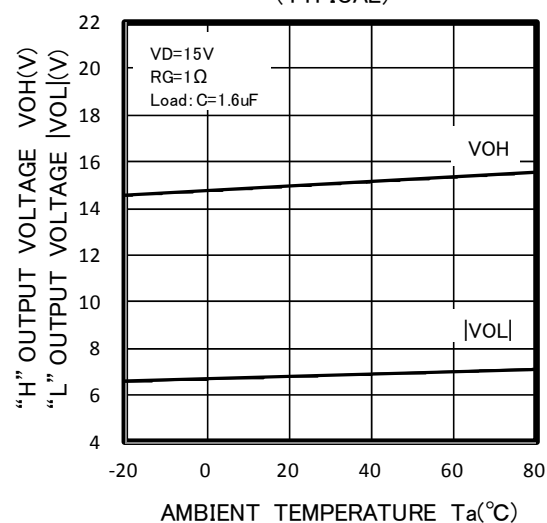
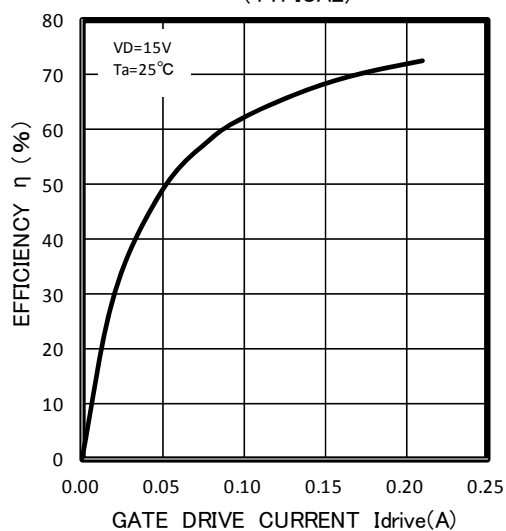
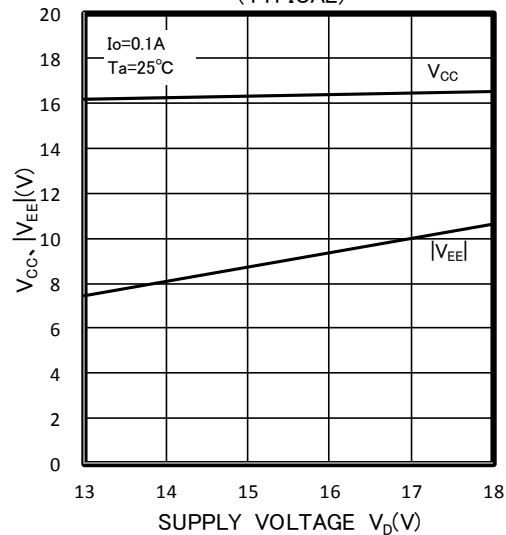
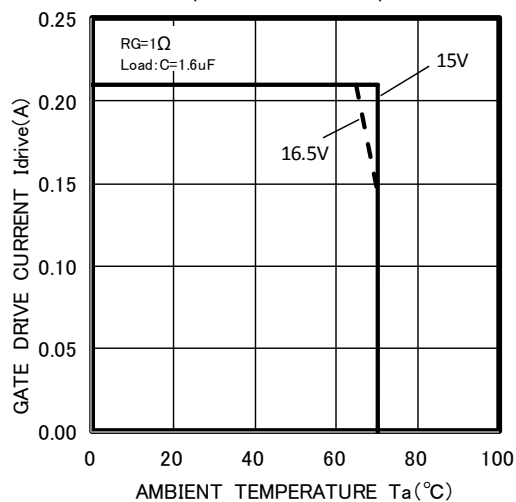
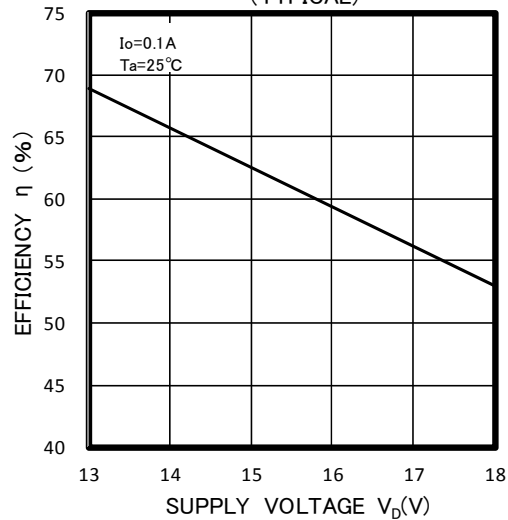
(*2) Refer to I_{drive}-Ta CHARACTERISTICS.**ELECTRICAL CHARACTERISTICS** (Unless otherwise noted, Ta=25°C, VD=15V, RG=1 ohm, CL=1.6μF, f=3kHz)

Symbol	Parameter	Conditions	Limits			Unit
			Min	Typ	Max	
VD	Supply voltage	Recommended range	14.2	15	15.8	V
V _{IN}	Pull-up voltage on input side	Recommended range	4.75	5	5.25	V
I _{IH}	Input signal current	Recommended range	10	12	16	mA
f	Switching frequency	Recommended range	-	-	10	kHz
R _G	Gate resistance	Recommended range	0.33	-	-	ohm
I _{IH}	Input signal current	V _{IN} = 5V HCMOS drive	-	12	-	mA
V _{cc}	Gate positive supply voltage	-	15.2	16.5	17.5	V
V _{EE}	Gate negative supply voltage	-	-6	-8	-11.5	V
η	Gate supply efficiency	Load current = 210mA η = (V _{cc} + V _{EE}) x 0.21 / (15 x I _D) x 100	60	72	-	%
V _{OH}	"H" output voltage	10k ohm connected between pin 23-20	14	15.3	16.5	V
V _{OL}	"L" output voltage	10k ohm connected between pin 23-20	-5.5	-7	-11	V
t _{PLH}	"L-H" propagation time	I _{IH} = 12mA	0.3	-	1	μs
t _r	"L-H" rise time	I _{IH} = 12mA	-	0.6	1.2	μs
t _{PHL}	"H-L" propagation time	I _{IH} = 12mA	0.3	-	1	μs
t _f	"H-L" fall time	I _{IH} = 12mA	-	0.3	1.2	μs
t _{timer}	Timer	Between start and cancel (under input sign "OFF")	1	-	2	ms
I _{FO}	Fault output current	Applied pin 28, R = 4.7k ohm	-	5	-	mA
t _{trip1}	Controlled time detect short circuit 1	Pin 30 : 15V and more, pin 29 : open	-	3.5	-	μs
t _{trip2}	Controlled time detect short circuit 2 (*3)	Pin 30 : 15V and more, pin 29-21,22 : 10pF (connective capacitance)	-	3.9	-	μs
V _{SC}	SC detect voltage	Collector voltage of IGBT	15	-	-	V

(*3) Length of wiring of condenser controlled time detect short-circuit is within 5cm from pin 21,22 and 29 coming and going.

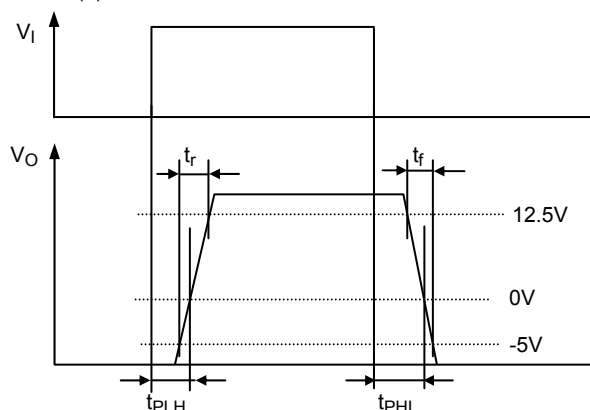
PERFORMANCE CURVES



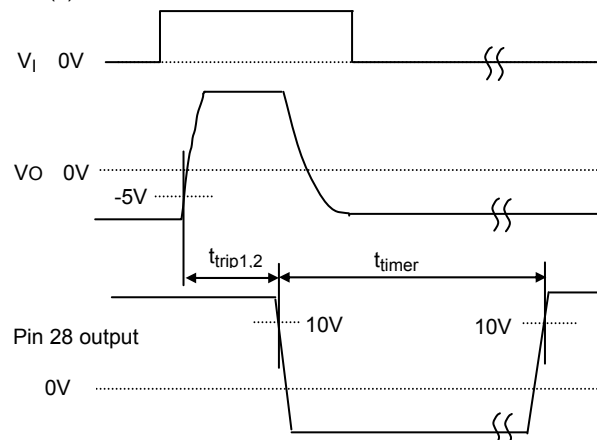
I_D - I_O CHARACTERISTICS
(TYPICAL) V_{OH} , $|V_{OL}|$ - T_a CHARACTERISTICS
(TYPICAL) η - I_{drive} CHARACTERISTICS
(TYPICAL) V_{CC} , $|V_{EE}|$ - V_D CHARACTERISTICS
(TYPICAL) I_{drive} - T_a CHARACTERISTICS
(MAXIMUM RATING) η - V_D CHARACTERISTICS
(TYPICAL)

DEFINITION OF CHARACTERISTICS

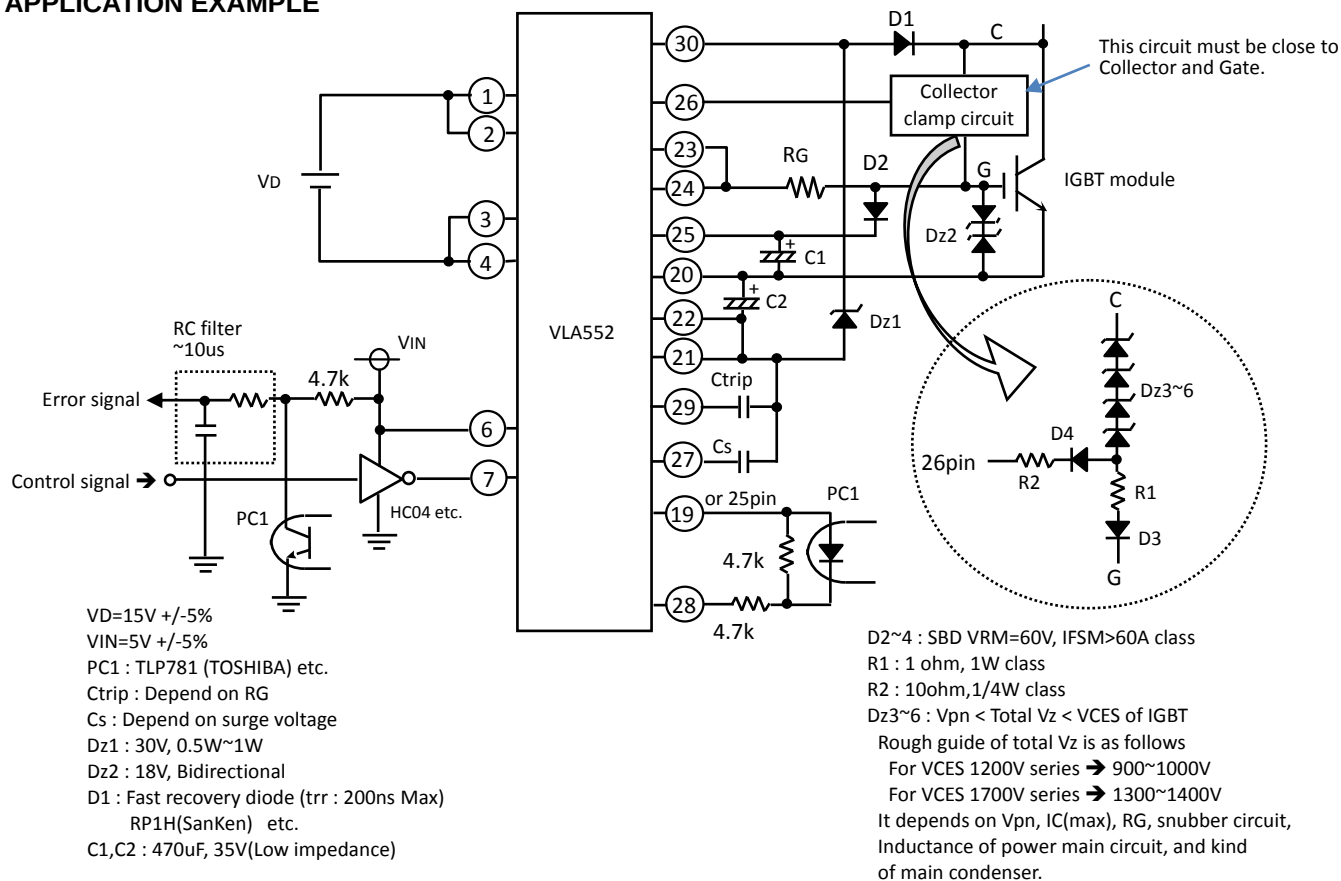
(1) SWITCHING OPERATION



(2) OPERATION OF SHORT CIRCUIT PROTECTION



APPLICATION EXAMPLE

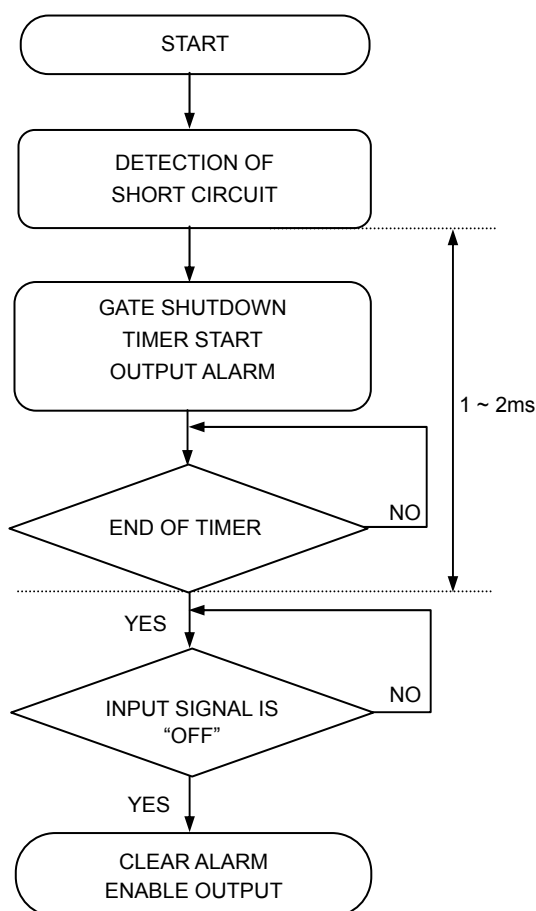


PRECAUTION

- (1) Voltage compensate capacitors are expected to be located as close as possible to the Hybrid IC.
- (2) Minimize the area of closed circuit of gate circuit so as not to be affected by induction noise.
- (3) D1 requires approximately the same voltage of IGBT modules.
- (4) When recovery current flow in D1, pin 30 is applied high voltage.
 In that case, counterplan for protection which insert a zener diode between pin21 and 30 are necessary like above diagram.
- (5) When you make late speed of reverse bias at the time of short circuit protection operation,
 please adjust and connect a capacitor between pin 21 and 27.
- (6) When the built in short-circuit protection circuit need not be used, please connect resistance of 4.7k ohm between pin 30 and 20.
 (D1 and Dz1 are not required.)
- (7) When the collector clamp circuit operates repeatedly, it may be destroyed for heat. That is why the confirmation of calorific value is necessary for zener diode by actual inverter test.

OPERATION OF PROTECTION CIRCUIT

- (1) In case the gate voltage is "H" and the collector voltage is high, this Hybrid IC will recognize the circuit as short circuit and immediately reduce the gate voltage. Besides, put out a fault sign ("L") which inform that protection circuit is operating at the same time from pin 28.
- (2) The protection circuit reset and return to ordinary condition if input signal is "L" when the predetermined time(1 ~ 2ms) passed. ("L" period needs 15 μ s or more)
- (3) When the output rises, the controlled time detect short circuit (Typ 3.5 μ s) is set up so that on-time of IGBT can be secured properly. It is possible to adjust that time by connecting the capacitor (Ctrip) between pin 21,22 and 29.

OPERATION FLOW ON DETECTING SHORT CIRCUIT

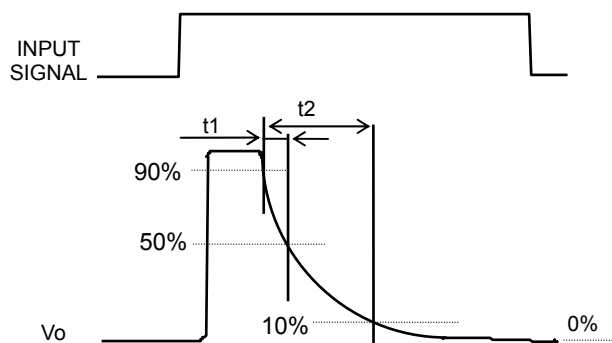
Note : Output voltage with protection circuit operating is about $-|V_{EE}| + 2V$.

ADJUSTMENT OF OUTPUT FALL TIME

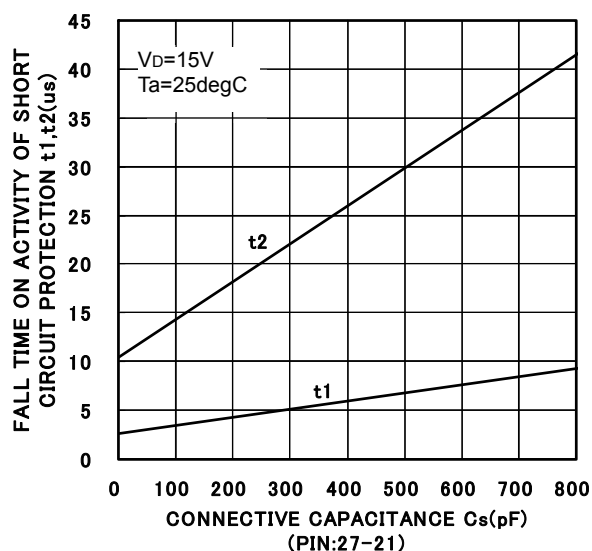
(When the protection circuit is operating)

In case you want to decrease the speed of reverse bias when the protection circuit is operating, you can adjust that speed by connecting the capacitor (Cs) between pin 21 and 27.

(Please refer to under figures.)



**t1,t2 VS. Cs CHARACTERISTICS
(TYPICAL)**



CAPACITY FOR POWER SUPPLY ON INPUT SIDE

This product has isolated DCDC converter built in for gate drive.
When you chose the power supply on input side,
please select the product that can supply the current capacity proven
by next 3steps.

1st step : Calculation for gate average current

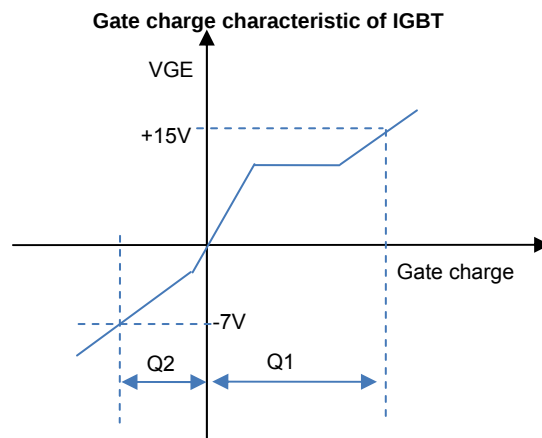
$$I_{drive} = (Q1 + |Q2|) \times f$$

I_{drive} : Gate average current

Q1 : Gate charge at +15V (Read from data sheet of IGBT)

Q2 : Gate charge at -7V (Read from data sheet of IGBT)

f : Switching frequency of IGBT



2nd step : Reading required current from performance curve

If the result of calculation for I_{drive} is 100mA

I_D is about 270mA by right chart.

I_D : Consumption current of DCDC converter in this HIC

3rd step : Securing the margin

$$I_{out} = I_D \times (1 + \text{margin})$$

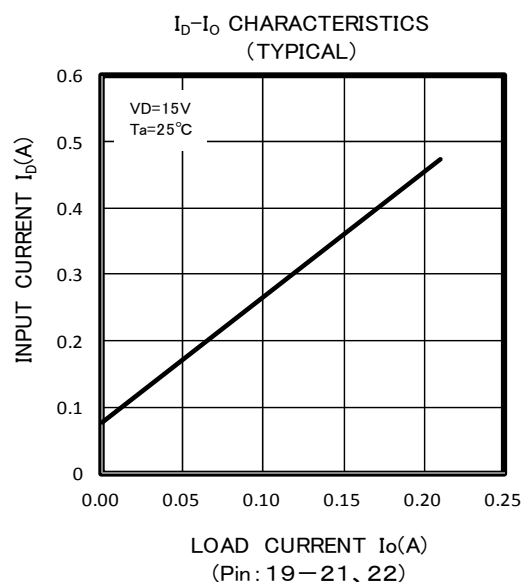
I_{out} : Output current of input power supply

Margin : 0.4

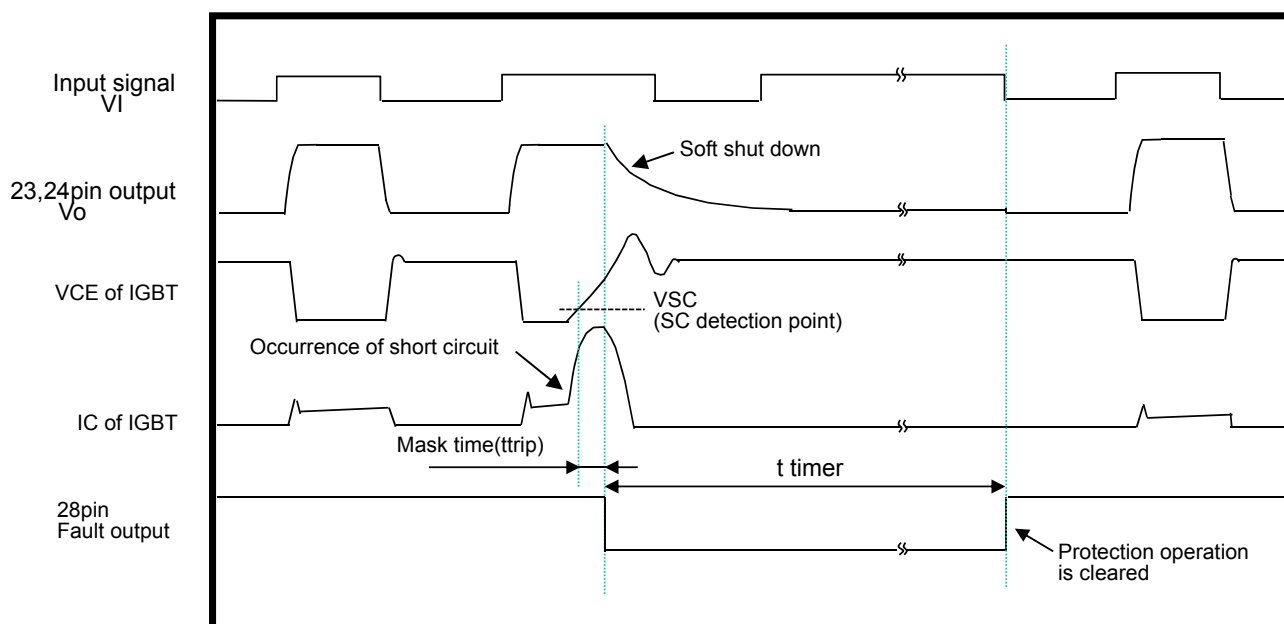
If the result of I_D is 270mA, please prepare the power supply
that has the following spec.

Output voltage : 15V

Output current : more than 350mA



TIMING CHART



FOR SAFETY USING

Great detail and careful attention are given to the production activity of Hics, such as the development, the quality of production, and in its reliability. However the reliability of Hics depends not only on their own factors but also in their condition of usage. When handling Hics, please note the following cautions.

CAUTIONS	
Packing	The materials used in packing Hics can only withstand normal external conditions. When exposed to outside shocks, rain and certain environmental contaminants, the packing materials will deteriorates. Please take care in handling.
Carrying	1) Don't stack boxes too high. Avoid placing heavy materials on boxes. 2) Boxes must be positioned correctly during transportation to avoid breakage. 3) Don't throw or drop boxes. 4) Keep boxes dry. Avoid rain or snow. 5) Minimal vibration and shock during transportation is desirable.
Storage	When storing Hics, please observe the following notices or possible deterioration of their electrical characteristics, risk of solder ability, and external damage may occur. 1) Devices must be stored where fluctuation of temperature and humidity is minimal, and must not be exposed to direct sunlight. Store at the normal temperature of 5 to 30 degrees Celsius with humidity at 40 to 60%. 2) Avoid locations where corrosive gasses are generated or where much dust accumulates. 3) Storage cases must be static proof. 4) Avoid putting weight on boxes.
Extended storage	When extended storage is necessary, Hics must be kept non-processed. When using Hics which have been stored for more than one year or under severe conditions, be sure to check that the exterior is free from flaw and other damages.
Maximum ratings	To prevent any electrical damages, use Hics within the maximum ratings. The temperature, current, voltage, etc. must not exceed these conditions.
Polarity	To protect Hics from destruction and deterioration due to wrong insertion, make sure of polarity in inserting leads into the board holes, conforming to the external view for the terminal arrangement.

**Keep safety first in your circuit designs!**

• ISAHAYA Electronics Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (1) placement of substitutive, auxiliary circuits, (2) use of non-flammable material or (3) prevention against any malfunction or mishap.

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