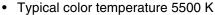


Standard SMD LED PLCC-2



FEATURES

- High efficient InGaN technology
- Very narrow chromaticity coordinate group categorization according to CIE1931 per packing unit



- · EIA and ICE standard package
- Compatible with reflow, vapor phase and wave solder processes acc. to CECC 00802 and J-STD-020



AUTOMOTIVE

- Available in 8 mm tape reel
- Preconditioning: according to JEDEC level 2a
- ESD-withstand voltage: up to 2 kV according to JESD22-A114-B
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- AEC-Q101 qualified

DESCRIPTION

This device has been designed to meet the increasing demand for white SMD LED.

The package of the VLMW41.. is the PLCC-2.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled with a mixture of epoxy and TAG phosphor.

The TAG phosphor converts the blue emission partially to yellow, which mixes with the remaining blue to give white.

PRODUCT GROUP AND PACKAGE DATA

Product group: LED
Package: SMD PLCC-2
Product series: standard
Angle of half intensity: ± 60°

APPLICATIONS

- Camera flash light
- · Signal and symbol luminaire
- Marker lights
- Interior and exterior automotive lighting: brake lights, turn lights, backlighting, side markers
- · Indicator lighting

PARTS TABLE			
PART	COLOR, LUMINOUS INTENSITY	CHROMATICITY COORDINATES	TECHNOLOGY
VLMW41S1T2-JKPL-08	White, I _V = (180 to 450) mcd	x/y typ.: 0.33/0.33	InGaN/Sapphire and TAG
VLMW41S1T2-JKPL-18	White, I _V = (180 to 450) mcd	x/y typ.: 0.33/0.33	InGaN/Sapphire and TAG
VLMW41S1T2-JKKL-08	White, $I_V = (180 \text{ to } 450) \text{ mcd}$	x/y typ.: 0.30/0.28	InGaN/Sapphire and TAG
VLMW41S1T2-KKLL-08	White, $I_V = (180 \text{ to } 450) \text{ mcd}$	x/y typ.: 0.31/0.30	InGaN/Sapphire and TAG
VLMW41S1T2-LKML-08	White, I _V = (180 to 450) mcd	x/y typ.: 0.32/0.31	InGaN/Sapphire and TAG
VLMW41S1T2-LKML-18	White, I _V = (180 to 450) mcd	x/y typ.: 0.32/0.31	InGaN/Sapphire and TAG
VLMW41S1T2-MKNL-08	White, I _V = (180 to 450) mcd	x/y typ.: 0.33/0.33	InGaN/Sapphire and TAG
VLMW41S1T2-NKOL-08	White, I _V = (180 to 450) mcd	x/y typ.: 0.34/0.34	InGaN/Sapphire and TAG
VLMW41S1T2-OKPL-08	White, $I_V = (180 \text{ to } 450) \text{ mcd}$	x/y typ.: 0.35/0.36	InGaN/Sapphire and TAG

^{**} Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902



ABSOLUTE MAXIMUI VLMW41	M RATINGS (T _{amb} = 25 °C, unless of	therwise specifie	ed)	
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
DC forward current	T _{amb} ≤ 80 °C	I _F	20	mA
Surge forward current	t _p ≤ 10 μs	I _{FSM}	0.1	Α
Power dissipation		P _V	84	mW
Junction temperature		T _j	110	°C
Storage temperature range		T _{stg}	- 40 to + 100	°C
Operating temperature range		T _{amb}	- 40 to + 100	°C
Thermal resistance junction/ ambient	Mounted on PC board (pad size > 16 mm²)	R _{thJA}	360	K/W

OPTICAL AND ELECT VLMW41, WHITE	ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified) ITE						
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity	I _F = 10 mA	VLMW41S1T2	I _V	180		450	mcd
		VLMW41S1T2-JKPL			0.33/0.33		
		VLMW41S1T2-JKKL			0.30/0.28		450 mcd
0		VLMW41S1T2-KKLL			0.31/0.30		
Chromaticity coordinates acc. to CIE 1931	I _F = 10 mA	VLMW41S1T2-LKML	x/y		0.32/0.31		
acc. to OIL 1931		VLMW41S1T2-MKNL			0.33/0.33		
		VLMW41S1T2-NKOL			0.34/0.34		
		VLMW41S1T2-OKPL			0.35/0.36		
Angle of half intensity	I _F = 10 mA		φ		± 60		deg
Forward voltage	I _F = 20 mA		V_{F}	2.7	3.3	4.2	V
Temperature coefficient of V _F	I _F = 10 mA		TC _{VF}		- 3		mV/K
Temperature coefficient of I _V	I _F = 10 mA		TC _{IV}		- 0.4		%/K

Note:

Not designed for reverse operation

LUMINOUS	SINTENSIT	TY CLASSIF	ICATION	
GROUP	LIGH	LIGHT INTENSITY (mcd)		
STANDARD	OPTIONAL	MIN.	MAX.	
S	1	180	224	
3	2	224	280	
т	1	280	355	
'	2	355	450	

Note:

Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of ± 11 %.

The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel). In order to ensure availability, single brightness groups are not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.

In order to ensure availability, single wavelength groups are not be orderable.

CROSSING TABLE	
VISHAY	OSRAM
VLMW41	LWT67C



	X	Υ		Х	Υ
	0.2960	0.2590		0.3189	0.330
ш	0.2910	0.2680	NAI.	0.3288	0.34
JK	0.3005 0.2825 ML	ML	0.3288	0.328	
	0.3045	0.2715		0.3197	0.31
	0.2910	0.2680		0.3288	0.30
п	0.2850	0.2790	NIZ	0.3288	0.328
JL	0.2960	0.2955	NK	0.3386	0.34
	0.3005	0.2825		0.3386	0.32
	0.3045	0.2715		0.3288	0.328
KK	0.3005	0.2825	NII	0.3288	0.34
KK	0.3100	0.2970	NL	0.3386	0.359
	0.3130	0.2840		0.3386	0.34
	0.3005	0.2825		0.3386	0.32
KL	0.2960	0.2955	ОК	0.3386	0.34
NL	0.3070	0.3120	UK UK	0.3484	0.35
	0.3100	0.2970		0.3484	0.33
	0.3100	0.2970		0.3386	0.34
LK	0.3197	0.3131	OL	0.3386	0.35
LK	0.3205	0.2956	OL	0.3484	0.37
	0.3130	0.2840		0.3484	0.35
	0.3070	0.3120		0.3484	0.33
1.1	0.3189	0.3302	PK	0.3484	0.35
LL	0.3197	0.3131	PK	0.3582	0.37
	0.3100	0.2970		0.3582	0.35
	0.3197	0.3131	DI.	0.3484	0.35
MIZ	0.3288	0.3282		0.3484	0.37
MK	0.3288	0.3081	PL	0.3582	0.37
	0.3205	0.2956		0.3582	0.37

Note:

Chromaticity coordinate groups are tested at a current pulse duration of 25 ms and a tolerance of \pm 0.01.

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

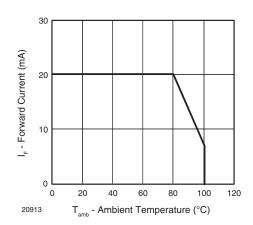


Figure 1. Forward Current vs. Ambient Temperature

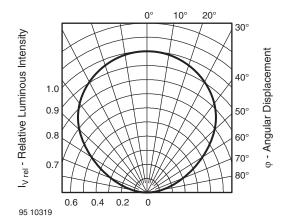


Figure 2. Rel. Luminous Intensity vs. Angular Displacement



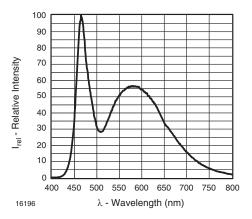


Figure 3. Relative Intensity vs. Wavelength

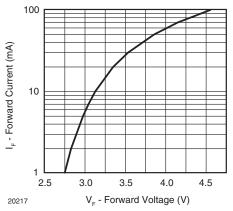


Figure 4. Forward Current vs. Forward Voltage

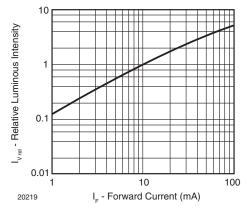


Figure 5. Relative Luminous Intensity vs. Forward Current

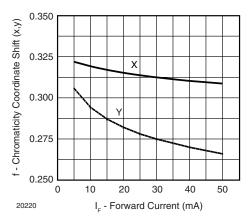


Figure 6. Chromaticity Coordinate Shift vs. Forward Current

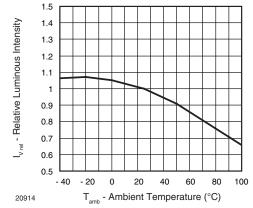


Figure 7. Rel. Luminous Intensity vs. Ambient Temperature

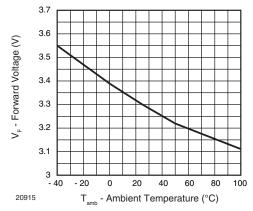
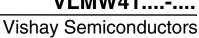


Figure 8. Forward Voltage vs. Ambient Temperature





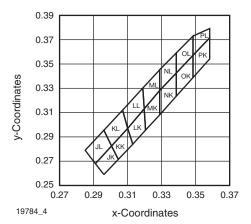
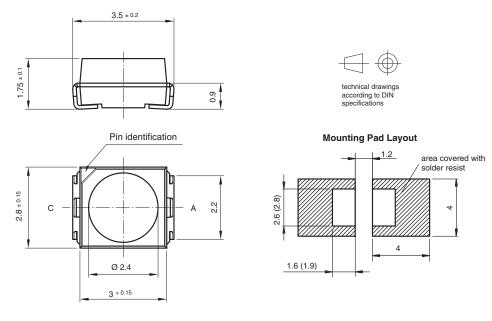


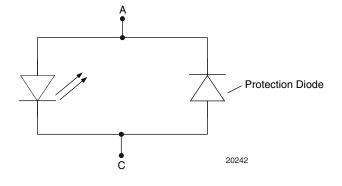
Figure 9. Coordinates of Colorgroups

PACKAGE DIMENSIONS in millimeters



Drawing-No.: 6.541-5089.01-4 Issue: 1; 10.06.10

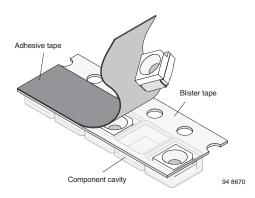
22174



METHOD OF TAPING/POLARITY AND TAPE AND REEL

SMD LED (VLM.3.../.4... - SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



TAPING OF VLM.3.../.4...

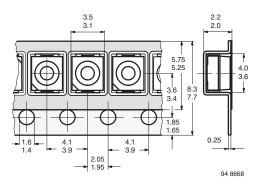


Figure 10. Tape Dimensions in mm for PLCC-2

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS.)

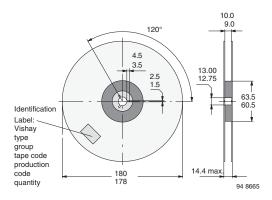


Figure 11. Reel Dimensions - GS08

VISHAY.

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS18 (= 8000 PCS.) PREFERRED

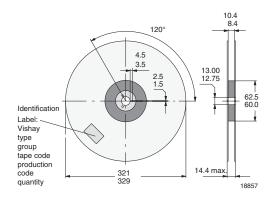


Figure 12. Reel Dimensions - GS18

SOLDERING PROFILE

IR Reflow Soldering Profile for Lead (Pb)-free Soldering Preconditioning acc. to JEDEC level 2a

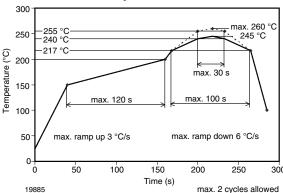


Figure 13. Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020)

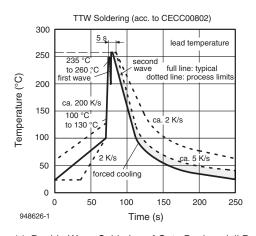
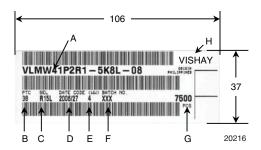


Figure 14. Double Wave Soldering of Opto Devices (all Packages)



BAR CODE PRODUCT LABEL EXAMPLE:



- A) Type of component
- B) Manufacturing plant
- C) SEL selection code (bin):

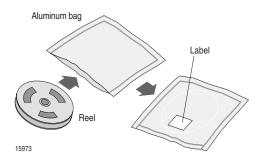
e.g.: R1 = code for luminous intensity group

5L = code for chrom. coordinate group

- D) Date code year/week
- E) Day code (e.g. 4: Thursday)
- F) Batch no.
- G) Total quantity
- H) Company code

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

Vishay Semiconductors

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

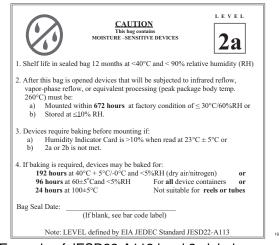
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C/- 0 °C and < 5 % RH (dry air/nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.





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