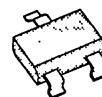


## PRODUCT SUMMARY

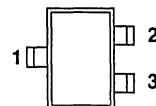
$V_{(BR)DSS}$ (V)	$r_{DS(ON)}$ ( $\Omega$ )	$I_D$ (A)	PACKAGE
60	5	0.18	SOT-23

Performance Curves: VNDS06 (See Section 7)

SOT-23



TOP VIEW


 1 DRAIN  
 2 SOURCE  
 3 GATE

PRODUCT MARKING	
VN0605T	V02

## ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	VN0605T	UNITS
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	
Continuous Drain Current	$I_D$	0.18	A
		0.11	
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	0.72	W
Power Dissipation	$P_D$	0.36	
		0.14	
Operating Junction Temperature	$T_J$	-55 to 150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55 to 150	
Lead Temperature (1/16" from case for 10 seconds)	$T_L$	300	

## THERMAL RESISTANCE

THERMAL RESISTANCE	SYMBOL	VN0605T	UNITS
Junction-to-Ambient	$R_{thJA}$	350	$^\circ\text{C/W}$

<sup>1</sup>Pulse width limited by maximum junction temperature

# VN0605T

**Siliconix**  
incorporated

ELECTRICAL CHARACTERISTICS <sup>1</sup>			LIMITS			
PARAMETER	SYMBOL	TEST CONDITIONS	TYP <sup>2</sup>	VN0605T		UNIT
				MIN	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0 \text{ V}, I_D = 10 \mu\text{A}$	70	60		V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 1 \text{ mA}$	2.3	0.8	3.0	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}$ $V_{GS} = \pm 20 \text{ V}$	$\pm 1$		$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 50 \text{ V}$ $V_{GS} = 0 \text{ V}$	0.02		1	$\mu\text{A}$
On-State Drain Current <sup>3</sup>	$I_{D(\text{ON})}$	$V_{DS} = 10 \text{ V}, V_{GS} = 10 \text{ V}$	700	500		mA
Drain-Source On-Resistance <sup>3</sup>	$r_{DS(\text{ON})}$	$V_{GS} = 4.5 \text{ V}, I_D = 50 \text{ mA}$	4.5		7.5	$\Omega$
		$V_{GS} = 10 \text{ V}$ $I_D = 0.5 \text{ A}$	3		5	
Forward Transconductance <sup>3</sup>	$g_{FS}$	$V_{DS} = 10 \text{ V}, I_D = 0.2 \text{ A}$	180	80		$\text{mS}$
Common Source Output Conductance <sup>3</sup>	$g_{os}$	$V_{DS} = 10 \text{ V}, I_D = 50 \text{ mA}$	500			$\mu\text{s}$
<b>DYNAMIC</b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 25 \text{ V}$ $V_{GS} = 0 \text{ V}$ $f = 1 \text{ MHz}$	16		60	$\text{pF}$
Output Capacitance	$C_{oss}$		11		25	
Reverse Transfer Capacitance	$C_{rss}$		2		5	
<b>SWITCHING</b>						
Turn-On Time	$t_{ON}$	$V_{DD} = 30 \text{ V}, R_L = 150 \Omega$ $I_D = 0.2 \text{ A}, V_{GEN} = 10 \text{ V}$ $R_G = 25 \Omega$ (Switching time is essentially independent of operating temperature)	7		20	$\text{ns}$
Turn-Off Time	$t_{OFF}$		11		20	

- NOTES: 1.  $T_A = 25^\circ\text{C}$  unless otherwise noted.  
 2. For design aid only, not subject to production testing.  
 3. Pulse test;  $PW = 80 \mu\text{s}$ , duty cycle  $\leq 1\%$ .