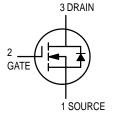
# **TMOS FET Transistor**

## N-Channel — Enhancement



# **VN10LM**



#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	60	Vdc
Gate–Source Voltage — Continuous — Non–repetitive (t <sub>p</sub> ≤ 50 μs)	V <sub>G</sub> S V <sub>G</sub> SM	±20 ±40	Vdc Vpk
Drain Current – Continuous <sup>(1)</sup> – Pulsed <sup>(2)</sup>	I <sub>D</sub>	0.3 1.0	Adc
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	1.0 8.0	Watts mW/°C
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-40 to +150	°C

#### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Drain–Source Breakdown Voltage $(V_{GS} = 0, I_D = 100 \mu A)$	V(BR)DSS	60	_	_	Vdc
Zero–Gate–Voltage Drain Current (V <sub>DS</sub> = 45 V, V <sub>GS</sub> = 0)	IDSS	_	0.1	10	μAdc
Gate–Body Leakage Current (VGS = -15 V, VDS = 0)	lgss <sup>1</sup>	_	_	100	nAdc
Gate–Body Leakage Current (VGS = 15 V, VDS = 0)	lgss <sup>2</sup>	_	_	-100	nAdc

- 1. The Power Dissipation of the package may result in a lower continuous drain current.
- 2. Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle.

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REV 1

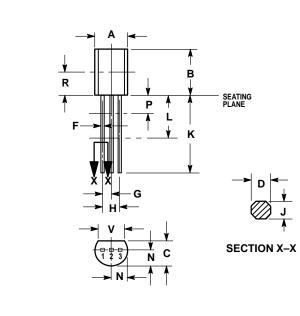


### **VN10LM**

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS					
Gate Threshold Voltage (V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 1.0 mA)	VGS(th)	0.8	_	2.5	Vdc
On-State Drain Current (V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 10 V)	I <sub>D</sub> (on)	750	_	_	mA
Forward Transconductance (V <sub>DS</sub> = 15 V, I <sub>D</sub> = 500 mA)	9fs	200	_	_	mmhos
Drain-Source On-Voltage (V <sub>GS</sub> = 5.0 V, I <sub>D</sub> = 200 mA)	V <sub>DS(on)</sub> <sup>1</sup>	_	_	1.5	Vdc
Drain-Source On-Voltage (V <sub>GS</sub> = 10 V, I <sub>D</sub> = 500 mA)	V <sub>DS(on)</sub> <sup>2</sup>	_	_	2.5	Vdc
Drain–Source On–Resistance (V <sub>GS</sub> = 5.0 V, I <sub>D</sub> = 200 mA)	rDS(on) <sup>1</sup>	_	_	7.5	Ω
Drain–Source On–Resistance (V <sub>GS</sub> = 10 V, I <sub>D</sub> = 500 mA)	rDS(on) <sup>2</sup>	_	_	5.0	Ω
Input Capacitance (V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0, f = 1.0 MHz)	C <sub>iss</sub>	_	_	60	pF
Output Capacitance $(V_{DS} = 25 \text{ V}, V_{GS} = 0, f = 1.0 \text{ MHz})$	C <sub>OSS</sub>	_	_	25	pF
Reverse Transfer Capacitance (V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0, f = 1.0 MHz)	C <sub>rss</sub>	_	_	5.0	pF
Turn–On Time $(V_{DS} = 15 \text{ V}, R_L = 23 \Omega, R_G = 50 \Omega, V_{in} = 20 \text{ V})$	t <sub>on</sub>	_	_	10	ns
Turn–Off Time $(V_{DS} = 15 \text{ V}, R_L = 23 \Omega, R_G = 50 \Omega, V_{in} = 20 \text{ V})$	<sup>t</sup> off	_	_	10	ns

#### **PACKAGE DIMENSIONS**



**CASE 029-05** (TO-226AE) ISSUE AD

- NOTES:

  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

  2. CONTROLLING DIMENSION: INCH.

  3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.

  4. DIMENSION F APPLIES BETWEEN P AND L. DIMENSIONS D AND J APPLY BETWEEN L AND K MIMIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIM	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.44	5.21
В	0.290	0.310	7.37	7.87
С	0.125	0.165	3.18	4.19
D	0.018	0.022	0.46	0.56
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.018	0.024	0.46	0.61
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.135		3.43	
V	0.135		3.43	

STYLE 22:
PIN 1. SOURCE
2. GATE
3. DRAIN

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#### How to reach us

**USA/EUROPE/Locations Not Listed**: Motorola Literature Distribution; P.O. Box 5405, Denver, Colorado 80217. 303–675–2140 or 1–800–441–2447

JAPAN: Nippon Motorola Ltd.: SPD, Strategic Planning Office, 4–32–1, Nishi–Gotanda, Shinagawa–ku, Tokyo 141, Japan. 81–3–5487–8488

**Mfax**™: RMFAX0@email.sps.mot.com – TOUCHTONE 602–244–6609 – US & Canada ONLY 1–800–774–18-

- TOUCHTONE 602-244-6609 ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, - US & Canada ONLY 1-800-774-1848 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852-26629298

INTERNET: http://motorola.com/sps



VN10LM/D