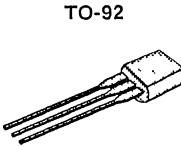


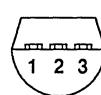
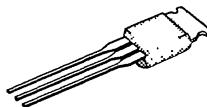
PRODUCT SUMMARY

PART NUMBER	$V_{(BR)DSS}$ (V)	$r_{DS(ON)}$ (Ω)	I_D (A)	PACKAGE
VN1206L	120	6	0.23	TO-92
VN1206M	120	6	0.26	TO-237


 1 SOURCE
 2 GATE
 3 DRAIN

Performance Curves: VNDQ12 (See Section 7)

TO-237


 1 SOURCE
 2 GATE
 3 & TAB - DRAIN

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	VN1206L	VN1206M	UNITS
Drain-Source Voltage	V_{DS}	120	120	V
Gate-Source Voltage	V_{GS}	± 30	± 30	
Continuous Drain Current	I_D	0.23	0.26	A
		0.15	0.16	
Pulsed Drain Current ¹	I_{DM}	2	2	W
Power Dissipation	P_D	0.8	1	
		0.32	0.40	
Operating Junction and Storage Temperature	T_j, T_{stg}	-55 to 150		°C
Lead Temperature (1/16" from case for 10 seconds)	T_L	300		

6

THERMAL RESISTANCE

THERMAL RESISTANCE	SYMBOL	VN1206L	VN1206M	UNITS
Junction-to-Ambient	R_{thJA}	156	125	°C/W

¹Pulse width limited by maximum junction temperature

VN1206L, VN1206M

Siliconix
incorporated

ELECTRICAL CHARACTERISTICS ¹			LIMITS			
PARAMETER	SYMBOL	TEST CONDITIONS	TYP ²	VN1206		UNIT
				MIN	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0 \text{ V}$, $I_D = 100 \mu\text{A}$	145	120		V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$, $I_D = 1 \text{ mA}$	1.4	0.8	2	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}$ $V_{GS} = \pm 15 \text{ V}$	± 1 ± 5		± 100 ± 500	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 120 \text{ V}$ $V_{GS} = 0 \text{ V}$	0.001 0.5		10 500	μA
On-State Drain Current ³	$I_{D(\text{ON})}$	$V_{DS} = 10 \text{ V}$, $V_{GS} = 10 \text{ V}$	1.6	1		A
Drain-Source On-Resistance ³	$r_{DS(\text{ON})}$	$V_{GS} = 2.5 \text{ V}$, $I_D = 0.1 \text{ A}$	6		10	Ω
		$V_{GS} = 10 \text{ V}$ $I_D = 0.5 \text{ A}$	3.4 7		6 14.8	
Forward Transconductance ³	g_{FS}	$V_{DS} = 10 \text{ V}$, $I_D = 0.5 \text{ A}$	425	300		ms
Common Source Output Conductance ³	g_{os}	$V_{DS} = 7.5 \text{ V}$, $I_D = 0.1 \text{ A}$	400			μs
DYNAMIC						
Input Capacitance	C_{iss}	$V_{DS} = 25 \text{ V}$ $V_{GS} = 0 \text{ V}$ $f = 1 \text{ MHz}$	35		125	pF
Output Capacitance	C_{oss}		15		50	
Reverse Transfer Capacitance	C_{rss}		2		20	
Turn-On Time	$t_{d(\text{ON})}$	$V_{DD} = 60 \text{ V}$, $R_L = 150 \Omega$ $I_D = 0.4 \text{ A}$, $V_{GEN} = 10 \text{ V}$ $R_G = 25 \Omega$ (Switching time is essentially independent of operating temperature)	3		8	ns
	t_r		2.5		8	
Turn-Off Time	$t_{d(\text{OFF})}$		7		18	
	t_f		2.5		12	

- NOTES: 1. $T_A = 25^\circ\text{C}$ unless otherwise noted.
 2. For design aid only, not subject to production testing.
 3. Pulse test; $PW = 300 \mu\text{s}$, duty cycle $\leq 2\%$.